EFM32PG CPU Support Package Guide

Version: 4.2

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EFM32PG Support Package

This guide describes the following features of the EFM32PG CPU support package:

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Creating EFM32PG Projects

Creating an EFM32PG C/C++ executable project

To create a new minimal C/C++ executable project:

>   Select the **File** > **New** > **New Project** menu item.
>   Select the **A C/C++ executable for Silicon Labs EFM32PG** project template.
>   Set the required project name and location directory.
>   Click **Next**.
>   If required, change any of the default project settings.
>   Click **Finish** to create the project.

Creating an EFM32PG library project

To create a new library project:

>   Select the **File** > **New** > **New Project** menu item.
>   Select the **A library for Silicon Labs EFM32PG** project template.
>   Set the required project name and location directory.
>   Click **Next**.
>   If required, change any of the default project settings.
>   Click **Finish** to create the project.

Creating an EFM32PG externally built executable project

To create a new project that will allow you to debug an existing externally built executable file:

>   Select the **File** > **New** > **New Project** menu item.
>   Select the **An externally built executable for Silicon Labs EFM32PG** project template.
>   Set the required project name and location directory.
>   Click **Next**.
>   Set the **Load File** project property to point to the executable file you want to download and debug.
>   If required, change any of the other default project settings.
>   Click **Finish** to create the project.

Creating an EFM32PG CrossWorks Tasking Library executable project

To create a new C/C++ executable project configured to use the CrossWorks Tasking Library:

>   Select the **File** > **New** > **New Project** menu item.
>   Select the **A CrossWorks Tasking Library executable for Silicon Labs EFM32PG** project template.
>   Set the required project name and location directory.
>   Click **Next**.

If required, change any of the other default project settings.

Click **Finish** to create the project.

Creating an EFM32PG assembly code only executable project

Please note, this template does not add any C/C++ startup code or libraries and is therefore not suitable for creating projects that include C/C++ code.

To create a new assembly code only executable project without:

Select the **File** > **New** > **New Project** menu item.

Select the **An assembly code only executable for Silicon Labs EFM32PG** project template.

Set the required project name and location directory.

Click **Next**.

If required, change any of the other default project settings.

Click **Finish** to create the project.

Opening EFM32PG Sample Solutions

EFM32PG Samples Solution

This solution contains general sample projects that run on EFM32PG devices. To open the EFM32PG Samples Solution:

> Select the **Tools** > **Show Installed Packages** menu item.
> Select the **Silicon Labs EFM32PG CPU Support Package** link.
> Select the **Samples Solutions** > **EFM32PG Samples Solution** link.

EFM32PG CMSIS-DSP Samples Solution

This solution contains sample projects that use the CMSIS-DSP library running on EFM32PG devices. To open the EFM32PG CMSIS-DSP Samples Solution:

> Select the **Tools** > **Show Installed Packages** menu item.
> Select the **Silicon Labs EFM32PG CPU Support Package** link.
> Select the **Sample Solutions** > **EFM32PG CMSIS-DSP Samples Solution** link.

EFM32PG Project Properties

Projects creating using the project templates in this support package have the following device specific project properties:

Heap Size

The heap size is set to be 256 bytes when a project is created. The heap size can be modified using the **Heap Size** project property.

Section Placement

You can select the memory configuration you require using the **Section Placement** project property.

For EFM32PG projects, the set of placements are:

> **Flash** - The application runs in internal Flash memory *(default)*.
> **Flash Vectors In RAM** - The application runs in internal Flash memory and exception vectors are copied to RAM memory.
> **Flash Copy To RAM** - The application starts in internal flash and copies itself to run from internal RAM memory.
> **RAM** - The application runs from internal RAM memory only.

Stack Sizes

The main stack size is set to be 256 bytes when a project is created.

The process stack size is set to be 0 bytes when a project is created.

The main and process stack sizes can be modified using the **Main Stack Size** and **Process Stack Size** project properties.

To change the location of the stacks, edit the section placement file and place the *.stack* and *.stack_process* sections as required.

Startup From Reset

By default, the application will only startup from power-on/reset in *Release* configuration. This acts as a safety net in case you accidently download a program in FLASH during development that crashes and prevents the debugger from taking control of the target over the debug interface thus rendering the device unusable.

For EFM32PG projects, the **Startup From Reset** project property can be set to one of the following:

> **No** - The application will not startup from reset.

Release Only - The application will only startup from reset when built in *Release* configuration *(default)*.

Yes - The application will always startup from reset.

Target Processor

Once a project has been created you can target different devices by modifying the **Target Processor** project property. See the **EFM32PG Devices** section for details on the files, preprocessor definitions and macro definitions used when a device is selected.

EFM32PG Project Templates

The project template system simplifies the creation of new projects with the IDE, it also system makes it easy to create new projects with a text editor or script. All that needs to be specified is the project name, the support packages that the project is dependent on, the target processor and the source files you want to add to the project. For example, create a file called *example.hzp* with the following contents:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="EFM32PG_EXE">
    <configuration Name="Common" package_dependencies="EFM32PG"
 Target="EFM32PG12B500F1024GL125" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

You can also add any other property settings that the project requires such as preprocessor definitions or include paths using the property save name, for example:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="EFM32PG_EXE">
    <configuration Name="Common" package_dependencies="EFM32PG"
 Target="EFM32PG12B500F1024GL125" c_preprocessor_definitions="MYDEF1=1;MYDEF2=TWO"
 c_user_include_directories="$(ProjectDir)/include1;$(ProjectDir)/include2" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

Available EFM32PG project templates

Template Name	Template Description
EFM32PG_ASM_EXE	EFM32PG Assembly Code Only Executable
EFM32PG_CTL_EXE	EFM32PG CTL Executable
EFM32PG_EXE	EFM32PG C/C++ Executable
EFM32PG_EXT_EXE	EFM32PG Externally Built Executable
EFM32PG_LIB	EFM32PG Library

EFM32PG Devices

This package supports the following EFM32PG devices:

EFM32PG12B Series Family

EFM32PG1B Series Family

EFM32PG22 Series Family

EFM32PG23 Series Family

EFM32PG12B Series Family

EFM32PG12B500F1024GL125

EFM32PG12B500F1024GM48

EFM32PG12B500F1024IL125

EFM32PG12B500F1024IM48

EFM32PG12B500F1024GL125

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Source/system_efm32pg12b.c
Family	EFM32PG
Sub Family	EFM32PG12B Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS1_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG12B500F1024GL125_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG12B500F1024GL125_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG12B500F1024GL125_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFM32PG12B500F1024GL125
__EFM32PG12B_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG12B500F1024GL125_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Source/system_efm32pg12b.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG12B500F1024GL125_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG12B Series

EFM32PG12B500F1024GM48

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Source/system_efm32pg12b.c
Family	EFM32PG
Sub Family	EFM32PG12B Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS1_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG12B500F1024GM48_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG12B500F1024GM48_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG12B500F1024GM48_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFM32PG12B500F1024GM48
__EFM32PG12B_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG12B500F1024GM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Source/system_efm32pg12b.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG12B500F1024GM48_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG12B Series

EFM32PG12B500F1024IL125

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Source/system_efm32pg12b.c
Family	EFM32PG
Sub Family	EFM32PG12B Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS1_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG12B500F1024IL125_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG12B500F1024IL125_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG12B500F1024IL125_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFM32PG12B500F1024IL125
__EFM32PG12B_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG12B500F1024IL125_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Source/system_efm32pg12b.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG12B500F1024IL125_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG12B Series

EFM32PG12B500F1024IM48

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Source/system_efm32pg12b.c
Family	EFM32PG
Sub Family	EFM32PG12B Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS1_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG12B500F1024IM48_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG12B500F1024IM48_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG12B500F1024IM48_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFM32PG12B500F1024IM48
__EFM32PG12B_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG12B500F1024IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG12B/Source/system_efm32pg12b.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG12B500F1024IM48_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG12B Series

EFM32PG1B Series Family

EFM32PG1B100F128GM32

EFM32PG1B100F128IM32

EFM32PG1B100F256GM32

EFM32PG1B100F256IM32

EFM32PG1B200F128GM32

EFM32PG1B200F128GM48

EFM32PG1B200F128IM32

EFM32PG1B200F256GM32

EFM32PG1B200F256GM48

EFM32PG1B200F256IM32

EFM32PG1B200F256IM48

EFM32PG1B100F128GM32

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
Family	EFM32PG
Sub Family	EFM32PG1B Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B100F128GM32_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B100F128GM32_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG1B100F128GM32_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFM32PG1B100F128GM32
__EFM32PG1B_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG1B100F128GM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG1B100F128GM32_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG1B Series

EFM32PG1B100F128IM32

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
Family	EFM32PG
Sub Family	EFM32PG1B Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B100F128IM32_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B100F128IM32_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG1B100F128IM32_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFM32PG1B100F128IM32
__EFM32PG1B_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG1B100F128IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG1B100F128IM32_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG1B Series

EFM32PG1B100F256GM32

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
Family	EFM32PG
Sub Family	EFM32PG1B Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B100F256GM32_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B100F256GM32_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG1B100F256GM32_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFM32PG1B100F256GM32
__EFM32PG1B_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG1B100F256GM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG1B100F256GM32_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG1B Series

EFM32PG1B100F256IM32

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
Family	EFM32PG
Sub Family	EFM32PG1B Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B100F256IM32_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B100F256IM32_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG1B100F256IM32_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFM32PG1B100F256IM32
__EFM32PG1B_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG1B100F256IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG1B100F256IM32_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG1B Series

EFM32PG1B200F128GM32

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
Family	EFM32PG
Sub Family	EFM32PG1B Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F128GM32_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F128GM32_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG1B200F128GM32_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFM32PG1B200F128GM32
__EFM32PG1B_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F128GM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG1B200F128GM32_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG1B Series

EFM32PG1B200F128GM48

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
Family	EFM32PG
Sub Family	EFM32PG1B Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F128GM48_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F128GM48_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG1B200F128GM48_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFM32PG1B200F128GM48
__EFM32PG1B_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F128GM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG1B200F128GM48_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG1B Series

EFM32PG1B200F128IM32

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
Family	EFM32PG
Sub Family	EFM32PG1B Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F128IM32_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F128IM32_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG1B200F128IM32_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFM32PG1B200F128IM32
__EFM32PG1B_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F128IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG1B200F128IM32_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG1B Series

EFM32PG1B200F256GM32

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/ EFM32PG1B/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/ EFM32PG1B/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/ EFM32PG1B/Source/system_efm32pg1b.c
Family	EFM32PG
Sub Family	EFM32PG1B Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/ EFM32PG1B200F256GM32_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/ EFM32PG1B200F256GM32_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/ EFM32PG1B200F256GM32_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFM32PG1B200F256GM32
__EFM32PG1B_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F256GM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/ system_efm32pg1b.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG1B200F256GM32_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG1B Series

EFM32PG1B200F256GM48

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
Family	EFM32PG
Sub Family	EFM32PG1B Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F256GM48_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F256GM48_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG1B200F256GM48_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFM32PG1B200F256GM48
__EFM32PG1B_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F256GM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG1B200F256GM48_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG1B Series

EFM32PG1B200F256IM32

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
Family	EFM32PG
Sub Family	EFM32PG1B Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F256IM32_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F256IM32_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG1B200F256IM32_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFM32PG1B200F256IM32
__EFM32PG1B_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F256IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG1B200F256IM32_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG1B Series

EFM32PG1B200F256IM48

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
Family	EFM32PG
Sub Family	EFM32PG1B Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F256IM48_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F256IM48_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG1B200F256IM48_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFM32PG1B200F256IM48
__EFM32PG1B_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG1B200F256IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG1B/Source/system_efm32pg1b.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG1B200F256IM48_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG1B Series

EFM32PG22 Series Family

EFM32PG22C200F128IM32

EFM32PG22C200F128IM40

EFM32PG22C200F256IM32

EFM32PG22C200F256IM40

EFM32PG22C200F32IM32

EFM32PG22C200F32IM40

EFM32PG22C200F512IM32

EFM32PG22C200F512IM40

EFM32PG22C200F64IM32

EFM32PG22C200F64IM40

EFM32PG22C200F128IM32

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
Family	EFM32PG
Sub Family	EFM32PG22 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F128IM32_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F128IM32_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F128IM32_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG22C200F128IM32
__EFM32PG22_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F128IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F128IM32_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG22 Series

EFM32PG22C200F128IM40

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
Family	EFM32PG
Sub Family	EFM32PG22 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F128IM40_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F128IM40_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F128IM40_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG22C200F128IM40
__EFM32PG22_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F128IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F128IM40_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG22 Series

EFM32PG22C200F256IM32

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
Family	EFM32PG
Sub Family	EFM32PG22 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F256IM32_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F256IM32_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F256IM32_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG22C200F256IM32
__EFM32PG22_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F256IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F256IM32_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG22 Series

EFM32PG22C200F256IM40

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
Family	EFM32PG
Sub Family	EFM32PG22 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F256IM40_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F256IM40_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F256IM40_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG22C200F256IM40
__EFM32PG22_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F256IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F256IM40_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG22 Series

EFM32PG22C200F32IM32

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
Family	EFM32PG
Sub Family	EFM32PG22 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F32IM32_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F32IM32_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F32IM32_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG22C200F32IM32
__EFM32PG22_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F32IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F32IM32_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG22 Series

EFM32PG22C200F32IM40

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
Family	EFM32PG
Sub Family	EFM32PG22 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F32IM40_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F32IM40_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F32IM40_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG22C200F32IM40
__EFM32PG22_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F32IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F32IM40_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG22 Series

EFM32PG22C200F512IM32

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
Family	EFM32PG
Sub Family	EFM32PG22 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F512IM32_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F512IM32_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F512IM32_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG22C200F512IM32
__EFM32PG22_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F512IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F512IM32_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG22 Series

EFM32PG22C200F512IM40

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
Family	EFM32PG
Sub Family	EFM32PG22 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F512IM40_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F512IM40_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F512IM40_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG22C200F512IM40
__EFM32PG22_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F512IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F512IM40_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG22 Series

EFM32PG22C200F64IM32

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
Family	EFM32PG
Sub Family	EFM32PG22 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F64IM32_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F64IM32_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F64IM32_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG22C200F64IM32
__EFM32PG22_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F64IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F64IM32_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG22 Series

EFM32PG22C200F64IM40

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
Family	EFM32PG
Sub Family	EFM32PG22 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F64IM40_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F64IM40_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F64IM40_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG22C200F64IM40
__EFM32PG22_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG22C200F64IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG22/Source/system_efm32pg22.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG22C200F64IM40_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG22 Series

EFM32PG23 Series Family

EFM32PG23B200F128IM40

EFM32PG23B200F256IM40

EFM32PG23B200F512IM40

EFM32PG23B200F64IM40

EFM32PG23B210F128IM48

EFM32PG23B210F256IM48

EFM32PG23B210F512IM48

EFM32PG23B210F64IM48

EFM32PG23B310F128IM48

EFM32PG23B310F256IM48

EFM32PG23B310F512IM48

EFM32PG23B310F64IM48

EFM32PG23B200F128IM40

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
Family	EFM32PG
Sub Family	EFM32PG23 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B200F128IM40_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B200F128IM40_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG23B200F128IM40_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG23B200F128IM40
__EFM32PG23_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x08000000 - 0x0801FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG23B200F128IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG23B200F128IM40_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG23 Series

EFM32PG23B200F256IM40

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
Family	EFM32PG
Sub Family	EFM32PG23 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B200F256IM40_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B200F256IM40_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG23B200F256IM40_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG23B200F256IM40
__EFM32PG23_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG23B200F256IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG23B200F256IM40_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG23 Series

EFM32PG23B200F512IM40

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
Family	EFM32PG
Sub Family	EFM32PG23 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B200F512IM40_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B200F512IM40_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG23B200F512IM40_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG23B200F512IM40
__EFM32PG23_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG23B200F512IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG23B200F512IM40_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG23 Series

EFM32PG23B200F64IM40

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
Family	EFM32PG
Sub Family	EFM32PG23 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B200F64IM40_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B200F64IM40_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG23B200F64IM40_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG23B200F64IM40
__EFM32PG23_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x08000000 - 0x0800FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG23B200F64IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG23B200F64IM40_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG23 Series

EFM32PG23B210F128IM48

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
Family	EFM32PG
Sub Family	EFM32PG23 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B210F128IM48_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B210F128IM48_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG23B210F128IM48_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG23B210F128IM48
__EFM32PG23_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x08000000 - 0x0801FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG23B210F128IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG23B210F128IM48_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG23 Series

EFM32PG23B210F256IM48

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
Family	EFM32PG
Sub Family	EFM32PG23 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B210F256IM48_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B210F256IM48_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG23B210F256IM48_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG23B210F256IM48
__EFM32PG23_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG23B210F256IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG23B210F256IM48_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG23 Series

EFM32PG23B210F512IM48

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
Family	EFM32PG
Sub Family	EFM32PG23 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B210F512IM48_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B210F512IM48_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG23B210F512IM48_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG23B210F512IM48
__EFM32PG23_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG23B210F512IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG23B210F512IM48_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG23 Series

EFM32PG23B210F64IM48

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/ EFM32PG23/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/ EFM32PG23/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/ EFM32PG23/Source/system_efm32pg23.c
Family	EFM32PG
Sub Family	EFM32PG23 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/ GECKOS2C3_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/ EFM32PG23B210F64IM48_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/ EFM32PG23B210F64IM48_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/ EFM32PG23B210F64IM48_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG23B210F64IM48
__EFM32PG23_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x08000000 - 0x0800FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG23B210F64IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/ system_efm32pg23.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG23B210F64IM48_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG23 Series

EFM32PG23B310F128IM48

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
Family	EFM32PG
Sub Family	EFM32PG23 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B310F128IM48_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B310F128IM48_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG23B310F128IM48_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG23B310F128IM48
__EFM32PG23_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x08000000 - 0x0801FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG23B310F128IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG23B310F128IM48_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG23 Series

EFM32PG23B310F256IM48

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
Family	EFM32PG
Sub Family	EFM32PG23 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B310F256IM48_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B310F256IM48_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG23B310F256IM48_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG23B310F256IM48
__EFM32PG23_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG23B310F256IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG23B310F256IM48_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG23 Series

EFM32PG23B310F512IM48

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
Family	EFM32PG
Sub Family	EFM32PG23 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B310F512IM48_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B310F512IM48_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG23B310F512IM48_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG23B310F512IM48
__EFM32PG23_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFM32PG/XML/EFM32PG23B310F512IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
DeviceVectorsFile=$(TargetsDir)/EFM32PG/Source/EFM32PG23B310F512IM48_Vectors.s
DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG23 Series

EFM32PG23B310F64IM48

Device Details	
CMSIS Header File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
CMSIS Include Path	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
CMSIS System File	$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Source/system_efm32pg23.c
Family	EFM32PG
Sub Family	EFM32PG23 Series
Loader File	$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
Memory Map File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B310F64IM48_MemoryMap.xml
Register Definition File	$(TargetsDir)/EFM32PG/XML/EFM32PG23B310F64IM48_Registers.xml
Vectors File	$(TargetsDir)/EFM32PG/Source/EFM32PG23B310F64IM48_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFM32PG23B310F64IM48
__EFM32PG23_Series_SUBFAMILY
__EFM32PG_FAMILY

Memory Segments	
FLASH	0x08000000 - 0x0800FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include
DeviceHeaderFile=$(TargetsDir)/EFM32PG/CMSIS/Device/SiliconLabs/EFM32PG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFM32PG/Loader/GECKOS2C3_Loader.elf
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DeviceFamily=EFM32PG
DeviceSubFamily=EFM32PG23 Series