



EFR32 CPU Support Package Guide

Version: 4.5



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MGM13S12F512GA	440
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MGM210L022JIF	443
MGM210L022JNF	444
MGM210LA22JIF	445
MGM210LA22JNF	446
MGM210P022JIA	447
MGM210P032JIA	448
MGM210PA22JIA	449
MGM210PA32JIA	450

MGM210PB22JIA	451
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MGM220PC22HNA	454
MGM24 Series Family	455
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MGM240PA22VNA	458
MGM240PA32VNA	459
MGM240PA32VNN	460
MGM240PB22VNA	461
MGM240PB32VNA	462
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WGM160P022KGN2	469
WGM160P022KGN3	470
WGM160PX22KGA2	471
WGM160PX22KGA3	472
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ZGM230SB27HGN	481
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EFR32 Support Package

This guide describes the following features of the EFR32 CPU support package:

- [How to create EFR32 projects](#)
- [How to open EFR32 sample projects](#)
- [EFR32 specific project properties](#)
- [EFR32 specific project templates](#)
- [Supported EFR32 devices](#)

Creating EFR32 Projects

Creating an EFR32 C/C++ executable project

To create a new minimal C/C++ executable project:

- Select the **File > New > New Project** menu item.
- Select the **A C/C++ executable for Silicon Labs EFR32** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

Creating an EFR32 library project

To create a new library project:

- Select the **File > New > New Project** menu item.
- Select the **A library for Silicon Labs EFR32** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

Creating an EFR32 externally built executable project

To create a new project that will allow you to debug an existing externally built executable file:

- Select the **File > New > New Project** menu item.
- Select the **An externally built executable for Silicon Labs EFR32** project template.
- Set the required project name and location directory.
- Click **Next**.
- Set the **Load File** project property to point to the executable file you want to download and debug.
- If required, change any of the other default project settings.
- Click **Finish** to create the project.

Creating an EFR32 CrossWorks Tasking Library executable project

To create a new C/C++ executable project configured to use the CrossWorks Tasking Library:

- Select the **File > New > New Project** menu item.
- Select the **A CrossWorks Tasking Library executable for Silicon Labs EFR32** project template.
- Set the required project name and location directory.
- Click **Next**.

If required, change any of the other default project settings.
Click **Finish** to create the project.

Creating an EFR32 assembly code only executable project

Please note, this template does not add any C/C++ startup code or libraries and is therefore not suitable for creating projects that include C/C++ code.

To create a new assembly code only executable project without:

Select the **File > New > New Project** menu item.
Select the **An assembly code only executable for Silicon Labs EFR32** project template.
Set the required project name and location directory.
Click **Next**.
If required, change any of the other default project settings.
Click **Finish** to create the project.

Opening EFR32 Sample Solutions

EFR32 Samples Solution

This solution contains general sample projects that run on EFR32 devices. To open the EFR32 Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **Silicon Labs EFR32 CPU Support Package** link.
- Select the **Samples Solutions > EFR32 Samples Solution** link.

EFR32 CMSIS-DSP Samples Solution

This solution contains sample projects that use the CMSIS-DSP library running on EFR32 devices. To open the EFR32 CMSIS-DSP Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **Silicon Labs EFR32 CPU Support Package** link.
- Select the **Sample Solutions > EFR32 CMSIS-DSP Samples Solution** link.

EFR32 Project Properties

Projects created using the project templates in this support package have the following device specific project properties:

Heap Size

The heap size is set to be 256 bytes when a project is created. The heap size can be modified using the **Heap Size** project property.

Section Placement

You can select the memory configuration you require using the **Section Placement** project property.

For EFR32 projects, the set of placements are:

Flash - The application runs in internal Flash memory (*default*).

Flash Vectors In RAM - The application runs in internal Flash memory and exception vectors are copied to RAM memory.

Flash Copy To RAM - The application starts in internal flash and copies itself to run from internal RAM memory.

RAM - The application runs from internal RAM memory only.

Stack Sizes

The main stack size is set to be 256 bytes when a project is created.

The process stack size is set to be 0 bytes when a project is created.

The main and process stack sizes can be modified using the **Main Stack Size** and **Process Stack Size** project properties.

To change the location of the stacks, edit the section placement file and place the `.stack` and `.stack_process` sections as required.

Startup From Reset

By default, the application will only startup from power-on/reset in *Release* configuration. This acts as a safety net in case you accidentally download a program in FLASH during development that crashes and prevents the debugger from taking control of the target over the debug interface thus rendering the device unusable.

For EFR32 projects, the **Startup From Reset** project property can be set to one of the following:

No - The application will not startup from reset.

Release Only - The application will only startup from reset when built in *Release* configuration (*default*).

Yes - The application will always startup from reset.

Target Processor

Once a project has been created you can target different devices by modifying the **Target Processor** project property. See the [EFR32 Devices](#) section for details on the files, preprocessor definitions and macro definitions used when a device is selected.

EFR32 Project Templates

The project template system simplifies the creation of new projects with the IDE, it also system makes it easy to create new projects with a text editor or script. All that needs to be specified is the project name, the support packages that the project is dependent on, the target processor and the source files you want to add to the project. For example, create a file called *example.hzp* with the following contents:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="EFR32_EXE">
    <configuration Name="Common" package_dependencies="EFR32" Target="WGM160P022KGA2" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

You can also add any other property settings that the project requires such as preprocessor definitions or include paths using the property save name, for example:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="EFR32_EXE">
    <configuration Name="Common" package_dependencies="EFR32" Target="WGM160P022KGA2"
      c_preprocessor_definitions="MYDEF1=1;MYDEF2=TWO" c_user_include_directories="$(ProjectDir)/
include1;$(ProjectDir)/include2" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

Available EFR32 project templates

Template Name	Template Description
EFR32_ASM_EXE	EFR32 Assembly Code Only Executable
EFR32_CTL_EXE	EFR32 CTL Executable
EFR32_EXE	EFR32 C/C++ Executable
EFR32_EXT_EXE	EFR32 Externally Built Executable
EFR32_LIB	EFR32 Library

EFR32 Devices

This package supports the following EFR32 devices:

[BGM1 Series Family](#)

[BGM13 Series Family](#)

[BGM21 Series Family](#)

[BGM22 Series Family](#)

[BGM24 Series Family](#)

[EFR32BG12P Series Family](#)

[EFR32BG13P Series Family](#)

[EFR32BG1B Series Family](#)

[EFR32BG1P Series Family](#)

[EFR32BG1V Series Family](#)

[EFR32BG21 Series Family](#)

[EFR32BG22 Series Family](#)

[EFR32BG24 Series Family](#)

[EFR32FG12P Series Family](#)

[EFR32FG13P Series Family](#)

[EFR32FG14P Series Family](#)

[EFR32FG14V Series Family](#)

[EFR32FG1P Series Family](#)

[EFR32FG1V Series Family](#)

[EFR32FG22 Series Family](#)

[EFR32FG23 Series Family](#)

[EFR32MG12P Series Family](#)

[EFR32MG13P Series Family](#)

[EFR32MG14P Series Family](#)

[EFR32MG1B Series Family](#)

[EFR32MG1P Series Family](#)

[EFR32MG1V Series Family](#)

[EFR32MG21 Series Family](#)

[EFR32MG22 Series Family](#)

[EFR32MG24 Series Family](#)

[EFR32ZG13L Series Family](#)

[EFR32ZG13P Series Family](#)

[EFR32ZG13S Series Family](#)

[EFR32ZG14P Series Family](#)

[EFR32ZG23 Series Family](#)

[MGM1 Series Family](#)

[MGM12 Series Family](#)

[MGM13 Series Family](#)

[MGM21 Series Family](#)

[MGM22 Series Family](#)

[MGM24 Series Family](#)

[WGM160 Series Family](#)

[ZGM13 Series Family](#)

[ZGM23 Series Family](#)

BGM1 Series Family

[BGM111A256V2](#)

[BGM111E256V2](#)

[BGM113A256V2](#)

[BGM11S12F256GA](#)

[BGM11S22F256GA](#)

[BGM121A256V2](#)

[BGM121N256V2](#)

[BGM123A256V2](#)

[BGM123N256V2](#)

BGM111A256V2

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c
Family	EFR32
Sub Family	BGM1 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM111A256V2_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM111A256V2_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM111A256V2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

BGM111A256V2

__BGM1_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM111A256V2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM111A256V2_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM1 Series

BGM111E256V2

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c
Family	EFR32
Sub Family	BGM1 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM111E256V2_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM111E256V2_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM111E256V2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

BGM111E256V2

__BGM1_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM111E256V2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM111E256V2_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM1 Series

BGM113A256V2

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c
Family	EFR32
Sub Family	BGM1 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM113A256V2_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM113A256V2_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM113A256V2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

BGM113A256V2

__BGM1_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM113A256V2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM113A256V2_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM1 Series

BGM11S12F256GA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c
Family	EFR32
Sub Family	BGM1 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM11S12F256GA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM11S12F256GA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM11S12F256GA_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

BGM11S12F256GA

__BGM1_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM11S12F256GA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM11S12F256GA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM1 Series

BGM11S22F256GA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c
Family	EFR32
Sub Family	BGM1 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM11S22F256GA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM11S22F256GA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM11S22F256GA_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

BGM11S22F256GA

__BGM1_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM11S22F256GA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM11S22F256GA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM1 Series

BGM121A256V2

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c
Family	EFR32
Sub Family	BGM1 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM121A256V2_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM121A256V2_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM121A256V2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

BGM121A256V2

__BGM1_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM121A256V2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM121A256V2_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM1 Series

BGM121N256V2

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c
Family	EFR32
Sub Family	BGM1 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM121N256V2_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM121N256V2_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM121N256V2_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
BGM121N256V2	
__BGM1_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/BGM121N256V2_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/BGM121N256V2_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	BGM1 Series

BGM123A256V2

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c
Family	EFR32
Sub Family	BGM1 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM123A256V2_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM123A256V2_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM123A256V2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

BGM123A256V2

__BGM1_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM123A256V2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM123A256V2_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM1 Series

BGM123N256V2

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c
Family	EFR32
Sub Family	BGM1 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM123N256V2_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM123N256V2_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM123N256V2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

BGM123N256V2

__BGM1_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM123N256V2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM1/Source/system_bgm1.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM123N256V2_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM1 Series

BGM13 Series Family

[BGM13P22F512GA](#)

[BGM13P22F512GE](#)

[BGM13P32F512GA](#)

[BGM13P32F512GE](#)

[BGM13S22F512GA](#)

[BGM13S22F512GN](#)

[BGM13S32F512GA](#)

[BGM13S32F512GN](#)

BGM13P22F512GA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Source/system_bgm13.c
Family	EFR32
Sub Family	BGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM13P22F512GA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM13P22F512GA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM13P22F512GA_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

BGM13P22F512GA

__BGM13_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM13P22F512GA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Source/system_bgm13.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM13P22F512GA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM13 Series

BGM13P22F512GE

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Source/system_bgm13.c
Family	EFR32
Sub Family	BGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM13P22F512GE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM13P22F512GE_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM13P22F512GE_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

BGM13P22F512GE

__BGM13_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM13P22F512GE_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Source/system_bgm13.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM13P22F512GE_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM13 Series

BGM13P32F512GA

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Source/system_bgm13.c
Family	EFR32
Sub Family	BGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM13P32F512GA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM13P32F512GA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM13P32F512GA_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
BGM13P32F512GA
__BGM13_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM13P32F512GA_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Source/system_bgm13.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM13P32F512GA_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=BGM13 Series

BGM13P32F512GE

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Source/system_bgm13.c
Family	EFR32
Sub Family	BGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM13P32F512GE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM13P32F512GE_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM13P32F512GE_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

BGM13P32F512GE

__BGM13_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM13P32F512GE_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Source/system_bgm13.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM13P32F512GE_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM13 Series

BGM13S22F512GA

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Source/system_bgm13.c
Family	EFR32
Sub Family	BGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM13S22F512GA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM13S22F512GA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM13S22F512GA_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
BGM13S22F512GA	
__BGM13_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/BGM13S22F512GA_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Source/system_bgm13.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/BGM13S22F512GA_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	BGM13 Series

BGM13S22F512GN

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Source/system_bgm13.c
Family	EFR32
Sub Family	BGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM13S22F512GN_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM13S22F512GN_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM13S22F512GN_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

BGM13S22F512GN

__BGM13_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM13S22F512GN_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Source/system_bgm13.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM13S22F512GN_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM13 Series

BGM13S32F512GA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Source/system_bgm13.c
Family	EFR32
Sub Family	BGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM13S32F512GA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM13S32F512GA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM13S32F512GA_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

BGM13S32F512GA

__BGM13_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM13S32F512GA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Source/system_bgm13.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM13S32F512GA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM13 Series

BGM13S32F512GN

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Source/system_bgm13.c
Family	EFR32
Sub Family	BGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM13S32F512GN_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM13S32F512GN_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM13S32F512GN_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

BGM13S32F512GN

__BGM13_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM13S32F512GN_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM13/Source/system_bgm13.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM13S32F512GN_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM13 Series

BGM21 Series Family

[BGM210LA22JIF](#)

[BGM210LA22JNF](#)

[BGM210P022JIA](#)

[BGM210P032JIA](#)

[BGM210PA22JIA](#)

[BGM210PA32JIA](#)

[BGM210PB22JIA](#)

[BGM210PB32JIA](#)

BGM210LA22JIF

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Source/system_bgm21.c
Family	EFR32
Sub Family	BGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM210LA22JIF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM210LA22JIF_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM210LA22JIF_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

BGM210LA22JIF

__BGM21_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM210LA22JIF_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Source/system_bgm21.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM210LA22JIF_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM21 Series

BGM210LA22JNF

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Source/system_bgm21.c
Family	EFR32
Sub Family	BGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM210LA22JNF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM210LA22JNF_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM210LA22JNF_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

BGM210LA22JNF

__BGM21_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM210LA22JNF_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Source/system_bgm21.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM210LA22JNF_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM21 Series

BGM210P022JIA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Source/system_bgm21.c
Family	EFR32
Sub Family	BGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM210P022JIA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM210P022JIA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM210P022JIA_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
BGM210P022JIA
__BGM21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/BGM210P022JIA_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Source/system_bgm21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/BGM210P022JIA_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=BGM21 Series
```

BGM210P032JIA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Source/system_bgm21.c
Family	EFR32
Sub Family	BGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM210P032JIA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM210P032JIA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM210P032JIA_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
BGM210P032JIA
__BGM21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/BGM210P032JIA_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Source/system_bgm21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/BGM210P032JIA_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=BGM21 Series
```

BGM210PA22JIA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Source/system_bgm21.c
Family	EFR32
Sub Family	BGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM210PA22JIA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM210PA22JIA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM210PA22JIA_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

BGM210PA22JIA

__BGM21_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM210PA22JIA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Source/system_bgm21.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM210PA22JIA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM21 Series

BGM210PA32JIA

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Source/system_bgm21.c
Family	EFR32
Sub Family	BGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM210PA32JIA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM210PA32JIA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM210PA32JIA_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
BGM210PA32JIA
__BGM21_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM210PA32JIA_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Source/system_bgm21.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM210PA32JIA_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=BGM21 Series

BGM210PB22JIA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Source/system_bgm21.c
Family	EFR32
Sub Family	BGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM210PB22JIA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM210PB22JIA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM210PB22JIA_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

BGM210PB22JIA

__BGM21_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM210PB22JIA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Source/system_bgm21.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM210PB22JIA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM21 Series

BGM210PB32JIA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Source/system_bgm21.c
Family	EFR32
Sub Family	BGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM210PB32JIA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM210PB32JIA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM210PB32JIA_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

BGM210PB32JIA

__BGM21_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM210PB32JIA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM21/Source/system_bgm21.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM210PB32JIA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM21 Series

BGM22 Series Family

[BGM220PC22HNA](#)

[BGM220PC22WGA](#)

[BGM220SC12WGA](#)

[BGM220SC22HNA](#)

[BGM220SC22WGA](#)

BGM220PC22HNA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Source/system_bgm22.c
Family	EFR32
Sub Family	BGM22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM220PC22HNA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM220PC22HNA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM220PC22HNA_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

BGM220PC22HNA

__BGM22_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM220PC22HNA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Source/system_bgm22.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM220PC22HNA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM22 Series

BGM220PC22WGA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Source/system_bgm22.c
Family	EFR32
Sub Family	BGM22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM220PC22WGA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM220PC22WGA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM220PC22WGA_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

BGM220PC22WGA

__BGM22_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x00057FFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM220PC22WGA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Source/system_bgm22.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM220PC22WGA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM22 Series

BGM220SC12WGA

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Source/system_bgm22.c
Family	EFR32
Sub Family	BGM22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM220SC12WGA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM220SC12WGA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM220SC12WGA_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
BGM220SC12WGA
__BGM22_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x00057FFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM220SC12WGA_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Source/system_bgm22.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM220SC12WGA_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=BGM22 Series

BGM220SC22HNA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Source/system_bgm22.c
Family	EFR32
Sub Family	BGM22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM220SC22HNA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM220SC22HNA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM220SC22HNA_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

BGM220SC22HNA

__BGM22_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM220SC22HNA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Source/system_bgm22.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM220SC22HNA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM22 Series

BGM220SC22WGA

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Source/system_bgm22.c
Family	EFR32
Sub Family	BGM22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM220SC22WGA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM220SC22WGA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM220SC22WGA_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
BGM220SC22WGA
__BGM22_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x00057FFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM220SC22WGA_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM22/Source/system_bgm22.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM220SC22WGA_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=BGM22 Series

BGM24 Series Family

[BGM240PA22VNA](#)

[BGM240PA32VNA](#)

[BGM240PA32VNN](#)

[BGM240PB22VNA](#)

[BGM240PB32VNA](#)

[BGM240PB32VNN](#)

BGM240PA22VNA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Source/system_bgm24.c
Family	EFR32
Sub Family	BGM24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM240PA22VNA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM240PA22VNA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM240PA22VNA_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

BGM240PA22VNA

__BGM24_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM240PA22VNA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Source/system_bgm24.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM240PA22VNA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM24 Series

BGM240PA32VNA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Source/system_bgm24.c
Family	EFR32
Sub Family	BGM24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM240PA32VNA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM240PA32VNA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM240PA32VNA_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

BGM240PA32VNA

__BGM24_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM240PA32VNA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Source/system_bgm24.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM240PA32VNA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM24 Series

BGM240PA32VNN

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Source/system_bgm24.c
Family	EFR32
Sub Family	BGM24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM240PA32VNN_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM240PA32VNN_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM240PA32VNN_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

BGM240PA32VNN

__BGM24_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM240PA32VNN_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Source/system_bgm24.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM240PA32VNN_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM24 Series

BGM240PB22VNA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Source/system_bgm24.c
Family	EFR32
Sub Family	BGM24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM240PB22VNA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM240PB22VNA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM240PB22VNA_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

BGM240PB22VNA

__BGM24_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM240PB22VNA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Source/system_bgm24.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM240PB22VNA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=BGM24 Series

BGM240PB32VNA

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Source/system_bgm24.c
Family	EFR32
Sub Family	BGM24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM240PB32VNA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM240PB32VNA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM240PB32VNA_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
BGM240PB32VNA
__BGM24_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM240PB32VNA_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Source/system_bgm24.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM240PB32VNA_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=BGM24 Series

BGM240PB32VNN

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Source/system_bgm24.c
Family	EFR32
Sub Family	BGM24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/BGM240PB32VNN_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/BGM240PB32VNN_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/BGM240PB32VNN_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
BGM240PB32VNN
__BGM24_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/BGM240PB32VNN_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/BGM24/Source/system_bgm24.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/BGM240PB32VNN_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=BGM24 Series

EFR32BG12P Series Family

[EFR32BG12P132F1024GL125](#)
[EFR32BG12P132F1024GM48](#)
[EFR32BG12P232F1024GL125](#)
[EFR32BG12P232F1024GM48](#)
[EFR32BG12P232F1024GM68](#)
[EFR32BG12P232F1024IM68](#)
[EFR32BG12P232F512GM68](#)
[EFR32BG12P232F512IM68](#)
[EFR32BG12P332F1024GL125](#)
[EFR32BG12P332F1024GM48](#)
[EFR32BG12P332F1024IM48](#)
[EFR32BG12P432F1024GL125](#)
[EFR32BG12P432F1024GM48](#)
[EFR32BG12P433F1024GL125](#)
[EFR32BG12P433F1024GM48](#)
[EFR32BG12P433F1024GM68](#)
[EFR32BG12P433F1024IM68](#)

EFR32BG12P132F1024GL125

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
Family	EFR32
Sub Family	EFR32BG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG12P132F1024GL125_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG12P132F1024GL125_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG12P132F1024GL125_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG12P132F1024GL125

__EFR32BG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG12P132F1024GL125_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG12P132F1024GL125_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG12P Series

EFR32BG12P132F1024GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
Family	EFR32
Sub Family	EFR32BG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG12P132F1024GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG12P132F1024GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG12P132F1024GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG12P132F1024GM48

__EFR32BG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG12P132F1024GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG12P132F1024GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG12P Series

EFR32BG12P232F1024GL125

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
Family	EFR32
Sub Family	EFR32BG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG12P232F1024GL125_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG12P232F1024GL125_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG12P232F1024GL125_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG12P232F1024GL125

__EFR32BG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG12P232F1024GL125_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG12P232F1024GL125_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG12P Series

EFR32BG12P232F1024GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
Family	EFR32
Sub Family	EFR32BG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG12P232F1024GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG12P232F1024GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG12P232F1024GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG12P232F1024GM48

__EFR32BG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG12P232F1024GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG12P232F1024GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG12P Series

EFR32BG12P232F1024GM68

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
Family	EFR32
Sub Family	EFR32BG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG12P232F1024GM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG12P232F1024GM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG12P232F1024GM68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG12P232F1024GM68

__EFR32BG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG12P232F1024GM68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG12P232F1024GM68_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG12P Series

EFR32BG12P232F1024IM68

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
Family	EFR32
Sub Family	EFR32BG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG12P232F1024IM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG12P232F1024IM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG12P232F1024IM68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG12P232F1024IM68

__EFR32BG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG12P232F1024IM68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG12P232F1024IM68_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG12P Series

EFR32BG12P232F512GM68

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
Family	EFR32
Sub Family	EFR32BG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG12P232F512GM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG12P232F512GM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG12P232F512GM68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG12P232F512GM68

__EFR32BG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG12P232F512GM68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG12P232F512GM68_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG12P Series

EFR32BG12P232F512IM68

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
Family	EFR32
Sub Family	EFR32BG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG12P232F512IM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG12P232F512IM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG12P232F512IM68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG12P232F512IM68

__EFR32BG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG12P232F512IM68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG12P232F512IM68_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG12P Series

EFR32BG12P332F1024GL125

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
Family	EFR32
Sub Family	EFR32BG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG12P332F1024GL125_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG12P332F1024GL125_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG12P332F1024GL125_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG12P332F1024GL125

__EFR32BG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG12P332F1024GL125_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG12P332F1024GL125_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG12P Series

EFR32BG12P332F1024GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
Family	EFR32
Sub Family	EFR32BG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG12P332F1024GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG12P332F1024GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG12P332F1024GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG12P332F1024GM48

__EFR32BG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG12P332F1024GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG12P332F1024GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG12P Series

EFR32BG12P332F1024IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
Family	EFR32
Sub Family	EFR32BG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG12P332F1024IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG12P332F1024IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG12P332F1024IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG12P332F1024IM48

__EFR32BG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG12P332F1024IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG12P332F1024IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG12P Series

EFR32BG12P432F1024GL125

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
Family	EFR32
Sub Family	EFR32BG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG12P432F1024GL125_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG12P432F1024GL125_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG12P432F1024GL125_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG12P432F1024GL125

__EFR32BG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG12P432F1024GL125_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG12P432F1024GL125_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG12P Series

EFR32BG12P432F1024GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
Family	EFR32
Sub Family	EFR32BG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG12P432F1024GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG12P432F1024GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG12P432F1024GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG12P432F1024GM48

__EFR32BG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG12P432F1024GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG12P432F1024GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG12P Series

EFR32BG12P433F1024GL125

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
Family	EFR32
Sub Family	EFR32BG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG12P433F1024GL125_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG12P433F1024GL125_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG12P433F1024GL125_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
EFR32BG12P433F1024GL125	
__EFR32BG12P_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/EFR32BG12P433F1024GL125_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/EFR32BG12P433F1024GL125_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	EFR32BG12P Series

EFR32BG12P433F1024GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
Family	EFR32
Sub Family	EFR32BG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG12P433F1024GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG12P433F1024GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG12P433F1024GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG12P433F1024GM48

__EFR32BG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG12P433F1024GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG12P433F1024GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG12P Series

EFR32BG12P433F1024GM68

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
Family	EFR32
Sub Family	EFR32BG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG12P433F1024GM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG12P433F1024GM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG12P433F1024GM68_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
EFR32BG12P433F1024GM68	
__EFR32BG12P_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/EFR32BG12P433F1024GM68_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/EFR32BG12P433F1024GM68_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	EFR32BG12P Series

EFR32BG12P433F1024IM68

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c
Family	EFR32
Sub Family	EFR32BG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG12P433F1024IM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG12P433F1024IM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG12P433F1024IM68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG12P433F1024IM68

__EFR32BG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG12P433F1024IM68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG12P/Source/system_efr32bg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG12P433F1024IM68_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG12P Series

EFR32BG13P Series Family

[EFR32BG13P532F512GM32](#)

[EFR32BG13P532F512GM48](#)

[EFR32BG13P632F512GM32](#)

[EFR32BG13P632F512GM48](#)

[EFR32BG13P632F512GM51](#)

[EFR32BG13P632F512IM32](#)

[EFR32BG13P632F512IM48](#)

[EFR32BG13P732F512GM32](#)

[EFR32BG13P732F512GM48](#)

[EFR32BG13P732F512GM51](#)

[EFR32BG13P733F512GM48](#)

EFR32BG13P532F512GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c
Family	EFR32
Sub Family	EFR32BG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG13P532F512GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG13P532F512GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG13P532F512GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG13P532F512GM32

__EFR32BG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG13P532F512GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG13P532F512GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG13P Series

EFR32BG13P532F512GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c
Family	EFR32
Sub Family	EFR32BG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG13P532F512GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG13P532F512GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG13P532F512GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG13P532F512GM48

__EFR32BG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG13P532F512GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG13P532F512GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG13P Series

EFR32BG13P632F512GM32

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c
Family	EFR32
Sub Family	EFR32BG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG13P632F512GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG13P632F512GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG13P632F512GM32_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
EFR32BG13P632F512GM32	
__EFR32BG13P_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/EFR32BG13P632F512GM32_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/EFR32BG13P632F512GM32_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	EFR32BG13P Series

EFR32BG13P632F512GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c
Family	EFR32
Sub Family	EFR32BG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG13P632F512GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG13P632F512GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG13P632F512GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG13P632F512GM48

__EFR32BG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG13P632F512GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG13P632F512GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG13P Series

EFR32BG13P632F512GM51

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c
Family	EFR32
Sub Family	EFR32BG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG13P632F512GM51_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG13P632F512GM51_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG13P632F512GM51_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG13P632F512GM51

__EFR32BG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG13P632F512GM51_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG13P632F512GM51_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG13P Series

EFR32BG13P632F512IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c
Family	EFR32
Sub Family	EFR32BG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG13P632F512IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG13P632F512IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG13P632F512IM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG13P632F512IM32

__EFR32BG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG13P632F512IM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG13P632F512IM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG13P Series

EFR32BG13P632F512IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c
Family	EFR32
Sub Family	EFR32BG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG13P632F512IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG13P632F512IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG13P632F512IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG13P632F512IM48

__EFR32BG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG13P632F512IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG13P632F512IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG13P Series

EFR32BG13P732F512GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c
Family	EFR32
Sub Family	EFR32BG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG13P732F512GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG13P732F512GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG13P732F512GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG13P732F512GM32

__EFR32BG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG13P732F512GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG13P732F512GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG13P Series

EFR32BG13P732F512GM48

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c
Family	EFR32
Sub Family	EFR32BG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG13P732F512GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG13P732F512GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG13P732F512GM48_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
EFR32BG13P732F512GM48	
__EFR32BG13P_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/EFR32BG13P732F512GM48_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/EFR32BG13P732F512GM48_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	EFR32BG13P Series

EFR32BG13P732F512GM51

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c
Family	EFR32
Sub Family	EFR32BG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG13P732F512GM51_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG13P732F512GM51_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG13P732F512GM51_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG13P732F512GM51

__EFR32BG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG13P732F512GM51_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG13P732F512GM51_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG13P Series

EFR32BG13P733F512GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c
Family	EFR32
Sub Family	EFR32BG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG13P733F512GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG13P733F512GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG13P733F512GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG13P733F512GM48

__EFR32BG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG13P733F512GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG13P/Source/system_efr32bg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG13P733F512GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG13P Series

EFR32BG1B Series Family

[EFR32BG1B132F128GJ43](#)
[EFR32BG1B132F128GM32](#)
[EFR32BG1B132F128GM48](#)
[EFR32BG1B132F256GD32](#)
[EFR32BG1B132F256GJ43](#)
[EFR32BG1B132F256GM32](#)
[EFR32BG1B132F256GM48](#)
[EFR32BG1B132F256GM56](#)
[EFR32BG1B132F256IM32](#)
[EFR32BG1B232F128GJ43](#)
[EFR32BG1B232F128GM32](#)
[EFR32BG1B232F128GM48](#)
[EFR32BG1B232F256GJ43](#)
[EFR32BG1B232F256GM32](#)
[EFR32BG1B232F256GM48](#)
[EFR32BG1B232F256GM56](#)
[EFR32BG1B232F256IM32](#)

EFR32BG1B132F128GJ43

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
Family	EFR32
Sub Family	EFR32BG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F128GJ43_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F128GJ43_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1B132F128GJ43_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1B132F128GJ43

__EFR32BG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1B132F128GJ43_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1B132F128GJ43_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1B Series

EFR32BG1B132F128GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
Family	EFR32
Sub Family	EFR32BG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F128GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F128GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1B132F128GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1B132F128GM32

__EFR32BG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1B132F128GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1B132F128GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1B Series

EFR32BG1B132F128GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
Family	EFR32
Sub Family	EFR32BG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F128GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F128GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1B132F128GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1B132F128GM48

__EFR32BG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1B132F128GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1B132F128GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1B Series

EFR32BG1B132F256GD32

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
Family	EFR32
Sub Family	EFR32BG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256GD32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256GD32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1B132F256GD32_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFR32BG1B132F256GD32
__EFR32BG1B_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256GD32_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1B132F256GD32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG1B Series

EFR32BG1B132F256GJ43

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
Family	EFR32
Sub Family	EFR32BG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256GJ43_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256GJ43_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1B132F256GJ43_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1B132F256GJ43

__EFR32BG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256GJ43_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1B132F256GJ43_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1B Series

EFR32BG1B132F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
Family	EFR32
Sub Family	EFR32BG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1B132F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1B132F256GM32

__EFR32BG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1B132F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1B Series

EFR32BG1B132F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
Family	EFR32
Sub Family	EFR32BG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1B132F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1B132F256GM48

__EFR32BG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1B132F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1B Series

EFR32BG1B132F256GM56

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
Family	EFR32
Sub Family	EFR32BG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256GM56_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256GM56_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1B132F256GM56_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1B132F256GM56

__EFR32BG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256GM56_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1B132F256GM56_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1B Series

EFR32BG1B132F256IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
Family	EFR32
Sub Family	EFR32BG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1B132F256IM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1B132F256IM32

__EFR32BG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1B132F256IM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1B132F256IM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1B Series

EFR32BG1B232F128GJ43

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
Family	EFR32
Sub Family	EFR32BG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1B232F128GJ43_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1B232F128GJ43_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1B232F128GJ43_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1B232F128GJ43

__EFR32BG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1B232F128GJ43_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1B232F128GJ43_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1B Series

EFR32BG1B232F128GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
Family	EFR32
Sub Family	EFR32BG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1B232F128GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1B232F128GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1B232F128GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1B232F128GM32

__EFR32BG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1B232F128GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1B232F128GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1B Series

EFR32BG1B232F128GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
Family	EFR32
Sub Family	EFR32BG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1B232F128GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1B232F128GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1B232F128GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1B232F128GM48

__EFR32BG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1B232F128GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1B232F128GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1B Series

EFR32BG1B232F256GJ43

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
Family	EFR32
Sub Family	EFR32BG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1B232F256GJ43_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1B232F256GJ43_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1B232F256GJ43_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1B232F256GJ43

__EFR32BG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1B232F256GJ43_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1B232F256GJ43_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1B Series

EFR32BG1B232F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
Family	EFR32
Sub Family	EFR32BG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1B232F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1B232F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1B232F256GM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM4
EFR32BG1B232F256GM32
__EFR32BG1B_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG1B232F256GM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG1B232F256GM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG1B Series
```

EFR32BG1B232F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
Family	EFR32
Sub Family	EFR32BG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1B232F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1B232F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1B232F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1B232F256GM48

__EFR32BG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1B232F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1B232F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1B Series

EFR32BG1B232F256GM56

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
Family	EFR32
Sub Family	EFR32BG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1B232F256GM56_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1B232F256GM56_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1B232F256GM56_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFR32BG1B232F256GM56
__EFR32BG1B_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1B232F256GM56_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1B232F256GM56_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG1B Series

EFR32BG1B232F256IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c
Family	EFR32
Sub Family	EFR32BG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1B232F256IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1B232F256IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1B232F256IM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1B232F256IM32

__EFR32BG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1B232F256IM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1B/Source/system_efr32bg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1B232F256IM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1B Series

EFR32BG1P Series Family

[EFR32BG1P232F256GJ43](#)

[EFR32BG1P232F256GM32](#)

[EFR32BG1P232F256GM48](#)

[EFR32BG1P232F256IM32](#)

[EFR32BG1P232F256IM48](#)

[EFR32BG1P233F256GM48](#)

[EFR32BG1P332F256GJ43](#)

[EFR32BG1P332F256GM32](#)

[EFR32BG1P332F256GM48](#)

[EFR32BG1P333F256GM48](#)

[EFR32BG1P333F256IM48](#)

EFR32BG1P232F256GJ43

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c
Family	EFR32
Sub Family	EFR32BG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1P232F256GJ43_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1P232F256GJ43_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1P232F256GJ43_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1P232F256GJ43

__EFR32BG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1P232F256GJ43_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1P232F256GJ43_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1P Series

EFR32BG1P232F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c
Family	EFR32
Sub Family	EFR32BG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1P232F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1P232F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1P232F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1P232F256GM32

__EFR32BG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1P232F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1P232F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1P Series

EFR32BG1P232F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c
Family	EFR32
Sub Family	EFR32BG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1P232F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1P232F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1P232F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1P232F256GM48

__EFR32BG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1P232F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1P232F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1P Series

EFR32BG1P232F256IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c
Family	EFR32
Sub Family	EFR32BG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1P232F256IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1P232F256IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1P232F256IM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1P232F256IM32

__EFR32BG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1P232F256IM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1P232F256IM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1P Series

EFR32BG1P232F256IM48

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c
Family	EFR32
Sub Family	EFR32BG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1P232F256IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1P232F256IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1P232F256IM48_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFR32BG1P232F256IM48
__EFR32BG1P_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1P232F256IM48_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1P232F256IM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG1P Series

EFR32BG1P233F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c
Family	EFR32
Sub Family	EFR32BG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1P233F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1P233F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1P233F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1P233F256GM48

__EFR32BG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1P233F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1P233F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1P Series

EFR32BG1P332F256GJ43

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c
Family	EFR32
Sub Family	EFR32BG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1P332F256GJ43_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1P332F256GJ43_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1P332F256GJ43_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1P332F256GJ43

__EFR32BG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1P332F256GJ43_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1P332F256GJ43_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1P Series

EFR32BG1P332F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c
Family	EFR32
Sub Family	EFR32BG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1P332F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1P332F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1P332F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1P332F256GM32

__EFR32BG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1P332F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1P332F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1P Series

EFR32BG1P332F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c
Family	EFR32
Sub Family	EFR32BG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1P332F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1P332F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1P332F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1P332F256GM48

__EFR32BG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1P332F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1P332F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1P Series

EFR32BG1P333F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c
Family	EFR32
Sub Family	EFR32BG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1P333F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1P333F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1P333F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1P333F256GM48

__EFR32BG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1P333F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1P333F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1P Series

EFR32BG1P333F256IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c
Family	EFR32
Sub Family	EFR32BG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1P333F256IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1P333F256IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1P333F256IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1P333F256IM48

__EFR32BG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1P333F256IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1P/Source/system_efr32bg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1P333F256IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1P Series

EFR32BG1V Series Family

[EFR32BG1V132F128GJ43](#)

[EFR32BG1V132F128GM32](#)

[EFR32BG1V132F128GM48](#)

[EFR32BG1V132F256GJ43](#)

[EFR32BG1V132F256GM32](#)

[EFR32BG1V132F256GM48](#)

[EFR32BG1V132F256IM32](#)

EFR32BG1V132F128GJ43

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Source/system_efr32bg1v.c
Family	EFR32
Sub Family	EFR32BG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1V132F128GJ43_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1V132F128GJ43_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1V132F128GJ43_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1V132F128GJ43

__EFR32BG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1V132F128GJ43_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Source/system_efr32bg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1V132F128GJ43_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1V Series

EFR32BG1V132F128GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Source/system_efr32bg1v.c
Family	EFR32
Sub Family	EFR32BG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1V132F128GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1V132F128GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1V132F128GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1V132F128GM32

__EFR32BG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1V132F128GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Source/system_efr32bg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1V132F128GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1V Series

EFR32BG1V132F128GM48

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Source/system_efr32bg1v.c
Family	EFR32
Sub Family	EFR32BG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1V132F128GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1V132F128GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1V132F128GM48_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
EFR32BG1V132F128GM48	
__EFR32BG1V_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/EFR32BG1V132F128GM48_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Source/system_efr32bg1v.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/EFR32BG1V132F128GM48_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	EFR32BG1V Series

EFR32BG1V132F256GJ43

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Source/system_efr32bg1v.c
Family	EFR32
Sub Family	EFR32BG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1V132F256GJ43_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1V132F256GJ43_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1V132F256GJ43_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1V132F256GJ43

__EFR32BG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1V132F256GJ43_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Source/system_efr32bg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1V132F256GJ43_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1V Series

EFR32BG1V132F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Source/system_efr32bg1v.c
Family	EFR32
Sub Family	EFR32BG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1V132F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1V132F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1V132F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1V132F256GM32

__EFR32BG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1V132F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Source/system_efr32bg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1V132F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1V Series

EFR32BG1V132F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Source/system_efr32bg1v.c
Family	EFR32
Sub Family	EFR32BG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1V132F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1V132F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1V132F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1V132F256GM48

__EFR32BG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1V132F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Source/system_efr32bg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1V132F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1V Series

EFR32BG1V132F256IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Source/system_efr32bg1v.c
Family	EFR32
Sub Family	EFR32BG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG1V132F256IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG1V132F256IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG1V132F256IM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32BG1V132F256IM32

__EFR32BG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG1V132F256IM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG1V/Source/system_efr32bg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG1V132F256IM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32BG1V Series

EFR32BG21 Series Family

[EFR32BG21A010F1024IM32](#)

[EFR32BG21A010F512IM32](#)

[EFR32BG21A010F768IM32](#)

[EFR32BG21A020F1024IM32](#)

[EFR32BG21A020F512IM32](#)

[EFR32BG21A020F768IM32](#)

[EFR32BG21B010F1024IM32](#)

[EFR32BG21B010F512IM32](#)

[EFR32BG21B010F768IM32](#)

[EFR32BG21B020F1024IM32](#)

[EFR32BG21B020F512IM32](#)

[EFR32BG21B020F768IM32](#)

EFR32BG21A010F1024IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
Family	EFR32
Sub Family	EFR32BG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG21A010F1024IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG21A010F1024IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG21A010F1024IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG21A010F1024IM32
__EFR32BG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG21A010F1024IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG21A010F1024IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG21 Series
```

EFR32BG21A010F512IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
Family	EFR32
Sub Family	EFR32BG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG21A010F512IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG21A010F512IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG21A010F512IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG21A010F512IM32
__EFR32BG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG21A010F512IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG21A010F512IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG21 Series
```

EFR32BG21A010F768IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
Family	EFR32
Sub Family	EFR32BG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG21A010F768IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG21A010F768IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG21A010F768IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG21A010F768IM32
__EFR32BG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000BFFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG21A010F768IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG21A010F768IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG21 Series
```


EFR32BG21A020F1024IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
Family	EFR32
Sub Family	EFR32BG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG21A020F1024IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG21A020F1024IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG21A020F1024IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG21A020F1024IM32
__EFR32BG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG21A020F1024IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG21A020F1024IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG21 Series
```

EFR32BG21A020F512IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
Family	EFR32
Sub Family	EFR32BG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG21A020F512IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG21A020F512IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG21A020F512IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG21A020F512IM32
__EFR32BG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG21A020F512IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG21A020F512IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG21 Series
```

EFR32BG21A020F768IM32

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
Family	EFR32
Sub Family	EFR32BG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG21A020F768IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG21A020F768IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG21A020F768IM32_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFR32BG21A020F768IM32
__EFR32BG21_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x000BFFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG21A020F768IM32_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG21A020F768IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG21 Series

EFR32BG21B010F1024IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
Family	EFR32
Sub Family	EFR32BG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG21B010F1024IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG21B010F1024IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG21B010F1024IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG21B010F1024IM32
__EFR32BG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG21B010F1024IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG21B010F1024IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG21 Series
```

EFR32BG21B010F512IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
Family	EFR32
Sub Family	EFR32BG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG21B010F512IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG21B010F512IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG21B010F512IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG21B010F512IM32
__EFR32BG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG21B010F512IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG21B010F512IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG21 Series
```

EFR32BG21B010F768IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
Family	EFR32
Sub Family	EFR32BG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG21B010F768IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG21B010F768IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG21B010F768IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG21B010F768IM32
__EFR32BG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000BFFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG21B010F768IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG21B010F768IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG21 Series
```

EFR32BG21B020F1024IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
Family	EFR32
Sub Family	EFR32BG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG21B020F1024IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG21B020F1024IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG21B020F1024IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG21B020F1024IM32
__EFR32BG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG21B020F1024IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG21B020F1024IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG21 Series
```

EFR32BG21B020F512IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
Family	EFR32
Sub Family	EFR32BG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG21B020F512IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG21B020F512IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG21B020F512IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG21B020F512IM32
__EFR32BG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG21B020F512IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG21B020F512IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG21 Series
```


EFR32BG21B020F768IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
Family	EFR32
Sub Family	EFR32BG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG21B020F768IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG21B020F768IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG21B020F768IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG21B020F768IM32
__EFR32BG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000BFFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG21B020F768IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG21/Source/system_efr32bg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG21B020F768IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG21 Series
```

EFR32BG22 Series Family

[EFR32BG22C112F352GM32](#)

[EFR32BG22C112F352GM40](#)

[EFR32BG22C222F352GM32](#)

[EFR32BG22C222F352GM40](#)

[EFR32BG22C222F352GN32](#)

[EFR32BG22C224F512GM32](#)

[EFR32BG22C224F512GM40](#)

[EFR32BG22C224F512GN32](#)

[EFR32BG22C224F512IM32](#)

[EFR32BG22C224F512IM40](#)

EFR32BG22C112F352GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
Family	EFR32
Sub Family	EFR32BG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG22C112F352GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG22C112F352GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG22C112F352GM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG22C112F352GM32
__EFR32BG22_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x00057FFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG22C112F352GM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG22C112F352GM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG22 Series
```

EFR32BG22C112F352GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
Family	EFR32
Sub Family	EFR32BG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG22C112F352GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG22C112F352GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG22C112F352GM40_Vectors.s

Preprocessor Definitions

```

ARM_MATH_ARMV8MML
EFR32BG22C112F352GM40
__EFR32BG22_Series_SUBFAMILY
__EFR32_FAMILY

```

Memory Segments

FLASH	0x00000000 - 0x00057FFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```

DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG22C112F352GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG22C112F352GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG22 Series

```

EFR32BG22C222F352GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
Family	EFR32
Sub Family	EFR32BG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG22C222F352GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG22C222F352GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG22C222F352GM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG22C222F352GM32
__EFR32BG22_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x00057FFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG22C222F352GM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG22C222F352GM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG22 Series
```

EFR32BG22C222F352GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
Family	EFR32
Sub Family	EFR32BG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG22C222F352GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG22C222F352GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG22C222F352GM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG22C222F352GM40
__EFR32BG22_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x00057FFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG22C222F352GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG22C222F352GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG22 Series
```

EFR32BG22C222F352GN32

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
Family	EFR32
Sub Family	EFR32BG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG22C222F352GN32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG22C222F352GN32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG22C222F352GN32_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFR32BG22C222F352GN32
__EFR32BG22_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x00057FFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32BG22C222F352GN32_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32BG22C222F352GN32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG22 Series

EFR32BG22C224F512GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
Family	EFR32
Sub Family	EFR32BG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG22C224F512GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG22C224F512GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG22C224F512GM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG22C224F512GM32
__EFR32BG22_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG22C224F512GM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG22C224F512GM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG22 Series
```


EFR32BG22C224F512GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
Family	EFR32
Sub Family	EFR32BG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG22C224F512GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG22C224F512GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG22C224F512GM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG22C224F512GM40
__EFR32BG22_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG22C224F512GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG22C224F512GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG22 Series
```

EFR32BG22C224F512GN32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
Family	EFR32
Sub Family	EFR32BG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG22C224F512GN32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG22C224F512GN32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG22C224F512GN32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG22C224F512GN32
__EFR32BG22_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG22C224F512GN32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG22C224F512GN32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG22 Series
```

EFR32BG22C224F512IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
Family	EFR32
Sub Family	EFR32BG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG22C224F512IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG22C224F512IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG22C224F512IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG22C224F512IM32
__EFR32BG22_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG22C224F512IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG22C224F512IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG22 Series
```

EFR32BG22C224F512IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ EFR32BG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ EFR32BG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ EFR32BG22/Source/system_efr32bg22.c
Family	EFR32
Sub Family	EFR32BG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/ EFR32BG22C224F512IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/ EFR32BG22C224F512IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/ EFR32BG22C224F512IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG22C224F512IM40
__EFR32BG22_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG22C224F512IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG22/Source/system_efr32bg22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG22C224F512IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG22 Series
```

EFR32BG24 Series Family

[EFR32BG24A010F1024IM40](#)

[EFR32BG24A010F1024IM48](#)

[EFR32BG24A010F1536IM40](#)

[EFR32BG24A020F1024IM40](#)

[EFR32BG24A020F1024IM48](#)

[EFR32BG24A020F1536IM40](#)

[EFR32BG24A610F1536IM40](#)

[EFR32BG24A620F1536IM40](#)

EFR32BG24A010F1024IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Source/system_efr32bg24.c
Family	EFR32
Sub Family	EFR32BG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG24A010F1024IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG24A010F1024IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG24A010F1024IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG24A010F1024IM40
__EFR32BG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x080FFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG24A010F1024IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Source/system_efr32bg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG24A010F1024IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG24 Series
```

EFR32BG24A010F1024IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Source/system_efr32bg24.c
Family	EFR32
Sub Family	EFR32BG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG24A010F1024IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG24A010F1024IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG24A010F1024IM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG24A010F1024IM48
__EFR32BG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x080FFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG24A010F1024IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Source/system_efr32bg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG24A010F1024IM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG24 Series
```

EFR32BG24A010F1536IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Source/system_efr32bg24.c
Family	EFR32
Sub Family	EFR32BG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG24A010F1536IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG24A010F1536IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG24A010F1536IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG24A010F1536IM40
__EFR32BG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG24A010F1536IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Source/system_efr32bg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG24A010F1536IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG24 Series
```


EFR32BG24A020F1024IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Source/system_efr32bg24.c
Family	EFR32
Sub Family	EFR32BG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG24A020F1024IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG24A020F1024IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG24A020F1024IM40_Vectors.s

Preprocessor Definitions

```

ARM_MATH_ARMV8MML
EFR32BG24A020F1024IM40
__EFR32BG24_Series_SUBFAMILY
__EFR32_FAMILY

```

Memory Segments

FLASH	0x08000000 - 0x080FFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

```

DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG24A020F1024IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Source/system_efr32bg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG24A020F1024IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG24 Series

```

EFR32BG24A020F1024IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Source/system_efr32bg24.c
Family	EFR32
Sub Family	EFR32BG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG24A020F1024IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG24A020F1024IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG24A020F1024IM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG24A020F1024IM48
__EFR32BG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x080FFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG24A020F1024IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Source/system_efr32bg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG24A020F1024IM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG24 Series
```

EFR32BG24A020F1536IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Source/system_efr32bg24.c
Family	EFR32
Sub Family	EFR32BG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG24A020F1536IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG24A020F1536IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG24A020F1536IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG24A020F1536IM40
__EFR32BG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG24A020F1536IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Source/system_efr32bg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG24A020F1536IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG24 Series
```

EFR32BG24A610F1536IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Source/system_efr32bg24.c
Family	EFR32
Sub Family	EFR32BG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG24A610F1536IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG24A610F1536IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG24A610F1536IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32BG24A610F1536IM40
__EFR32BG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG24A610F1536IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Source/system_efr32bg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG24A610F1536IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG24 Series
```

EFR32BG24A620F1536IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Source/system_efr32bg24.c
Family	EFR32
Sub Family	EFR32BG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32BG24A620F1536IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32BG24A620F1536IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32BG24A620F1536IM40_Vectors.s

Preprocessor Definitions

```

ARM_MATH_ARMV8MML
EFR32BG24A620F1536IM40
__EFR32BG24_Series_SUBFAMILY
__EFR32_FAMILY

```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

```

DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32BG24A620F1536IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32BG24/Source/system_efr32bg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32BG24A620F1536IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32BG24 Series

```

EFR32FG12P Series Family

[EFR32FG12P231F1024GL125](#)
[EFR32FG12P231F1024GM48](#)
[EFR32FG12P231F1024GM68](#)
[EFR32FG12P231F512GM68](#)
[EFR32FG12P232F1024GL125](#)
[EFR32FG12P232F1024GM48](#)
[EFR32FG12P431F1024GL125](#)
[EFR32FG12P431F1024GM48](#)
[EFR32FG12P431F1024GM68](#)
[EFR32FG12P431F1024IM48](#)
[EFR32FG12P431F512GM68](#)
[EFR32FG12P432F1024GL125](#)
[EFR32FG12P432F1024GM48](#)
[EFR32FG12P433F1024GL125](#)
[EFR32FG12P433F1024GM48](#)
[EFR32FG12P433F1024GM68](#)

EFR32FG12P231F1024GL125

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c
Family	EFR32
Sub Family	EFR32FG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG12P231F1024GL125_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG12P231F1024GL125_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG12P231F1024GL125_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
EFR32FG12P231F1024GL125	
__EFR32FG12P_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/EFR32FG12P231F1024GL125_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/EFR32FG12P231F1024GL125_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	EFR32FG12P Series

EFR32FG12P231F1024GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c
Family	EFR32
Sub Family	EFR32FG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG12P231F1024GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG12P231F1024GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG12P231F1024GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG12P231F1024GM48

__EFR32FG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG12P231F1024GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG12P231F1024GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG12P Series

EFR32FG12P231F1024GM68

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c
Family	EFR32
Sub Family	EFR32FG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG12P231F1024GM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG12P231F1024GM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG12P231F1024GM68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG12P231F1024GM68

__EFR32FG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG12P231F1024GM68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG12P231F1024GM68_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG12P Series

EFR32FG12P231F512GM68

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c
Family	EFR32
Sub Family	EFR32FG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG12P231F512GM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG12P231F512GM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG12P231F512GM68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG12P231F512GM68

__EFR32FG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG12P231F512GM68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG12P231F512GM68_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG12P Series

EFR32FG12P232F1024GL125

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c
Family	EFR32
Sub Family	EFR32FG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG12P232F1024GL125_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG12P232F1024GL125_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG12P232F1024GL125_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG12P232F1024GL125

__EFR32FG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG12P232F1024GL125_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG12P232F1024GL125_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG12P Series

EFR32FG12P232F1024GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c
Family	EFR32
Sub Family	EFR32FG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG12P232F1024GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG12P232F1024GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG12P232F1024GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG12P232F1024GM48

__EFR32FG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG12P232F1024GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG12P232F1024GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG12P Series

EFR32FG12P431F1024GL125

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c
Family	EFR32
Sub Family	EFR32FG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG12P431F1024GL125_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG12P431F1024GL125_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG12P431F1024GL125_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG12P431F1024GL125

__EFR32FG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG12P431F1024GL125_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG12P431F1024GL125_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG12P Series

EFR32FG12P431F1024GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c
Family	EFR32
Sub Family	EFR32FG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG12P431F1024GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG12P431F1024GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG12P431F1024GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG12P431F1024GM48

__EFR32FG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG12P431F1024GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG12P431F1024GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG12P Series

EFR32FG12P431F1024GM68

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c
Family	EFR32
Sub Family	EFR32FG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG12P431F1024GM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG12P431F1024GM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG12P431F1024GM68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG12P431F1024GM68

__EFR32FG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG12P431F1024GM68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG12P431F1024GM68_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG12P Series

EFR32FG12P431F1024IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c
Family	EFR32
Sub Family	EFR32FG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG12P431F1024IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG12P431F1024IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG12P431F1024IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG12P431F1024IM48

__EFR32FG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG12P431F1024IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG12P431F1024IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG12P Series

EFR32FG12P431F512GM68

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c
Family	EFR32
Sub Family	EFR32FG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG12P431F512GM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG12P431F512GM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG12P431F512GM68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG12P431F512GM68

__EFR32FG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG12P431F512GM68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG12P431F512GM68_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG12P Series

EFR32FG12P432F1024GL125

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c
Family	EFR32
Sub Family	EFR32FG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG12P432F1024GL125_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG12P432F1024GL125_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG12P432F1024GL125_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG12P432F1024GL125

__EFR32FG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG12P432F1024GL125_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG12P432F1024GL125_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG12P Series

EFR32FG12P432F1024GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c
Family	EFR32
Sub Family	EFR32FG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG12P432F1024GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG12P432F1024GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG12P432F1024GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG12P432F1024GM48

__EFR32FG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG12P432F1024GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG12P432F1024GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG12P Series

EFR32FG12P433F1024GL125

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c
Family	EFR32
Sub Family	EFR32FG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG12P433F1024GL125_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG12P433F1024GL125_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG12P433F1024GL125_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG12P433F1024GL125

__EFR32FG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG12P433F1024GL125_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG12P433F1024GL125_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG12P Series

EFR32FG12P433F1024GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c
Family	EFR32
Sub Family	EFR32FG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG12P433F1024GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG12P433F1024GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG12P433F1024GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG12P433F1024GM48

__EFR32FG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG12P433F1024GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG12P433F1024GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG12P Series

EFR32FG12P433F1024GM68

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c
Family	EFR32
Sub Family	EFR32FG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG12P433F1024GM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG12P433F1024GM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG12P433F1024GM68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG12P433F1024GM68

__EFR32FG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG12P433F1024GM68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG12P/Source/system_efr32fg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG12P433F1024GM68_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG12P Series

EFR32FG13P Series Family

[EFR32FG13P231F512GM32](#)

[EFR32FG13P231F512GM48](#)

[EFR32FG13P231F512IM32](#)

[EFR32FG13P231F512IM48](#)

[EFR32FG13P232F512GM32](#)

[EFR32FG13P232F512GM48](#)

[EFR32FG13P233F512GM48](#)

EFR32FG13P231F512GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Source/system_efr32fg13p.c
Family	EFR32
Sub Family	EFR32FG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG13P231F512GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG13P231F512GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG13P231F512GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG13P231F512GM32

__EFR32FG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG13P231F512GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Source/system_efr32fg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG13P231F512GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG13P Series

EFR32FG13P231F512GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Source/system_efr32fg13p.c
Family	EFR32
Sub Family	EFR32FG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG13P231F512GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG13P231F512GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG13P231F512GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG13P231F512GM48

__EFR32FG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG13P231F512GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Source/system_efr32fg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG13P231F512GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG13P Series

EFR32FG13P231F512IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Source/system_efr32fg13p.c
Family	EFR32
Sub Family	EFR32FG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG13P231F512IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG13P231F512IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG13P231F512IM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG13P231F512IM32

__EFR32FG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG13P231F512IM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Source/system_efr32fg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG13P231F512IM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG13P Series

EFR32FG13P231F512IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Source/system_efr32fg13p.c
Family	EFR32
Sub Family	EFR32FG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG13P231F512IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG13P231F512IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG13P231F512IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG13P231F512IM48

__EFR32FG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG13P231F512IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Source/system_efr32fg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG13P231F512IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG13P Series

EFR32FG13P232F512GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Source/system_efr32fg13p.c
Family	EFR32
Sub Family	EFR32FG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG13P232F512GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG13P232F512GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG13P232F512GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG13P232F512GM32

__EFR32FG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG13P232F512GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Source/system_efr32fg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG13P232F512GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG13P Series

EFR32FG13P232F512GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Source/system_efr32fg13p.c
Family	EFR32
Sub Family	EFR32FG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG13P232F512GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG13P232F512GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG13P232F512GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG13P232F512GM48

__EFR32FG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG13P232F512GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Source/system_efr32fg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG13P232F512GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG13P Series

EFR32FG13P233F512GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Source/system_efr32fg13p.c
Family	EFR32
Sub Family	EFR32FG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG13P233F512GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG13P233F512GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG13P233F512GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG13P233F512GM48

__EFR32FG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG13P233F512GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG13P/Source/system_efr32fg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG13P233F512GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG13P Series

EFR32FG14P Series Family

[EFR32FG14P231F128GM32](#)

[EFR32FG14P231F128GM48](#)

[EFR32FG14P231F256GM32](#)

[EFR32FG14P231F256GM48](#)

[EFR32FG14P231F256IM32](#)

[EFR32FG14P231F256IM48](#)

[EFR32FG14P232F128GM32](#)

[EFR32FG14P232F128GM48](#)

[EFR32FG14P232F256GM32](#)

[EFR32FG14P232F256GM48](#)

[EFR32FG14P233F128GM48](#)

[EFR32FG14P233F256GM48](#)

EFR32FG14P231F128GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c
Family	EFR32
Sub Family	EFR32FG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG14P231F128GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG14P231F128GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG14P231F128GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG14P231F128GM32

__EFR32FG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG14P231F128GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG14P231F128GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG14P Series

EFR32FG14P231F128GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c
Family	EFR32
Sub Family	EFR32FG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG14P231F128GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG14P231F128GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG14P231F128GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG14P231F128GM48

__EFR32FG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG14P231F128GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG14P231F128GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG14P Series

EFR32FG14P231F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c
Family	EFR32
Sub Family	EFR32FG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG14P231F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG14P231F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG14P231F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG14P231F256GM32

__EFR32FG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG14P231F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG14P231F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG14P Series

EFR32FG14P231F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c
Family	EFR32
Sub Family	EFR32FG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG14P231F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG14P231F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG14P231F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG14P231F256GM48

__EFR32FG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG14P231F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG14P231F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG14P Series

EFR32FG14P231F256IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c
Family	EFR32
Sub Family	EFR32FG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG14P231F256IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG14P231F256IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG14P231F256IM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG14P231F256IM32

__EFR32FG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG14P231F256IM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG14P231F256IM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG14P Series

EFR32FG14P231F256IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c
Family	EFR32
Sub Family	EFR32FG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG14P231F256IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG14P231F256IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG14P231F256IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG14P231F256IM48

__EFR32FG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG14P231F256IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG14P231F256IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG14P Series

EFR32FG14P232F128GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c
Family	EFR32
Sub Family	EFR32FG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG14P232F128GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG14P232F128GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG14P232F128GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG14P232F128GM32

__EFR32FG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG14P232F128GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG14P232F128GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG14P Series

EFR32FG14P232F128GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c
Family	EFR32
Sub Family	EFR32FG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG14P232F128GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG14P232F128GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG14P232F128GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG14P232F128GM48

__EFR32FG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG14P232F128GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG14P232F128GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG14P Series

EFR32FG14P232F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c
Family	EFR32
Sub Family	EFR32FG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG14P232F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG14P232F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG14P232F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG14P232F256GM32

__EFR32FG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG14P232F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG14P232F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG14P Series

EFR32FG14P232F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c
Family	EFR32
Sub Family	EFR32FG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG14P232F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG14P232F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG14P232F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG14P232F256GM48

__EFR32FG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG14P232F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG14P232F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG14P Series

EFR32FG14P233F128GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c
Family	EFR32
Sub Family	EFR32FG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG14P233F128GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG14P233F128GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG14P233F128GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG14P233F128GM48

__EFR32FG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG14P233F128GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG14P233F128GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG14P Series

EFR32FG14P233F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c
Family	EFR32
Sub Family	EFR32FG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG14P233F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG14P233F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG14P233F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG14P233F256GM48

__EFR32FG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG14P233F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14P/Source/system_efr32fg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG14P233F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG14P Series

EFR32FG14V Series Family

[EFR32FG14V132F256GM32](#)

[EFR32FG14V132F256GM48](#)

EFR32FG14V132F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14V/Source/system_efr32fg14v.c
Family	EFR32
Sub Family	EFR32FG14V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG14V132F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG14V132F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG14V132F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG14V132F256GM32

__EFR32FG14V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG14V132F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14V/Source/system_efr32fg14v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG14V132F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG14V Series

EFR32FG14V132F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14V/Source/system_efr32fg14v.c
Family	EFR32
Sub Family	EFR32FG14V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG14V132F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG14V132F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG14V132F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG14V132F256GM48

__EFR32FG14V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG14V132F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG14V/Source/system_efr32fg14v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG14V132F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG14V Series

EFR32FG1P Series Family

[EFR32FG1P131F128GM32](#)

[EFR32FG1P131F128GM48](#)

[EFR32FG1P131F256GM32](#)

[EFR32FG1P131F256GM48](#)

[EFR32FG1P131F256IM32](#)

[EFR32FG1P131F256IM48](#)

[EFR32FG1P131F64GM32](#)

[EFR32FG1P131F64GM48](#)

[EFR32FG1P132F128GM32](#)

[EFR32FG1P132F128GM48](#)

[EFR32FG1P132F256GM32](#)

[EFR32FG1P132F256GM48](#)

[EFR32FG1P132F64GM32](#)

[EFR32FG1P132F64GM48](#)

[EFR32FG1P133F128GM48](#)

[EFR32FG1P133F256GM32](#)

[EFR32FG1P133F256GM48](#)

[EFR32FG1P133F64GM48](#)

EFR32FG1P131F128GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P131F128GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P131F128GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P131F128GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1P131F128GM32

__EFR32FG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1P131F128GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1P131F128GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1P Series

EFR32FG1P131F128GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P131F128GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P131F128GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P131F128GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1P131F128GM48

__EFR32FG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1P131F128GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1P131F128GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1P Series

EFR32FG1P131F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P131F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P131F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P131F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1P131F256GM32

__EFR32FG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1P131F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1P131F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1P Series

EFR32FG1P131F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P131F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P131F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P131F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1P131F256GM48

__EFR32FG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1P131F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1P131F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1P Series

EFR32FG1P131F256IM32

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P131F256IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P131F256IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P131F256IM32_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
EFR32FG1P131F256IM32	
__EFR32FG1P_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/EFR32FG1P131F256IM32_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/EFR32FG1P131F256IM32_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	EFR32FG1P Series

EFR32FG1P131F256IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P131F256IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P131F256IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P131F256IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1P131F256IM48

__EFR32FG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1P131F256IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1P131F256IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1P Series

EFR32FG1P131F64GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P131F64GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P131F64GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P131F64GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1P131F64GM32

__EFR32FG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1P131F64GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1P131F64GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1P Series

EFR32FG1P131F64GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P131F64GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P131F64GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P131F64GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1P131F64GM48

__EFR32FG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1P131F64GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1P131F64GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1P Series

EFR32FG1P132F128GM32

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P132F128GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P132F128GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P132F128GM32_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFR32FG1P132F128GM32
__EFR32FG1P_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1P132F128GM32_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1P132F128GM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG1P Series

EFR32FG1P132F128GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P132F128GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P132F128GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P132F128GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1P132F128GM48

__EFR32FG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1P132F128GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1P132F128GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1P Series

EFR32FG1P132F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P132F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P132F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P132F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1P132F256GM32

__EFR32FG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1P132F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1P132F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1P Series

EFR32FG1P132F256GM48

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P132F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P132F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P132F256GM48_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
EFR32FG1P132F256GM48	
__EFR32FG1P_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/EFR32FG1P132F256GM48_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/EFR32FG1P132F256GM48_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	EFR32FG1P Series

EFR32FG1P132F64GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P132F64GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P132F64GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P132F64GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1P132F64GM32

__EFR32FG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1P132F64GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1P132F64GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1P Series

EFR32FG1P132F64GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P132F64GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P132F64GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P132F64GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1P132F64GM48

__EFR32FG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1P132F64GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1P132F64GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1P Series

EFR32FG1P133F128GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P133F128GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P133F128GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P133F128GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1P133F128GM48

__EFR32FG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1P133F128GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1P133F128GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1P Series

EFR32FG1P133F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P133F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P133F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P133F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1P133F256GM32

__EFR32FG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1P133F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1P133F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1P Series

EFR32FG1P133F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P133F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P133F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P133F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1P133F256GM48

__EFR32FG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1P133F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1P133F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1P Series

EFR32FG1P133F64GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c
Family	EFR32
Sub Family	EFR32FG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1P133F64GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1P133F64GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1P133F64GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1P133F64GM48

__EFR32FG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1P133F64GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1P/Source/system_efr32fg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1P133F64GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1P Series

EFR32FG1V Series Family

EFR32FG1V032F128GM32
EFR32FG1V032F256GM32
EFR32FG1V131F128GM32
EFR32FG1V131F128GM48
EFR32FG1V131F256GM32
EFR32FG1V131F256GM48
EFR32FG1V131F32GM32
EFR32FG1V131F32GM48
EFR32FG1V131F64GM32
EFR32FG1V131F64GM48
EFR32FG1V132F128GM32
EFR32FG1V132F128GM48
EFR32FG1V132F256GM32
EFR32FG1V132F256GM48
EFR32FG1V132F32GM32
EFR32FG1V132F32GM48
EFR32FG1V132F64GM32
EFR32FG1V132F64GM48
EFR32FG1V232F256GM32
EFR32FG1V232F256GM48

EFR32FG1V032F128GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V032F128GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V032F128GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V032F128GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1V032F128GM32

__EFR32FG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1V032F128GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1V032F128GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1V Series

EFR32FG1V032F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V032F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V032F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V032F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1V032F256GM32

__EFR32FG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1V032F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1V032F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1V Series

EFR32FG1V131F128GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F128GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F128GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V131F128GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1V131F128GM32

__EFR32FG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1V131F128GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1V131F128GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1V Series

EFR32FG1V131F128GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F128GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F128GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V131F128GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1V131F128GM48

__EFR32FG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1V131F128GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1V131F128GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1V Series

EFR32FG1V131F256GM32

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V131F256GM32_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFR32FG1V131F256GM32
__EFR32FG1V_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1V131F256GM32_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1V131F256GM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG1V Series

EFR32FG1V131F256GM48

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V131F256GM48_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
EFR32FG1V131F256GM48	
__EFR32FG1V_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F256GM48_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/EFR32FG1V131F256GM48_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	EFR32FG1V Series

EFR32FG1V131F32GM32

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F32GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F32GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V131F32GM32_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
EFR32FG1V131F32GM32	
__EFR32FG1V_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F32GM32_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/EFR32FG1V131F32GM32_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	EFR32FG1V Series

EFR32FG1V131F32GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F32GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F32GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V131F32GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1V131F32GM48

__EFR32FG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1V131F32GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1V131F32GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1V Series

EFR32FG1V131F64GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F64GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F64GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V131F64GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1V131F64GM32

__EFR32FG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1V131F64GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1V131F64GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1V Series

EFR32FG1V131F64GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F64GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V131F64GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V131F64GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1V131F64GM48

__EFR32FG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1V131F64GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1V131F64GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1V Series

EFR32FG1V132F128GM32

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V132F128GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V132F128GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V132F128GM32_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
EFR32FG1V132F128GM32	
__EFR32FG1V_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/EFR32FG1V132F128GM32_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/EFR32FG1V132F128GM32_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	EFR32FG1V Series

EFR32FG1V132F128GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V132F128GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V132F128GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V132F128GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1V132F128GM48

__EFR32FG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1V132F128GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1V132F128GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1V Series

EFR32FG1V132F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V132F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V132F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V132F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1V132F256GM32

__EFR32FG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1V132F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1V132F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1V Series

EFR32FG1V132F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V132F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V132F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V132F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1V132F256GM48

__EFR32FG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1V132F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1V132F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1V Series

EFR32FG1V132F32GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V132F32GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V132F32GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V132F32GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1V132F32GM32

__EFR32FG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1V132F32GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1V132F32GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1V Series

EFR32FG1V132F32GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V132F32GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V132F32GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V132F32GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1V132F32GM48

__EFR32FG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1V132F32GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1V132F32GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1V Series

EFR32FG1V132F64GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V132F64GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V132F64GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V132F64GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1V132F64GM32

__EFR32FG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1V132F64GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1V132F64GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1V Series

EFR32FG1V132F64GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V132F64GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V132F64GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V132F64GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1V132F64GM48

__EFR32FG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1V132F64GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1V132F64GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1V Series

EFR32FG1V232F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V232F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V232F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V232F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1V232F256GM32

__EFR32FG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1V232F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1V232F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1V Series

EFR32FG1V232F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c
Family	EFR32
Sub Family	EFR32FG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG1V232F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG1V232F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG1V232F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32FG1V232F256GM48

__EFR32FG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32FG1V232F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG1V/Source/system_efr32fg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32FG1V232F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32FG1V Series

EFR32FG22 Series Family

[EFR32FG22C121F256GM32](#)

[EFR32FG22C121F256GM40](#)

[EFR32FG22C121F512GM32](#)

[EFR32FG22C121F512GM40](#)

EFR32FG22C121F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Source/system_efr32fg22.c
Family	EFR32
Sub Family	EFR32FG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG22C121F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG22C121F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG22C121F256GM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG22C121F256GM32
__EFR32FG22_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG22C121F256GM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Source/system_efr32fg22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG22C121F256GM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG22 Series
```


EFR32FG22C121F256GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Source/system_efr32fg22.c
Family	EFR32
Sub Family	EFR32FG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG22C121F256GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG22C121F256GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG22C121F256GM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG22C121F256GM40
__EFR32FG22_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG22C121F256GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Source/system_efr32fg22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG22C121F256GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG22 Series
```

EFR32FG22C121F512GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Source/system_efr32fg22.c
Family	EFR32
Sub Family	EFR32FG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG22C121F512GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG22C121F512GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG22C121F512GM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG22C121F512GM32
__EFR32FG22_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG22C121F512GM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Source/system_efr32fg22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG22C121F512GM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG22 Series
```

EFR32FG22C121F512GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Source/system_efr32fg22.c
Family	EFR32
Sub Family	EFR32FG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG22C121F512GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG22C121F512GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG22C121F512GM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG22C121F512GM40
__EFR32FG22_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG22C121F512GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG22/Source/system_efr32fg22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG22C121F512GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG22 Series
```

EFR32FG23 Series Family

[EFR32FG23A010F128GM40](#)
[EFR32FG23A010F256GM40](#)
[EFR32FG23A010F256GM48](#)
[EFR32FG23A010F512GM40](#)
[EFR32FG23A010F512GM48](#)
[EFR32FG23A011F512GM40](#)
[EFR32FG23A020F128GM40](#)
[EFR32FG23A020F256GM40](#)
[EFR32FG23A020F256GM48](#)
[EFR32FG23A020F512GM40](#)
[EFR32FG23A020F512GM48](#)
[EFR32FG23A021F512GM40](#)
[EFR32FG23B010F128GM40](#)
[EFR32FG23B010F512GM48](#)
[EFR32FG23B010F512IM40](#)
[EFR32FG23B010F512IM48](#)
[EFR32FG23B020F128GM40](#)
[EFR32FG23B020F512IM40](#)
[EFR32FG23B020F512IM48](#)

EFR32FG23A010F128GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23A010F128GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23A010F128GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23A010F128GM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23A010F128GM40
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0801FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23A010F128GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23A010F128GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```

EFR32FG23A010F256GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23A010F256GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23A010F256GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23A010F256GM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23A010F256GM40
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23A010F256GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23A010F256GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```

EFR32FG23A010F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23A010F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23A010F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23A010F256GM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23A010F256GM48
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23A010F256GM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23A010F256GM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```

EFR32FG23A010F512GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23A010F512GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23A010F512GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23A010F512GM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23A010F512GM40
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23A010F512GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23A010F512GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```


EFR32FG23A010F512GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23A010F512GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23A010F512GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23A010F512GM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23A010F512GM48
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23A010F512GM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23A010F512GM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```

EFR32FG23A011F512GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23A011F512GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23A011F512GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23A011F512GM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23A011F512GM40
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23A011F512GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23A011F512GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```

EFR32FG23A020F128GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23A020F128GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23A020F128GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23A020F128GM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23A020F128GM40
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0801FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23A020F128GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23A020F128GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```

EFR32FG23A020F256GM40

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23A020F256GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23A020F256GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23A020F256GM40_Vectors.s

Preprocessor Definitions	
ARM_MATH_ARMV8MML	
EFR32FG23A020F256GM40	
__EFR32FG23_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/EFR32FG23A020F256GM40_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/EFR32FG23A020F256GM40_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	EFR32FG23 Series

EFR32FG23A020F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23A020F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23A020F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23A020F256GM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23A020F256GM48
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0803FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23A020F256GM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23A020F256GM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```

EFR32FG23A020F512GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23A020F512GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23A020F512GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23A020F512GM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23A020F512GM40
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23A020F512GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23A020F512GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```

EFR32FG23A020F512GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23A020F512GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23A020F512GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23A020F512GM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23A020F512GM48
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23A020F512GM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23A020F512GM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```

EFR32FG23A021F512GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23A021F512GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23A021F512GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23A021F512GM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23A021F512GM40
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23A021F512GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23A021F512GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```


EFR32FG23B010F128GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23B010F128GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23B010F128GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23B010F128GM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23B010F128GM40
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0801FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23B010F128GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23B010F128GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```

EFR32FG23B010F512GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23B010F512GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23B010F512GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23B010F512GM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23B010F512GM48
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23B010F512GM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23B010F512GM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```

EFR32FG23B010F512IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23B010F512IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23B010F512IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23B010F512IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23B010F512IM40
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23B010F512IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23B010F512IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```

EFR32FG23B010F512IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23B010F512IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23B010F512IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23B010F512IM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23B010F512IM48
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23B010F512IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23B010F512IM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```

EFR32FG23B020F128GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23B020F128GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23B020F128GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23B020F128GM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23B020F128GM40
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0801FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23B020F128GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23B020F128GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```

EFR32FG23B020F512IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23B020F512IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23B020F512IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23B020F512IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23B020F512IM40
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23B020F512IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23B020F512IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```

EFR32FG23B020F512IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
Family	EFR32
Sub Family	EFR32FG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32FG23B020F512IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32FG23B020F512IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32FG23B020F512IM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32FG23B020F512IM48
__EFR32FG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32FG23B020F512IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32FG23/Source/system_efr32fg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32FG23B020F512IM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32FG23 Series
```

EFR32MG12P Series Family

[EFR32MG12P132F1024GL125](#)
[EFR32MG12P132F1024GM48](#)
[EFR32MG12P132F512GM68](#)
[EFR32MG12P231F1024GM48](#)
[EFR32MG12P231F1024GM68](#)
[EFR32MG12P232F1024GL125](#)
[EFR32MG12P232F1024GM48](#)
[EFR32MG12P232F1024GM68](#)
[EFR32MG12P232F512GM68](#)
[EFR32MG12P332F1024GL125](#)
[EFR32MG12P332F1024GM48](#)
[EFR32MG12P332F1024IM48](#)
[EFR32MG12P431F1024GM48](#)
[EFR32MG12P431F1024GM68](#)
[EFR32MG12P432F1024GL125](#)
[EFR32MG12P432F1024GM48](#)
[EFR32MG12P432F1024GM68](#)
[EFR32MG12P432F1024IM48](#)
[EFR32MG12P433F1024GL125](#)
[EFR32MG12P433F1024GM48](#)
[EFR32MG12P433F1024GM68](#)
[EFR32MG12P433F1024IL125](#)
[EFR32MG12P433F1024IM48](#)

EFR32MG12P132F1024GL125

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P132F1024GL125_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P132F1024GL125_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P132F1024GL125_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P132F1024GL125

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P132F1024GL125_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P132F1024GL125_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P132F1024GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P132F1024GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P132F1024GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P132F1024GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P132F1024GM48

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P132F1024GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P132F1024GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P132F512GM68

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P132F512GM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P132F512GM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P132F512GM68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P132F512GM68

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P132F512GM68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P132F512GM68_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P231F1024GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P231F1024GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P231F1024GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P231F1024GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P231F1024GM48

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P231F1024GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P231F1024GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P231F1024GM68

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P231F1024GM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P231F1024GM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P231F1024GM68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P231F1024GM68

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P231F1024GM68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P231F1024GM68_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P232F1024GL125

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P232F1024GL125_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P232F1024GL125_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P232F1024GL125_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFR32MG12P232F1024GL125
__EFR32MG12P_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P232F1024GL125_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P232F1024GL125_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG12P Series

EFR32MG12P232F1024GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P232F1024GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P232F1024GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P232F1024GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P232F1024GM48

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P232F1024GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P232F1024GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P232F1024GM68

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P232F1024GM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P232F1024GM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P232F1024GM68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P232F1024GM68

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P232F1024GM68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P232F1024GM68_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P232F512GM68

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P232F512GM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P232F512GM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P232F512GM68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P232F512GM68

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P232F512GM68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P232F512GM68_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P332F1024GL125

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P332F1024GL125_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P332F1024GL125_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P332F1024GL125_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFR32MG12P332F1024GL125
__EFR32MG12P_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P332F1024GL125_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P332F1024GL125_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG12P Series

EFR32MG12P332F1024GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P332F1024GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P332F1024GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P332F1024GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P332F1024GM48

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P332F1024GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P332F1024GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P332F1024IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P332F1024IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P332F1024IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P332F1024IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P332F1024IM48

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P332F1024IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P332F1024IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P431F1024GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P431F1024GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P431F1024GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P431F1024GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P431F1024GM48

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P431F1024GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P431F1024GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P431F1024GM68

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P431F1024GM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P431F1024GM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P431F1024GM68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P431F1024GM68

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P431F1024GM68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P431F1024GM68_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P432F1024GL125

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P432F1024GL125_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P432F1024GL125_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P432F1024GL125_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P432F1024GL125

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P432F1024GL125_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P432F1024GL125_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P432F1024GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P432F1024GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P432F1024GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P432F1024GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P432F1024GM48

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P432F1024GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P432F1024GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P432F1024GM68

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P432F1024GM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P432F1024GM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P432F1024GM68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P432F1024GM68

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P432F1024GM68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P432F1024GM68_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P432F1024IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P432F1024IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P432F1024IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P432F1024IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P432F1024IM48

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P432F1024IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P432F1024IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P433F1024GL125

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P433F1024GL125_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P433F1024GL125_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P433F1024GL125_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFR32MG12P433F1024GL125
__EFR32MG12P_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P433F1024GL125_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P433F1024GL125_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG12P Series

EFR32MG12P433F1024GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P433F1024GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P433F1024GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P433F1024GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P433F1024GM48

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P433F1024GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P433F1024GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P433F1024GM68

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P433F1024GM68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P433F1024GM68_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P433F1024GM68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P433F1024GM68

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P433F1024GM68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P433F1024GM68_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG12P433F1024IL125

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P433F1024IL125_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P433F1024IL125_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P433F1024IL125_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM4
EFR32MG12P433F1024IL125
__EFR32MG12P_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG12P433F1024IL125_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG12P433F1024IL125_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG12P Series
```

EFR32MG12P433F1024IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c
Family	EFR32
Sub Family	EFR32MG12P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG12P433F1024IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG12P433F1024IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG12P433F1024IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG12P433F1024IM48

__EFR32MG12P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG12P433F1024IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG12P/Source/system_efr32mg12p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG12P433F1024IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG12P Series

EFR32MG13P Series Family

[EFR32MG13P632F512GM32](#)

[EFR32MG13P632F512GM48](#)

[EFR32MG13P632F512GM51](#)

[EFR32MG13P731F512GM48](#)

[EFR32MG13P732F512GM32](#)

[EFR32MG13P732F512GM48](#)

[EFR32MG13P732F512GM51](#)

[EFR32MG13P732F512IM32](#)

[EFR32MG13P732F512IM48](#)

[EFR32MG13P733F512GM48](#)

[EFR32MG13P733F512IM48](#)

[EFR32MG13P832F512IM48](#)

EFR32MG13P632F512GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c
Family	EFR32
Sub Family	EFR32MG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG13P632F512GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG13P632F512GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG13P632F512GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG13P632F512GM32

__EFR32MG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG13P632F512GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG13P632F512GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG13P Series

EFR32MG13P632F512GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c
Family	EFR32
Sub Family	EFR32MG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG13P632F512GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG13P632F512GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG13P632F512GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG13P632F512GM48

__EFR32MG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG13P632F512GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG13P632F512GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG13P Series

EFR32MG13P632F512GM51

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c
Family	EFR32
Sub Family	EFR32MG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG13P632F512GM51_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG13P632F512GM51_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG13P632F512GM51_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG13P632F512GM51

__EFR32MG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG13P632F512GM51_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG13P632F512GM51_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG13P Series

EFR32MG13P731F512GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c
Family	EFR32
Sub Family	EFR32MG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG13P731F512GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG13P731F512GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG13P731F512GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG13P731F512GM48

__EFR32MG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG13P731F512GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG13P731F512GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG13P Series

EFR32MG13P732F512GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c
Family	EFR32
Sub Family	EFR32MG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG13P732F512GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG13P732F512GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG13P732F512GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG13P732F512GM32

__EFR32MG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG13P732F512GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG13P732F512GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG13P Series

EFR32MG13P732F512GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c
Family	EFR32
Sub Family	EFR32MG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG13P732F512GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG13P732F512GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG13P732F512GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG13P732F512GM48

__EFR32MG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG13P732F512GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG13P732F512GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG13P Series

EFR32MG13P732F512GM51

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c
Family	EFR32
Sub Family	EFR32MG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG13P732F512GM51_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG13P732F512GM51_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG13P732F512GM51_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG13P732F512GM51

__EFR32MG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG13P732F512GM51_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG13P732F512GM51_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG13P Series

EFR32MG13P732F512IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c
Family	EFR32
Sub Family	EFR32MG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG13P732F512IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG13P732F512IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG13P732F512IM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG13P732F512IM32

__EFR32MG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG13P732F512IM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG13P732F512IM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG13P Series

EFR32MG13P732F512IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c
Family	EFR32
Sub Family	EFR32MG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG13P732F512IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG13P732F512IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG13P732F512IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG13P732F512IM48

__EFR32MG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG13P732F512IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG13P732F512IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG13P Series

EFR32MG13P733F512GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c
Family	EFR32
Sub Family	EFR32MG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG13P733F512GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG13P733F512GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG13P733F512GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG13P733F512GM48

__EFR32MG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG13P733F512GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG13P733F512GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG13P Series

EFR32MG13P733F512IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c
Family	EFR32
Sub Family	EFR32MG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG13P733F512IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG13P733F512IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG13P733F512IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG13P733F512IM48

__EFR32MG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG13P733F512IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG13P733F512IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG13P Series

EFR32MG13P832F512IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c
Family	EFR32
Sub Family	EFR32MG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG13P832F512IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG13P832F512IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG13P832F512IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG13P832F512IM48

__EFR32MG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG13P832F512IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG13P/Source/system_efr32mg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG13P832F512IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG13P Series

EFR32MG14P Series Family

[EFR32MG14P632F256GM32](#)

[EFR32MG14P632F256GM48](#)

[EFR32MG14P731F256GM48](#)

[EFR32MG14P732F256GM32](#)

[EFR32MG14P732F256GM48](#)

[EFR32MG14P732F256IM32](#)

[EFR32MG14P732F256IM48](#)

[EFR32MG14P733F256GM48](#)

[EFR32MG14P733F256IM48](#)

EFR32MG14P632F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c
Family	EFR32
Sub Family	EFR32MG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG14P632F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG14P632F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG14P632F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG14P632F256GM32

__EFR32MG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG14P632F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG14P632F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG14P Series

EFR32MG14P632F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c
Family	EFR32
Sub Family	EFR32MG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG14P632F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG14P632F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG14P632F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG14P632F256GM48

__EFR32MG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG14P632F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG14P632F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG14P Series

EFR32MG14P731F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c
Family	EFR32
Sub Family	EFR32MG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG14P731F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG14P731F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG14P731F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG14P731F256GM48

__EFR32MG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG14P731F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG14P731F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG14P Series

EFR32MG14P732F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c
Family	EFR32
Sub Family	EFR32MG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG14P732F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG14P732F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG14P732F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG14P732F256GM32

__EFR32MG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG14P732F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG14P732F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG14P Series

EFR32MG14P732F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c
Family	EFR32
Sub Family	EFR32MG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG14P732F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG14P732F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG14P732F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG14P732F256GM48

__EFR32MG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG14P732F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG14P732F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG14P Series

EFR32MG14P732F256IM32

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c
Family	EFR32
Sub Family	EFR32MG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG14P732F256IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG14P732F256IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG14P732F256IM32_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
EFR32MG14P732F256IM32	
__EFR32MG14P_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/EFR32MG14P732F256IM32_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/EFR32MG14P732F256IM32_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	EFR32MG14P Series

EFR32MG14P732F256IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c
Family	EFR32
Sub Family	EFR32MG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG14P732F256IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG14P732F256IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG14P732F256IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG14P732F256IM48

__EFR32MG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG14P732F256IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG14P732F256IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG14P Series

EFR32MG14P733F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c
Family	EFR32
Sub Family	EFR32MG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG14P733F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG14P733F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG14P733F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG14P733F256GM48

__EFR32MG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG14P733F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG14P733F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG14P Series

EFR32MG14P733F256IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c
Family	EFR32
Sub Family	EFR32MG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG14P733F256IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG14P733F256IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG14P733F256IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG14P733F256IM48

__EFR32MG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG14P733F256IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG14P/Source/system_efr32mg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG14P733F256IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG14P Series

EFR32MG1B Series Family

[EFR32MG1B131F256GM32](#)
[EFR32MG1B131F256GM48](#)
[EFR32MG1B132F256GJ43](#)
[EFR32MG1B132F256GM32](#)
[EFR32MG1B132F256GM48](#)
[EFR32MG1B231F256GM32](#)
[EFR32MG1B231F256GM48](#)
[EFR32MG1B232F256GJ43](#)
[EFR32MG1B232F256GM32](#)
[EFR32MG1B232F256GM48](#)
[EFR32MG1B232F256IM48](#)
[EFR32MG1B632F256GM32](#)
[EFR32MG1B632F256IM32](#)
[EFR32MG1B732F256GM32](#)
[EFR32MG1B732F256IM32](#)

EFR32MG1B131F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c
Family	EFR32
Sub Family	EFR32MG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1B131F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1B131F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1B131F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1B131F256GM32

__EFR32MG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1B131F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1B131F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1B Series

EFR32MG1B131F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c
Family	EFR32
Sub Family	EFR32MG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1B131F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1B131F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1B131F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1B131F256GM48

__EFR32MG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1B131F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1B131F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1B Series

EFR32MG1B132F256GJ43

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c
Family	EFR32
Sub Family	EFR32MG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1B132F256GJ43_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1B132F256GJ43_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1B132F256GJ43_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1B132F256GJ43

__EFR32MG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1B132F256GJ43_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1B132F256GJ43_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1B Series

EFR32MG1B132F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c
Family	EFR32
Sub Family	EFR32MG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1B132F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1B132F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1B132F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1B132F256GM32

__EFR32MG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1B132F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1B132F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1B Series

EFR32MG1B132F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c
Family	EFR32
Sub Family	EFR32MG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1B132F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1B132F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1B132F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1B132F256GM48

__EFR32MG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1B132F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1B132F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1B Series

EFR32MG1B231F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c
Family	EFR32
Sub Family	EFR32MG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1B231F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1B231F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1B231F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1B231F256GM32

__EFR32MG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1B231F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1B231F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1B Series

EFR32MG1B231F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c
Family	EFR32
Sub Family	EFR32MG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1B231F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1B231F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1B231F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1B231F256GM48

__EFR32MG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1B231F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1B231F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1B Series

EFR32MG1B232F256GJ43

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c
Family	EFR32
Sub Family	EFR32MG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1B232F256GJ43_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1B232F256GJ43_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1B232F256GJ43_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1B232F256GJ43

__EFR32MG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1B232F256GJ43_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1B232F256GJ43_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1B Series

EFR32MG1B232F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c
Family	EFR32
Sub Family	EFR32MG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1B232F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1B232F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1B232F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1B232F256GM32

__EFR32MG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1B232F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1B232F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1B Series

EFR32MG1B232F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c
Family	EFR32
Sub Family	EFR32MG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1B232F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1B232F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1B232F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1B232F256GM48

__EFR32MG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1B232F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1B232F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1B Series

EFR32MG1B232F256IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c
Family	EFR32
Sub Family	EFR32MG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1B232F256IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1B232F256IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1B232F256IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1B232F256IM48

__EFR32MG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1B232F256IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1B232F256IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1B Series

EFR32MG1B632F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c
Family	EFR32
Sub Family	EFR32MG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1B632F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1B632F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1B632F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1B632F256GM32

__EFR32MG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1B632F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1B632F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1B Series

EFR32MG1B632F256IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c
Family	EFR32
Sub Family	EFR32MG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1B632F256IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1B632F256IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1B632F256IM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1B632F256IM32

__EFR32MG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1B632F256IM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1B632F256IM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1B Series

EFR32MG1B732F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c
Family	EFR32
Sub Family	EFR32MG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1B732F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1B732F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1B732F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1B732F256GM32

__EFR32MG1B_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1B732F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1B732F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1B Series

EFR32MG1B732F256IM32

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c
Family	EFR32
Sub Family	EFR32MG1B Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1B732F256IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1B732F256IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1B732F256IM32_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
EFR32MG1B732F256IM32	
__EFR32MG1B_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/EFR32MG1B732F256IM32_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1B/Source/system_efr32mg1b.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/EFR32MG1B732F256IM32_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	EFR32MG1B Series

EFR32MG1P Series Family

[EFR32MG1P131F256GM48](#)
[EFR32MG1P132F256GJ43](#)
[EFR32MG1P132F256GM32](#)
[EFR32MG1P132F256GM48](#)
[EFR32MG1P132F256IM32](#)
[EFR32MG1P132F256IM48](#)
[EFR32MG1P133F256GM48](#)
[EFR32MG1P231F256GM48](#)
[EFR32MG1P232F256GJ43](#)
[EFR32MG1P232F256GM32](#)
[EFR32MG1P232F256GM48](#)
[EFR32MG1P232F256IM32](#)
[EFR32MG1P232F256IM48](#)
[EFR32MG1P233F256GM48](#)
[EFR32MG1P233F256IM48](#)
[EFR32MG1P632F256GM32](#)
[EFR32MG1P632F256IM32](#)
[EFR32MG1P732F256GM32](#)
[EFR32MG1P732F256IM32](#)

EFR32MG1P131F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P131F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P131F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P131F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1P131F256GM48

__EFR32MG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P131F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P131F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1P Series

EFR32MG1P132F256GJ43

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P132F256GJ43_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P132F256GJ43_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P132F256GJ43_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1P132F256GJ43

__EFR32MG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P132F256GJ43_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P132F256GJ43_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1P Series

EFR32MG1P132F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P132F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P132F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P132F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1P132F256GM32

__EFR32MG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P132F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P132F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1P Series

EFR32MG1P132F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P132F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P132F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P132F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1P132F256GM48

__EFR32MG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P132F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P132F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1P Series

EFR32MG1P132F256IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P132F256IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P132F256IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P132F256IM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1P132F256IM32

__EFR32MG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P132F256IM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P132F256IM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1P Series

EFR32MG1P132F256IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P132F256IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P132F256IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P132F256IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1P132F256IM48

__EFR32MG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P132F256IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P132F256IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1P Series

EFR32MG1P133F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P133F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P133F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P133F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1P133F256GM48

__EFR32MG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P133F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P133F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1P Series

EFR32MG1P231F256GM48

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P231F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P231F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P231F256GM48_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFR32MG1P231F256GM48
__EFR32MG1P_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P231F256GM48_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P231F256GM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG1P Series

EFR32MG1P232F256GJ43

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P232F256GJ43_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P232F256GJ43_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P232F256GJ43_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1P232F256GJ43

__EFR32MG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P232F256GJ43_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P232F256GJ43_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1P Series

EFR32MG1P232F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P232F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P232F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P232F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1P232F256GM32

__EFR32MG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P232F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P232F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1P Series

EFR32MG1P232F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P232F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P232F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P232F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1P232F256GM48

__EFR32MG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P232F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P232F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1P Series

EFR32MG1P232F256IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P232F256IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P232F256IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P232F256IM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1P232F256IM32

__EFR32MG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P232F256IM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P232F256IM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1P Series

EFR32MG1P232F256IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P232F256IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P232F256IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P232F256IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1P232F256IM48

__EFR32MG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P232F256IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P232F256IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1P Series

EFR32MG1P233F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P233F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P233F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P233F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1P233F256GM48

__EFR32MG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P233F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P233F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1P Series

EFR32MG1P233F256IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P233F256IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P233F256IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P233F256IM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1P233F256IM48

__EFR32MG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P233F256IM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P233F256IM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1P Series

EFR32MG1P632F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P632F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P632F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P632F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1P632F256GM32

__EFR32MG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P632F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P632F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1P Series

EFR32MG1P632F256IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P632F256IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P632F256IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P632F256IM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1P632F256IM32

__EFR32MG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P632F256IM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P632F256IM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1P Series

EFR32MG1P732F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P732F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P732F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P732F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1P732F256GM32

__EFR32MG1P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P732F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P732F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1P Series

EFR32MG1P732F256IM32

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
Family	EFR32
Sub Family	EFR32MG1P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1P732F256IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1P732F256IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1P732F256IM32_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFR32MG1P732F256IM32
__EFR32MG1P_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1P732F256IM32_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1P/Source/system_efr32mg1p.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1P732F256IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG1P Series

EFR32MG1V Series Family

[EFR32MG1V131F256GM32](#)

[EFR32MG1V131F256GM48](#)

[EFR32MG1V132F256GM32](#)

[EFR32MG1V132F256GM48](#)

EFR32MG1V131F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Source/system_efr32mg1v.c
Family	EFR32
Sub Family	EFR32MG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1V131F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1V131F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1V131F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1V131F256GM32

__EFR32MG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1V131F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Source/system_efr32mg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1V131F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1V Series

EFR32MG1V131F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Source/system_efr32mg1v.c
Family	EFR32
Sub Family	EFR32MG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1V131F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1V131F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1V131F256GM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM4
EFR32MG1V131F256GM48
__EFR32MG1V_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG1V131F256GM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Source/system_efr32mg1v.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG1V131F256GM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG1V Series
```

EFR32MG1V132F256GM32

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Source/system_efr32mg1v.c
Family	EFR32
Sub Family	EFR32MG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1V132F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1V132F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1V132F256GM32_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
EFR32MG1V132F256GM32
__EFR32MG1V_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1V132F256GM32_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Source/system_efr32mg1v.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1V132F256GM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG1V Series

EFR32MG1V132F256GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Source/system_efr32mg1v.c
Family	EFR32
Sub Family	EFR32MG1V Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG1V132F256GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG1V132F256GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG1V132F256GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32MG1V132F256GM48

__EFR32MG1V_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG1V132F256GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG1V/Source/system_efr32mg1v.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG1V132F256GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG1V Series

EFR32MG21 Series Family

[EFR32MG21A010F1024IM32](#)

[EFR32MG21A010F512IM32](#)

[EFR32MG21A010F768IM32](#)

[EFR32MG21A020F1024IM32](#)

[EFR32MG21A020F512IM32](#)

[EFR32MG21A020F768IM32](#)

[EFR32MG21B010F1024IM32](#)

[EFR32MG21B010F512IM32](#)

[EFR32MG21B010F768IM32](#)

[EFR32MG21B020F1024IM32](#)

[EFR32MG21B020F512IM32](#)

[EFR32MG21B020F768IM32](#)

[RM21Z000F1024IM32](#)

EFR32MG21A010F1024IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ EFR32MG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ EFR32MG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ EFR32MG21/Source/system_efr32mg21.c
Family	EFR32
Sub Family	EFR32MG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/ EFR32MG21A010F1024IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/ EFR32MG21A010F1024IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/ EFR32MG21A010F1024IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG21A010F1024IM32
__EFR32MG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG21A010F1024IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG21A010F1024IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG21 Series
```

EFR32MG21A010F512IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
Family	EFR32
Sub Family	EFR32MG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG21A010F512IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG21A010F512IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG21A010F512IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG21A010F512IM32
__EFR32MG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG21A010F512IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG21A010F512IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG21 Series
```

EFR32MG21A010F768IM32

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
Family	EFR32
Sub Family	EFR32MG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG21A010F768IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG21A010F768IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG21A010F768IM32_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFR32MG21A010F768IM32
__EFR32MG21_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x000BFFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG21A010F768IM32_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG21A010F768IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG21 Series

EFR32MG21A020F1024IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ EFR32MG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ EFR32MG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ EFR32MG21/Source/system_efr32mg21.c
Family	EFR32
Sub Family	EFR32MG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/ EFR32MG21A020F1024IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/ EFR32MG21A020F1024IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/ EFR32MG21A020F1024IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG21A020F1024IM32
__EFR32MG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG21A020F1024IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG21A020F1024IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG21 Series
```

EFR32MG21A020F512IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
Family	EFR32
Sub Family	EFR32MG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG21A020F512IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG21A020F512IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG21A020F512IM32_Vectors.s

Preprocessor Definitions

```

ARM_MATH_ARMV8MML
EFR32MG21A020F512IM32
__EFR32MG21_Series_SUBFAMILY
__EFR32_FAMILY

```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```

DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG21A020F512IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG21A020F512IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG21 Series

```

EFR32MG21A020F768IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
Family	EFR32
Sub Family	EFR32MG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG21A020F768IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG21A020F768IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG21A020F768IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG21A020F768IM32
__EFR32MG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000BFFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG21A020F768IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG21A020F768IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG21 Series
```

EFR32MG21B010F1024IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
Family	EFR32
Sub Family	EFR32MG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG21B010F1024IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG21B010F1024IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG21B010F1024IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG21B010F1024IM32
__EFR32MG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG21B010F1024IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG21B010F1024IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG21 Series
```

EFR32MG21B010F512IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
Family	EFR32
Sub Family	EFR32MG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG21B010F512IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG21B010F512IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG21B010F512IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG21B010F512IM32
__EFR32MG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG21B010F512IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG21B010F512IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG21 Series
```


EFR32MG21B010F768IM32

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
Family	EFR32
Sub Family	EFR32MG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG21B010F768IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG21B010F768IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG21B010F768IM32_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
EFR32MG21B010F768IM32
__EFR32MG21_Series_SUBFAMILY
__EFR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x000BFFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG21B010F768IM32_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG21B010F768IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG21 Series

EFR32MG21B020F1024IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
Family	EFR32
Sub Family	EFR32MG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG21B020F1024IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG21B020F1024IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG21B020F1024IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG21B020F1024IM32
__EFR32MG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG21B020F1024IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG21B020F1024IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG21 Series
```

EFR32MG21B020F512IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
Family	EFR32
Sub Family	EFR32MG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG21B020F512IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG21B020F512IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG21B020F512IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG21B020F512IM32
__EFR32MG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG21B020F512IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG21B020F512IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG21 Series
```

EFR32MG21B020F768IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
Family	EFR32
Sub Family	EFR32MG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG21B020F768IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG21B020F768IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG21B020F768IM32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG21B020F768IM32
__EFR32MG21_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000BFFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG21B020F768IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG21B020F768IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG21 Series
```

RM21Z000F1024IM32

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
Family	EFR32
Sub Family	EFR32MG21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/RM21Z000F1024IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/RM21Z000F1024IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/RM21Z000F1024IM32_Vectors.s

Preprocessor Definitions	
ARM_MATH_ARMV8MML	
RM21Z000F1024IM32	
__EFR32MG21_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/RM21Z000F1024IM32_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG21/Source/system_efr32mg21.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/RM21Z000F1024IM32_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	EFR32MG21 Series

EFR32MG22 Series Family

[EFR32MG22A224F512IM40](#)

[EFR32MG22C224F512GN32](#)

[EFR32MG22C224F512IM32](#)

[EFR32MG22C224F512IM40](#)

EFR32MG22A224F512IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Source/system_efr32mg22.c
Family	EFR32
Sub Family	EFR32MG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG22A224F512IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG22A224F512IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG22A224F512IM40_Vectors.s

Preprocessor Definitions

```

ARM_MATH_ARMV8MML
EFR32MG22A224F512IM40
__EFR32MG22_Series_SUBFAMILY
__EFR32_FAMILY

```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```

DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG22A224F512IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Source/system_efr32mg22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG22A224F512IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG22 Series

```

EFR32MG22C224F512GN32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Source/system_efr32mg22.c
Family	EFR32
Sub Family	EFR32MG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG22C224F512GN32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG22C224F512GN32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG22C224F512GN32_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG22C224F512GN32
__EFR32MG22_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG22C224F512GN32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Source/system_efr32mg22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG22C224F512GN32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG22 Series
```


EFR32MG22C224F512IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Source/system_efr32mg22.c
Family	EFR32
Sub Family	EFR32MG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG22C224F512IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG22C224F512IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG22C224F512IM32_Vectors.s

Preprocessor Definitions

```

ARM_MATH_ARMV8MML
EFR32MG22C224F512IM32
__EFR32MG22_Series_SUBFAMILY
__EFR32_FAMILY

```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```

DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG22C224F512IM32_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Source/system_efr32mg22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG22C224F512IM32_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG22 Series

```

EFR32MG22C224F512IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Source/system_efr32mg22.c
Family	EFR32
Sub Family	EFR32MG22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG22C224F512IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG22C224F512IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG22C224F512IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG22C224F512IM40
__EFR32MG22_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG22C224F512IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG22/Source/system_efr32mg22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG22C224F512IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG22 Series
```

EFR32MG24 Series Family

[EFR32MG24A010F1024IM40](#)
[EFR32MG24A010F1024IM48](#)
[EFR32MG24A010F1536GM40](#)
[EFR32MG24A010F1536GM48](#)
[EFR32MG24A010F1536IM40](#)
[EFR32MG24A010F1536IM48](#)
[EFR32MG24A020F1024IM40](#)
[EFR32MG24A020F1024IM48](#)
[EFR32MG24A020F1536GM40](#)
[EFR32MG24A020F1536GM48](#)
[EFR32MG24A020F1536IM40](#)
[EFR32MG24A020F1536IM48](#)
[EFR32MG24A021F1024IM40](#)
[EFR32MG24A110F1024IM48](#)
[EFR32MG24A110F1536GM48](#)
[EFR32MG24A111F1536GM48](#)
[EFR32MG24A120F1536GM48](#)
[EFR32MG24A121F1536GM48](#)
[EFR32MG24A610F1536IM40](#)
[EFR32MG24A620F1536IM40](#)

EFR32MG24A010F1024IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A010F1024IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A010F1024IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A010F1024IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG24A010F1024IM40
__EFR32MG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x080FFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG24A010F1024IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG24A010F1024IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG24 Series
```

EFR32MG24A010F1024IM48

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A010F1024IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A010F1024IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A010F1024IM48_Vectors.s

Preprocessor Definitions	
ARM_MATH_ARMV8MML	
EFR32MG24A010F1024IM48	
__EFR32MG24_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x080FFFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/EFR32MG24A010F1024IM48_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/EFR32MG24A010F1024IM48_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	EFR32MG24 Series

EFR32MG24A010F1536GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A010F1536GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A010F1536GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A010F1536GM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG24A010F1536GM40
__EFR32MG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG24A010F1536GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG24A010F1536GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG24 Series
```

EFR32MG24A010F1536GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A010F1536GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A010F1536GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A010F1536GM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG24A010F1536GM48
__EFR32MG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG24A010F1536GM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG24A010F1536GM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG24 Series
```

EFR32MG24A010F1536IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A010F1536IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A010F1536IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A010F1536IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG24A010F1536IM40
__EFR32MG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2002FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG24A010F1536IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG24A010F1536IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG24 Series
```


EFR32MG24A010F1536IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A010F1536IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A010F1536IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A010F1536IM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG24A010F1536IM48
__EFR32MG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2002FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG24A010F1536IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG24A010F1536IM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG24 Series
```

EFR32MG24A020F1024IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/ EFR32MG24A020F1024IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/ EFR32MG24A020F1024IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/ EFR32MG24A020F1024IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG24A020F1024IM40
__EFR32MG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x080FFFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG24A020F1024IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG24A020F1024IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG24 Series
```

EFR32MG24A020F1024IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A020F1024IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A020F1024IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A020F1024IM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG24A020F1024IM48
__EFR32MG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x080FFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG24A020F1024IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG24A020F1024IM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG24 Series
```

EFR32MG24A020F1536GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A020F1536GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A020F1536GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A020F1536GM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG24A020F1536GM40
__EFR32MG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG24A020F1536GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG24A020F1536GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG24 Series
```

EFR32MG24A020F1536GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A020F1536GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A020F1536GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A020F1536GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

EFR32MG24A020F1536GM48

__EFR32MG24_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG24A020F1536GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG24A020F1536GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG24 Series

EFR32MG24A020F1536IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A020F1536IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A020F1536IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A020F1536IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG24A020F1536IM40
__EFR32MG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2002FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG24A020F1536IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG24A020F1536IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG24 Series
```

EFR32MG24A020F1536IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A020F1536IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A020F1536IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A020F1536IM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG24A020F1536IM48
__EFR32MG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2002FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG24A020F1536IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG24A020F1536IM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG24 Series
```

EFR32MG24A021F1024IM40

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A021F1024IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A021F1024IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A021F1024IM40_Vectors.s

Preprocessor Definitions	
ARM_MATH_ARMV8MML	
EFR32MG24A021F1024IM40	
__EFR32MG24_Series_SUBFAMILY	
__EFR32_FAMILY	

Memory Segments	
FLASH	0x08000000 - 0x080FFFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/EFR32MG24A021F1024IM40_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/EFR32MG24A021F1024IM40_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	EFR32MG24 Series

EFR32MG24A110F1024IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A110F1024IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A110F1024IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A110F1024IM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG24A110F1024IM48
__EFR32MG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x080FFFFFF
RAM	0x20000000 - 0x2001FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG24A110F1024IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG24A110F1024IM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG24 Series
```

EFR32MG24A110F1536GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A110F1536GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A110F1536GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A110F1536GM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG24A110F1536GM48
__EFR32MG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG24A110F1536GM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG24A110F1536GM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG24 Series
```

EFR32MG24A111F1536GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A111F1536GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A111F1536GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A111F1536GM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG24A111F1536GM48
__EFR32MG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG24A111F1536GM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG24A111F1536GM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG24 Series
```

EFR32MG24A120F1536GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A120F1536GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A120F1536GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A120F1536GM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG24A120F1536GM48
__EFR32MG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG24A120F1536GM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG24A120F1536GM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG24 Series
```

EFR32MG24A121F1536GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A121F1536GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A121F1536GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A121F1536GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

EFR32MG24A121F1536GM48

__EFR32MG24_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32MG24A121F1536GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32MG24A121F1536GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32MG24 Series

EFR32MG24A610F1536IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A610F1536IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A610F1536IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A610F1536IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG24A610F1536IM40
__EFR32MG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG24A610F1536IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG24A610F1536IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG24 Series
```

EFR32MG24A620F1536IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
Family	EFR32
Sub Family	EFR32MG24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32MG24A620F1536IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32MG24A620F1536IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32MG24A620F1536IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32MG24A620F1536IM40
__EFR32MG24_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32MG24A620F1536IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32MG24/Source/system_efr32mg24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32MG24A620F1536IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32MG24 Series
```

EFR32ZG13L Series Family

[EFR32ZG13L231F512GM32](#)

[EFR32ZG13L231F512IM32](#)

EFR32ZG13L231F512GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13L/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13L/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13L/Source/system_efr32zg13l.c
Family	EFR32
Sub Family	EFR32ZG13L Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG13L231F512GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG13L231F512GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG13L231F512GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32ZG13L231F512GM32

__EFR32ZG13L_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13L/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13L/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32ZG13L231F512GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13L/Source/system_efr32zg13l.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32ZG13L231F512GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32ZG13L Series

EFR32ZG13L231F512IM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13L/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13L/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13L/Source/system_efr32zg13l.c
Family	EFR32
Sub Family	EFR32ZG13L Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG13L231F512IM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG13L231F512IM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG13L231F512IM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32ZG13L231F512IM32

__EFR32ZG13L_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13L/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13L/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32ZG13L231F512IM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13L/Source/system_efr32zg13l.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32ZG13L231F512IM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32ZG13L Series

EFR32ZG13P Series Family

[EFR32ZG13P231F512GM64](#)

[EFR32ZG13P531F512GM32](#)

[EFR32ZG13P531F512GM48](#)

EFR32ZG13P231F512GM64

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Source/system_efr32zg13p.c
Family	EFR32
Sub Family	EFR32ZG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG13P231F512GM64_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG13P231F512GM64_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG13P231F512GM64_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32ZG13P231F512GM64

__EFR32ZG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32ZG13P231F512GM64_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Source/system_efr32zg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32ZG13P231F512GM64_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32ZG13P Series

EFR32ZG13P531F512GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Source/system_efr32zg13p.c
Family	EFR32
Sub Family	EFR32ZG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG13P531F512GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG13P531F512GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG13P531F512GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32ZG13P531F512GM32

__EFR32ZG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32ZG13P531F512GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Source/system_efr32zg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32ZG13P531F512GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32ZG13P Series

EFR32ZG13P531F512GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Source/system_efr32zg13p.c
Family	EFR32
Sub Family	EFR32ZG13P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG13P531F512GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG13P531F512GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG13P531F512GM48_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32ZG13P531F512GM48

__EFR32ZG13P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32ZG13P531F512GM48_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13P/Source/system_efr32zg13p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32ZG13P531F512GM48_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32ZG13P Series

EFR32ZG13S Series Family

[EFR32ZG13S231F512GM32](#)

EFR32ZG13S231F512GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13S/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13S/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13S/Source/system_efr32zg13s.c
Family	EFR32
Sub Family	EFR32ZG13S Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG13S231F512GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG13S231F512GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG13S231F512GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32ZG13S231F512GM32

__EFR32ZG13S_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13S/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13S/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32ZG13S231F512GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG13S/Source/system_efr32zg13s.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32ZG13S231F512GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32ZG13S Series

EFR32ZG14P Series Family

[EFR32ZG14P231F256GM32](#)

[EFR32ZG14P731F256GM32](#)

EFR32ZG14P231F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG14P/Source/system_efr32zg14p.c
Family	EFR32
Sub Family	EFR32ZG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG14P231F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG14P231F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG14P231F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32ZG14P231F256GM32

__EFR32ZG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32ZG14P231F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG14P/Source/system_efr32zg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32ZG14P231F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32ZG14P Series

EFR32ZG14P731F256GM32

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG14P/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG14P/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG14P/Source/system_efr32zg14p.c
Family	EFR32
Sub Family	EFR32ZG14P Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG14P731F256GM32_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG14P731F256GM32_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG14P731F256GM32_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EFR32ZG14P731F256GM32

__EFR32ZG14P_Series_SUBFAMILY

__EFR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG14P/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG14P/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/EFR32ZG14P731F256GM32_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG14P/Source/system_efr32zg14p.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/EFR32ZG14P731F256GM32_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=EFR32ZG14P Series

EFR32ZG23 Series Family

[EFR32ZG23A010F512GM40](#)

[EFR32ZG23A010F512GM48](#)

[EFR32ZG23A020F512GM40](#)

[EFR32ZG23A020F512GM48](#)

[EFR32ZG23B010F512IM40](#)

[EFR32ZG23B010F512IM48](#)

[EFR32ZG23B011F512IM40](#)

[EFR32ZG23B020F512IM40](#)

[EFR32ZG23B020F512IM48](#)

[EFR32ZG23B021F512IM40](#)

EFR32ZG23A010F512GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
Family	EFR32
Sub Family	EFR32ZG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG23A010F512GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG23A010F512GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG23A010F512GM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32ZG23A010F512GM40
__EFR32ZG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32ZG23A010F512GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32ZG23A010F512GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32ZG23 Series
```

EFR32ZG23A010F512GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
Family	EFR32
Sub Family	EFR32ZG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG23A010F512GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG23A010F512GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG23A010F512GM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32ZG23A010F512GM48
__EFR32ZG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32ZG23A010F512GM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32ZG23A010F512GM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32ZG23 Series
```

EFR32ZG23A020F512GM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
Family	EFR32
Sub Family	EFR32ZG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG23A020F512GM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG23A020F512GM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG23A020F512GM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32ZG23A020F512GM40
__EFR32ZG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32ZG23A020F512GM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32ZG23A020F512GM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32ZG23 Series
```

EFR32ZG23A020F512GM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
Family	EFR32
Sub Family	EFR32ZG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG23A020F512GM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG23A020F512GM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG23A020F512GM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32ZG23A020F512GM48
__EFR32ZG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32ZG23A020F512GM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32ZG23A020F512GM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32ZG23 Series
```


EFR32ZG23B010F512IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
Family	EFR32
Sub Family	EFR32ZG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG23B010F512IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG23B010F512IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG23B010F512IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32ZG23B010F512IM40
__EFR32ZG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32ZG23B010F512IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32ZG23B010F512IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32ZG23 Series
```

EFR32ZG23B010F512IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
Family	EFR32
Sub Family	EFR32ZG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG23B010F512IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG23B010F512IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG23B010F512IM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32ZG23B010F512IM48
__EFR32ZG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32ZG23B010F512IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32ZG23B010F512IM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32ZG23 Series
```

EFR32ZG23B011F512IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
Family	EFR32
Sub Family	EFR32ZG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG23B011F512IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG23B011F512IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG23B011F512IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32ZG23B011F512IM40
__EFR32ZG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32ZG23B011F512IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32ZG23B011F512IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32ZG23 Series
```

EFR32ZG23B020F512IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
Family	EFR32
Sub Family	EFR32ZG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG23B020F512IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG23B020F512IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG23B020F512IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32ZG23B020F512IM40
__EFR32ZG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32ZG23B020F512IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32ZG23B020F512IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32ZG23 Series
```

EFR32ZG23B020F512IM48

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
Family	EFR32
Sub Family	EFR32ZG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG23B020F512IM48_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG23B020F512IM48_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG23B020F512IM48_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32ZG23B020F512IM48
__EFR32ZG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32ZG23B020F512IM48_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32ZG23B020F512IM48_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32ZG23 Series
```

EFR32ZG23B021F512IM40

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
Family	EFR32
Sub Family	EFR32ZG23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/EFR32ZG23B021F512IM40_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/EFR32ZG23B021F512IM40_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/EFR32ZG23B021F512IM40_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
EFR32ZG23B021F512IM40
__EFR32ZG23_Series_SUBFAMILY
__EFR32_FAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/EFR32ZG23B021F512IM40_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/EFR32ZG23/Source/system_efr32zg23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/EFR32ZG23B021F512IM40_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=EFR32ZG23 Series
```

MGM1 Series Family

[MGM111A256V2](#)

[MGM111E256V2](#)

MGM111A256V2

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM1/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM1/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM1/Source/system_mgm1.c
Family	EFR32
Sub Family	MGM1 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM111A256V2_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM111A256V2_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM111A256V2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MGM111A256V2

__EFR32_FAMILY

__MGM1_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM1/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM1/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM111A256V2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM1/Source/system_mgm1.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM111A256V2_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM1 Series

MGM111E256V2

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM1/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM1/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM1/Source/system_mgm1.c
Family	EFR32
Sub Family	MGM1 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM111E256V2_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM111E256V2_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM111E256V2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MGM111E256V2

__EFR32_FAMILY

__MGM1_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007BFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM1/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM1/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOP2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM111E256V2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM1/Source/system_mgm1.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM111E256V2_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM1 Series

MGM12 Series Family

[MGM12P02F1024GA](#)

[MGM12P02F1024GE](#)

[MGM12P22F1024GA](#)

[MGM12P22F1024GE](#)

[MGM12P32F1024GA](#)

[MGM12P32F1024GE](#)

MGM12P02F1024GA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Source/system_mgm12.c
Family	EFR32
Sub Family	MGM12 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM12P02F1024GA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM12P02F1024GA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM12P02F1024GA_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MGM12P02F1024GA

__EFR32_FAMILY

__MGM12_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM12P02F1024GA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Source/system_mgm12.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM12P02F1024GA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM12 Series

MGM12P02F1024GE

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Source/system_mgm12.c
Family	EFR32
Sub Family	MGM12 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM12P02F1024GE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM12P02F1024GE_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM12P02F1024GE_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MGM12P02F1024GE

__EFR32_FAMILY

__MGM12_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM12P02F1024GE_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Source/system_mgm12.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM12P02F1024GE_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM12 Series

MGM12P22F1024GA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Source/system_mgm12.c
Family	EFR32
Sub Family	MGM12 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM12P22F1024GA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM12P22F1024GA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM12P22F1024GA_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MGM12P22F1024GA

__EFR32_FAMILY

__MGM12_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM12P22F1024GA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Source/system_mgm12.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM12P22F1024GA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM12 Series

MGM12P22F1024GE

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Source/system_mgm12.c
Family	EFR32
Sub Family	MGM12 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM12P22F1024GE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM12P22F1024GE_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM12P22F1024GE_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM4
MGM12P22F1024GE
__EFR32_FAMILY
__MGM12_Series_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/MGM12P22F1024GE_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Source/system_mgm12.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/MGM12P22F1024GE_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=MGM12 Series
```

MGM12P32F1024GA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Source/system_mgm12.c
Family	EFR32
Sub Family	MGM12 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM12P32F1024GA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM12P32F1024GA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM12P32F1024GA_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MGM12P32F1024GA

__EFR32_FAMILY

__MGM12_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM12P32F1024GA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Source/system_mgm12.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM12P32F1024GA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM12 Series

MGM12P32F1024GE

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Source/system_mgm12.c
Family	EFR32
Sub Family	MGM12 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM12P32F1024GE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM12P32F1024GE_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM12P32F1024GE_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MGM12P32F1024GE

__EFR32_FAMILY

__MGM12_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM12P32F1024GE_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM12/Source/system_mgm12.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM12P32F1024GE_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM12 Series

MGM13 Series Family

[MGM13P02F512GA](#)

[MGM13P02F512GE](#)

[MGM13P12F512GA](#)

[MGM13P12F512GE](#)

[MGM13S02F512GA](#)

[MGM13S02F512GN](#)

[MGM13S12F512GA](#)

[MGM13S12F512GN](#)

MGM13P02F512GA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Source/system_mgm13.c
Family	EFR32
Sub Family	MGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM13P02F512GA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM13P02F512GA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM13P02F512GA_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MGM13P02F512GA

__EFR32_FAMILY

__MGM13_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM13P02F512GA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Source/system_mgm13.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM13P02F512GA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM13 Series

MGM13P02F512GE

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Source/system_mgm13.c
Family	EFR32
Sub Family	MGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM13P02F512GE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM13P02F512GE_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM13P02F512GE_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MGM13P02F512GE

__EFR32_FAMILY

__MGM13_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM13P02F512GE_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Source/system_mgm13.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM13P02F512GE_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM13 Series

MGM13P12F512GA

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Source/system_mgm13.c
Family	EFR32
Sub Family	MGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM13P12F512GA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM13P12F512GA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM13P12F512GA_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
MGM13P12F512GA	
__EFR32_FAMILY	
__MGM13_Series_SUBFAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/MGM13P12F512GA_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Source/system_mgm13.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/MGM13P12F512GA_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	MGM13 Series

MGM13P12F512GE

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Source/system_mgm13.c
Family	EFR32
Sub Family	MGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM13P12F512GE_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM13P12F512GE_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM13P12F512GE_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MGM13P12F512GE

__EFR32_FAMILY

__MGM13_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM13P12F512GE_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Source/system_mgm13.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM13P12F512GE_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM13 Series

MGM13S02F512GA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Source/system_mgm13.c
Family	EFR32
Sub Family	MGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM13S02F512GA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM13S02F512GA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM13S02F512GA_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MGM13S02F512GA

__EFR32_FAMILY

__MGM13_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM13S02F512GA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Source/system_mgm13.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM13S02F512GA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM13 Series

MGM13S02F512GN

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Source/system_mgm13.c
Family	EFR32
Sub Family	MGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM13S02F512GN_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM13S02F512GN_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM13S02F512GN_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
MGM13S02F512GN
__EFR32_FAMILY
__MGM13_Series_SUBFAMILY

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM13S02F512GN_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Source/system_mgm13.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM13S02F512GN_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=MGM13 Series

MGM13S12F512GA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Source/system_mgm13.c
Family	EFR32
Sub Family	MGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM13S12F512GA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM13S12F512GA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM13S12F512GA_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MGM13S12F512GA

__EFR32_FAMILY

__MGM13_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM13S12F512GA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Source/system_mgm13.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM13S12F512GA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM13 Series

MGM13S12F512GN

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Source/system_mgm13.c
Family	EFR32
Sub Family	MGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM13S12F512GN_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM13S12F512GN_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM13S12F512GN_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MGM13S12F512GN

__EFR32_FAMILY

__MGM13_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM13S12F512GN_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM13/Source/system_mgm13.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM13S12F512GN_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM13 Series

MGM21 Series Family

[MGM210L022JIF](#)

[MGM210L022JNF](#)

[MGM210LA22JIF](#)

[MGM210LA22JNF](#)

[MGM210P022JIA](#)

[MGM210P032JIA](#)

[MGM210PA22JIA](#)

[MGM210PA32JIA](#)

[MGM210PB22JIA](#)

[MGM210PB32JIA](#)

MGM210L022JIF

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c
Family	EFR32
Sub Family	MGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM210L022JIF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM210L022JIF_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM210L022JIF_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

MGM210L022JIF

__EFR32_FAMILY

__MGM21_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM210L022JIF_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM210L022JIF_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM21 Series

MGM210L022JNF

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c
Family	EFR32
Sub Family	MGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM210L022JNF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM210L022JNF_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM210L022JNF_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
MGM210L022JNF
__EFR32_FAMILY
__MGM21_Series_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/MGM210L022JNF_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/MGM210L022JNF_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=MGM21 Series
```

MGM210LA22JIF

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c
Family	EFR32
Sub Family	MGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM210LA22JIF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM210LA22JIF_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM210LA22JIF_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
MGM210LA22JIF
__EFR32_FAMILY
__MGM21_Series_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/MGM210LA22JIF_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/MGM210LA22JIF_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=MGM21 Series
```

MGM210LA22JNF

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c
Family	EFR32
Sub Family	MGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM210LA22JNF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM210LA22JNF_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM210LA22JNF_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
MGM210LA22JNF
__EFR32_FAMILY
__MGM21_Series_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/MGM210LA22JNF_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/MGM210LA22JNF_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=MGM21 Series
```

MGM210P022JIA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c
Family	EFR32
Sub Family	MGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM210P022JIA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM210P022JIA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM210P022JIA_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
MGM210P022JIA
__EFR32_FAMILY
__MGM21_Series_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/MGM210P022JIA_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/MGM210P022JIA_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=MGM21 Series
```

MGM210P032JIA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c
Family	EFR32
Sub Family	MGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM210P032JIA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM210P032JIA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM210P032JIA_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
MGM210P032JIA
__EFR32_FAMILY
__MGM21_Series_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/MGM210P032JIA_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/MGM210P032JIA_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=MGM21 Series
```


MGM210PA22JIA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c
Family	EFR32
Sub Family	MGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM210PA22JIA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM210PA22JIA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM210PA22JIA_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

MGM210PA22JIA

__EFR32_FAMILY

__MGM21_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM210PA22JIA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM210PA22JIA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM21 Series

MGM210PA32JIA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c
Family	EFR32
Sub Family	MGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM210PA32JIA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM210PA32JIA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM210PA32JIA_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

MGM210PA32JIA

__EFR32_FAMILY

__MGM21_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM210PA32JIA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM210PA32JIA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM21 Series

MGM210PB22JIA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c
Family	EFR32
Sub Family	MGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM210PB22JIA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM210PB22JIA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM210PB22JIA_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
MGM210PB22JIA
__EFR32_FAMILY
__MGM21_Series_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/MGM210PB22JIA_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/MGM210PB22JIA_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=MGM21 Series
```

MGM210PB32JIA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c
Family	EFR32
Sub Family	MGM21 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM210PB32JIA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM210PB32JIA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM210PB32JIA_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

MGM210PB32JIA

__EFR32_FAMILY

__MGM21_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x20000000 - 0x20017FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM210PB32JIA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM21/Source/system_mgm21.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM210PB32JIA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM21 Series

MGM22 Series Family

[MGM220PC22HNA](#)

MGM220PC22HNA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM22/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM22/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM22/Source/system_mgm22.c
Family	EFR32
Sub Family	MGM22 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM220PC22HNA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM220PC22HNA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM220PC22HNA_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
MGM220PC22HNA
__EFR32_FAMILY
__MGM22_Series_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM22/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM22/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/MGM220PC22HNA_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM22/Source/system_mgm22.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/MGM220PC22HNA_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=MGM22 Series
```

MGM24 Series Family

[MGM240L022RNF](#)

[MGM240L022VNF](#)

[MGM240PA22VNA](#)

[MGM240PA32VNA](#)

[MGM240PA32VNN](#)

[MGM240PB22VNA](#)

[MGM240PB32VNA](#)

[MGM240PB32VNN](#)

[MGM240SA22VNA](#)

[MGM240SB22VNA](#)

MGM240L022RNF

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c
Family	EFR32
Sub Family	MGM24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM240L022RNF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM240L022RNF_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM240L022RNF_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

MGM240L022RNF

__EFR32_FAMILY

__MGM24_Series_SUBFAMILY

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x20027FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM240L022RNF_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM240L022RNF_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM24 Series

MGM240L022VNF

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c
Family	EFR32
Sub Family	MGM24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM240L022VNF_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM240L022VNF_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM240L022VNF_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
MGM240L022VNF
__EFR32_FAMILY
__MGM24_Series_SUBFAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/MGM240L022VNF_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/MGM240L022VNF_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=MGM24 Series
```

MGM240PA22VNA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c
Family	EFR32
Sub Family	MGM24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM240PA22VNA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM240PA22VNA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM240PA22VNA_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

MGM240PA22VNA

__EFR32_FAMILY

__MGM24_Series_SUBFAMILY

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM240PA22VNA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM240PA22VNA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM24 Series

MGM240PA32VNA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c
Family	EFR32
Sub Family	MGM24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM240PA32VNA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM240PA32VNA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM240PA32VNA_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
MGM240PA32VNA
__EFR32_FAMILY
__MGM24_Series_SUBFAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/MGM240PA32VNA_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/MGM240PA32VNA_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=MGM24 Series
```

MGM240PA32VNN

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c
Family	EFR32
Sub Family	MGM24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM240PA32VNN_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM240PA32VNN_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM240PA32VNN_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

MGM240PA32VNN

__EFR32_FAMILY

__MGM24_Series_SUBFAMILY

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM240PA32VNN_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM240PA32VNN_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM24 Series

MGM240PB22VNA

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c
Family	EFR32
Sub Family	MGM24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM240PB22VNA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM240PB22VNA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM240PB22VNA_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
MGM240PB22VNA
__EFR32_FAMILY
__MGM24_Series_SUBFAMILY

Memory Segments	
FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM240PB22VNA_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM240PB22VNA_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=MGM24 Series

MGM240PB32VNA

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c
Family	EFR32
Sub Family	MGM24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM240PB32VNA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM240PB32VNA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM240PB32VNA_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
MGM240PB32VNA
__EFR32_FAMILY
__MGM24_Series_SUBFAMILY

Memory Segments	
FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM240PB32VNA_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM240PB32VNA_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=MGM24 Series

MGM240PB32VNN

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c
Family	EFR32
Sub Family	MGM24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM240PB32VNN_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM240PB32VNN_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM240PB32VNN_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

MGM240PB32VNN

__EFR32_FAMILY

__MGM24_Series_SUBFAMILY

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM240PB32VNN_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM240PB32VNN_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM24 Series

MGM240SA22VNA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c
Family	EFR32
Sub Family	MGM24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM240SA22VNA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM240SA22VNA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM240SA22VNA_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
MGM240SA22VNA
__EFR32_FAMILY
__MGM24_Series_SUBFAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/MGM240SA22VNA_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/MGM240SA22VNA_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=MGM24 Series
```


MGM240SB22VNA

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c
Family	EFR32
Sub Family	MGM24 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/MGM240SB22VNA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/MGM240SB22VNA_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/MGM240SB22VNA_Vectors.s

Preprocessor Definitions

ARM_MATH_ARMV8MML

MGM240SB22VNA

__EFR32_FAMILY

__MGM24_Series_SUBFAMILY

Memory Segments

FLASH	0x08000000 - 0x0817FFFF
RAM	0x20000000 - 0x2003FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/MGM240SB22VNA_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/MGM24/Source/system_mgm24.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/MGM240SB22VNA_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=MGM24 Series

WGM160 Series Family

[WGM160P022KGA2](#)

[WGM160P022KGA3](#)

[WGM160P022KGN2](#)

[WGM160P022KGN3](#)

[WGM160PX22KGA2](#)

[WGM160PX22KGA3](#)

[WGM160PX22KGN2](#)

[WGM160PX22KGN3](#)

WGM160P022KGA2

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Source/system_wgm160.c
Family	EFR32
Sub Family	WGM160 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/WGM160P022KGA2_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/WGM160P022KGA2_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/WGM160P022KGA2_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
WGM160P022KGA2	
__EFR32_FAMILY	
__WGM160_Series_SUBFAMILY	

Memory Segments	
FLASH	0x00000000 - 0x001FFFFFF
RAM	0x20000000 - 0x2007FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/WGM160P022KGA2_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Source/system_wgm160.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/WGM160P022KGA2_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	WGM160 Series

WGM160P022KGA3

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Source/system_wgm160.c
Family	EFR32
Sub Family	WGM160 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/WGM160P022KGA3_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/WGM160P022KGA3_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/WGM160P022KGA3_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

WGM160P022KGA3

__EFR32_FAMILY

__WGM160_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
RAM	0x20000000 - 0x2007FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/WGM160P022KGA3_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Source/system_wgm160.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/WGM160P022KGA3_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=WGM160 Series

WGM160P022KGN2

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Source/system_wgm160.c
Family	EFR32
Sub Family	WGM160 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/WGM160P022KGN2_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/WGM160P022KGN2_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/WGM160P022KGN2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

WGM160P022KGN2

__EFR32_FAMILY

__WGM160_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
RAM	0x20000000 - 0x2007FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/WGM160P022KGN2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Source/system_wgm160.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/WGM160P022KGN2_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=WGM160 Series

WGM160P022KGN3

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Source/system_wgm160.c
Family	EFR32
Sub Family	WGM160 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/WGM160P022KGN3_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/WGM160P022KGN3_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/WGM160P022KGN3_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

WGM160P022KGN3

__EFR32_FAMILY

__WGM160_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
RAM	0x20000000 - 0x2007FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/WGM160P022KGN3_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Source/system_wgm160.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/WGM160P022KGN3_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=WGM160 Series

WGM160PX22KGA2

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Source/system_wgm160.c
Family	EFR32
Sub Family	WGM160 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/WGM160PX22KGA2_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/WGM160PX22KGA2_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/WGM160PX22KGA2_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
WGM160PX22KGA2	
__EFR32_FAMILY	
__WGM160_Series_SUBFAMILY	

Memory Segments	
FLASH	0x00000000 - 0x001FFFFFF
RAM	0x20000000 - 0x2007FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/WGM160PX22KGA2_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Source/system_wgm160.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/WGM160PX22KGA2_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	WGM160 Series

WGM160PX22KGA3

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Source/system_wgm160.c
Family	EFR32
Sub Family	WGM160 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/WGM160PX22KGA3_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/WGM160PX22KGA3_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/WGM160PX22KGA3_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
WGM160PX22KGA3	
__EFR32_FAMILY	
__WGM160_Series_SUBFAMILY	

Memory Segments	
FLASH	0x00000000 - 0x001FFFFFF
RAM	0x20000000 - 0x2007FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/WGM160PX22KGA3_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Source/system_wgm160.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/WGM160PX22KGA3_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	WGM160 Series

WGM160PX22KGN2

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Source/system_wgm160.c
Family	EFR32
Sub Family	WGM160 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/WGM160PX22KGN2_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/WGM160PX22KGN2_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/WGM160PX22KGN2_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
WGM160PX22KGN2
__EFR32_FAMILY
__WGM160_Series_SUBFAMILY

Memory Segments	
FLASH	0x00000000 - 0x001FFFFFF
RAM	0x20000000 - 0x2007FFFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/WGM160PX22KGN2_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Source/system_wgm160.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/WGM160PX22KGN2_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=WGM160 Series

WGM160PX22KGN3

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Source/system_wgm160.c
Family	EFR32
Sub Family	WGM160 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/WGM160PX22KGN3_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/WGM160PX22KGN3_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/WGM160PX22KGN3_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
WGM160PX22KGN3	
__EFR32_FAMILY	
__WGM160_Series_SUBFAMILY	

Memory Segments	
FLASH	0x00000000 - 0x001FFFFFF
RAM	0x20000000 - 0x2007FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include
DeviceHeaderFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EFR32/XML/WGM160PX22KGN3_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/WGM160/Source/system_wgm160.c
DeviceVectorsFile	\$(TargetsDir)/EFR32/Source/WGM160PX22KGN3_Vectors.s
DeviceFamily	EFR32
DeviceSubFamily	WGM160 Series

ZGM13 Series Family

[ZGM130S037HGN](#)

[ZGM130S037HGN1](#)

ZGM130S037HGN

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM13/Source/system_zgm13.c
Family	EFR32
Sub Family	ZGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/ZGM130S037HGN_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/ZGM130S037HGN_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/ZGM130S037HGN_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

ZGM130S037HGN

__EFR32_FAMILY

__ZGM13_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM13/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM13/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/ZGM130S037HGN_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM13/Source/system_zgm13.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/ZGM130S037HGN_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=ZGM13 Series

ZGM130S037HGN1

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM13/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM13/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM13/Source/system_zgm13.c
Family	EFR32
Sub Family	ZGM13 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/ZGM130S037HGN1_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/ZGM130S037HGN1_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/ZGM130S037HGN1_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

ZGM130S037HGN1

__EFR32_FAMILY

__ZGM13_Series_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM13/Include

DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM13/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/ZGM130S037HGN1_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM13/Source/system_zgm13.c

DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/ZGM130S037HGN1_Vectors.s

DeviceFamily=EFR32

DeviceSubFamily=ZGM13 Series

ZGM23 Series Family

[ZGM230SA27HGN](#)

[ZGM230SA27HNN](#)

[ZGM230SB27HGN](#)

[ZGM230SB27HNN](#)

ZGM230SA27HGN

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Source/system_zgm23.c
Family	EFR32
Sub Family	ZGM23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/ZGM230SA27HGN_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/ZGM230SA27HGN_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/ZGM230SA27HGN_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
ZGM230SA27HGN
__EFR32_FAMILY
__ZGM23_Series_SUBFAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/ZGM230SA27HGN_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Source/system_zgm23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/ZGM230SA27HGN_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=ZGM23 Series
```

ZGM230SA27HNN

Device Details

CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Source/system_zgm23.c
Family	EFR32
Sub Family	ZGM23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/ZGM230SA27HNN_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/ZGM230SA27HNN_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/ZGM230SA27HNN_Vectors.s

Preprocessor Definitions

```
ARM_MATH_ARMV8MML
ZGM230SA27HNN
__EFR32_FAMILY
__ZGM23_Series_SUBFAMILY
```

Memory Segments

FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Include
DeviceHeaderFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Include/em_device.h
DeviceLoaderFile=$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/EFR32/XML/ZGM230SA27HNN_Registers.xml
DeviceSystemFile=$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Source/system_zgm23.c
DeviceVectorsFile=$(TargetsDir)/EFR32/Source/ZGM230SA27HNN_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=ZGM23 Series
```


ZGM230SB27HGN

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Source/system_zgm23.c
Family	EFR32
Sub Family	ZGM23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/ZGM230SB27HGN_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/ZGM230SB27HGN_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/ZGM230SB27HGN_Vectors.s

Preprocessor Definitions
ARM_MATH_ARMV8MML
ZGM230SB27HGN
__EFR32_FAMILY
__ZGM23_Series_SUBFAMILY

Memory Segments	
FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Include
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/ZGM230SB27HGN_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Source/system_zgm23.c
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/ZGM230SB27HGN_Vectors.s
DeviceFamily=EFR32
DeviceSubFamily=ZGM23 Series

ZGM230SB27HNN

Device Details	
CMSIS Header File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Include
CMSIS System File	\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Source/system_zgm23.c
Family	EFR32
Sub Family	ZGM23 Series
Loader File	\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf
Memory Map File	\$(TargetsDir)/EFR32/XML/ZGM230SB27HNN_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EFR32/XML/ZGM230SB27HNN_Registers.xml
Vectors File	\$(TargetsDir)/EFR32/Source/ZGM230SB27HNN_Vectors.s
Preprocessor Definitions	
ARM_MATH_ARMV8MML	
ZGM230SB27HNN	
__EFR32_FAMILY	
__ZGM23_Series_SUBFAMILY	
Memory Segments	
FLASH	0x08000000 - 0x0807FFFF
RAM	0x20000000 - 0x2000FFFF
Project Macros	
DeviceIncludePath=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Include	
DeviceHeaderFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Include/em_device.h	
DeviceLoaderFile=\$(TargetsDir)/EFR32/Loader/GECKOS2C3_Loader.elf	
DeviceRegisterDefinitionFile=\$(TargetsDir)/EFR32/XML/ZGM230SB27HNN_Registers.xml	
DeviceSystemFile=\$(TargetsDir)/EFR32/CMSIS/Device/SiliconLabs/ZGM23/Source/system_zgm23.c	
DeviceVectorsFile=\$(TargetsDir)/EFR32/Source/ZGM230SB27HNN_Vectors.s	
DeviceFamily=EFR32	
DeviceSubFamily=ZGM23 Series	