



EZR32 CPU Support Package Guide

Version: 4.5



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EZR32LG230F256R67	57
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EZR32LG230F64R61	62
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EZR32LG330F64R60	82
EZR32LG330F64R61	83
EZR32LG330F64R63	84
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EZR32WG230F256R55	97
EZR32WG230F256R60	98
EZR32WG230F256R61	99
EZR32WG230F256R63	100
EZR32WG230F256R67	101
EZR32WG230F256R68	102
EZR32WG230F256R69	103
EZR32WG230F64R55	104
EZR32WG230F64R60	105
EZR32WG230F64R61	106
EZR32WG230F64R63	107
EZR32WG230F64R67	108
EZR32WG230F64R68	109
EZR32WG230F64R69	110
EZR32WG330F128R55	111
EZR32WG330F128R60	112
EZR32WG330F128R61	113
EZR32WG330F128R63	114
EZR32WG330F128R67	115

EZR32WG330F128R68	116
EZR32WG330F128R69	117
EZR32WG330F256R55	118
EZR32WG330F256R60	119
EZR32WG330F256R61	120
EZR32WG330F256R63	121
EZR32WG330F256R67	122
EZR32WG330F256R68	123
EZR32WG330F256R69	124
EZR32WG330F64R55	125
EZR32WG330F64R60	126
EZR32WG330F64R61	127
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EZR32 Support Package

This guide describes the following features of the EZR32 CPU support package:

- [How to create EZR32 projects](#)
- [How to open EZR32 sample projects](#)
- [EZR32 specific project properties](#)
- [EZR32 specific project templates](#)
- [Supported EZR32 devices](#)

Creating EZR32 Projects

Creating an EZR32 C/C++ executable project

To create a new minimal C/C++ executable project:

- Select the **File > New > New Project** menu item.
- Select the **A C/C++ executable for Silicon Labs EZR32** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

Creating an EZR32 library project

To create a new library project:

- Select the **File > New > New Project** menu item.
- Select the **A library for Silicon Labs EZR32** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

Creating an EZR32 externally built executable project

To create a new project that will allow you to debug an existing externally built executable file:

- Select the **File > New > New Project** menu item.
- Select the **An externally built executable for Silicon Labs EZR32** project template.
- Set the required project name and location directory.
- Click **Next**.
- Set the **Load File** project property to point to the executable file you want to download and debug.
- If required, change any of the other default project settings.
- Click **Finish** to create the project.

Creating an EZR32 CrossWorks Tasking Library executable project

To create a new C/C++ executable project configured to use the CrossWorks Tasking Library:

- Select the **File > New > New Project** menu item.
- Select the **A CrossWorks Tasking Library executable for Silicon Labs EZR32** project template.
- Set the required project name and location directory.
- Click **Next**.

If required, change any of the other default project settings.
Click **Finish** to create the project.

Creating an EZR32 assembly code only executable project

Please note, this template does not add any C/C++ startup code or libraries and is therefore not suitable for creating projects that include C/C++ code.

To create a new assembly code only executable project without:

Select the **File > New > New Project** menu item.
Select the **An assembly code only executable for Silicon Labs EZR32** project template.
Set the required project name and location directory.
Click **Next**.
If required, change any of the other default project settings.
Click **Finish** to create the project.

Opening EZR32 Sample Solutions

EZR32 Samples Solution

This solution contains general sample projects that run on EZR32 devices. To open the EZR32 Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **Silicon Labs EZR32 CPU Support Package** link.
- Select the **Samples Solutions > EZR32 Samples Solution** link.

EZR32 CMSIS-DSP Samples Solution

This solution contains sample projects that use the CMSIS-DSP library running on EZR32 devices. To open the EZR32 CMSIS-DSP Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **Silicon Labs EZR32 CPU Support Package** link.
- Select the **Sample Solutions > EZR32 CMSIS-DSP Samples Solution** link.

EZR32 Project Properties

Projects creating using the project templates in this support package have the following device specific project properties:

Heap Size

The heap size is set to be 256 bytes when a project is created. The heap size can be modified using the **Heap Size** project property.

Section Placement

You can select the memory configuration you require using the **Section Placement** project property.

For EZR32 projects, the set of placements are:

Flash - The application runs in internal Flash memory (*default*).

Flash Vectors In RAM - The application runs in internal Flash memory and exception vectors are copied to RAM memory.

Flash Copy To RAM - The application starts in internal flash and copies itself to run from internal RAM memory.

RAM - The application runs from internal RAM memory only.

Stack Sizes

The main stack size is set to be 256 bytes when a project is created.

The process stack size is set to be 0 bytes when a project is created.

The main and process stack sizes can be modified using the **Main Stack Size** and **Process Stack Size** project properties.

To change the location of the stacks, edit the section placement file and place the `.stack` and `.stack_process` sections as required.

Startup From Reset

By default, the application will only startup from power-on/reset in *Release* configuration. This acts as a safety net in case you accidentally download a program in FLASH during development that crashes and prevents the debugger from taking control of the target over the debug interface thus rendering the device unusable.

For EZR32 projects, the **Startup From Reset** project property can be set to one of the following:

No - The application will not startup from reset.

Release Only - The application will only startup from reset when built in *Release* configuration (*default*).

Yes - The application will always startup from reset.

Target Processor

Once a project has been created you can target different devices by modifying the **Target Processor** project property. See the [EZR32 Devices](#) section for details on the files, preprocessor definitions and macro definitions used when a device is selected.

EZR32 Project Templates

The project template system simplifies the creation of new projects with the IDE, it also system makes it easy to create new projects with a text editor or script. All that needs to be specified is the project name, the support packages that the project is dependent on, the target processor and the source files you want to add to the project. For example, create a file called *example.hzp* with the following contents:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="EZR32_EXE">
    <configuration Name="Common" package_dependencies="EZR32" Target="EZR32LG230F256R55" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

You can also add any other property settings that the project requires such as preprocessor definitions or include paths using the property save name, for example:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="EZR32_EXE">
    <configuration Name="Common" package_dependencies="EZR32" Target="EZR32LG230F256R55"
      c_preprocessor_definitions="MYDEF1=1;MYDEF2=TWO" c_user_include_directories="$(ProjectDir)/
      include1;$(ProjectDir)/include2" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

Available EZR32 project templates

Template Name	Template Description
EZR32_ASM_EXE	EZR32 Assembly Code Only Executable
EZR32_CTL_EXE	EZR32 CTL Executable
EZR32_EXE	EZR32 C/C++ Executable
EZR32_EXT_EXE	EZR32 Externally Built Executable
EZR32_LIB	EZR32 Library

EZR32 Devices

This package supports the following EZR32 devices:

[EZR32HG Series Family](#)

[EZR32LG Series Family](#)

[EZR32WG Series Family](#)

EZR32HG Series Family

EZR32HG220F32R55

EZR32HG220F32R60

EZR32HG220F32R61

EZR32HG220F32R63

EZR32HG220F32R67

EZR32HG220F32R68

EZR32HG220F32R69

EZR32HG220F64R55

EZR32HG220F64R60

EZR32HG220F64R61

EZR32HG220F64R63

EZR32HG220F64R67

EZR32HG220F64R68

EZR32HG220F64R69

EZR32HG320F32R55

EZR32HG320F32R60

EZR32HG320F32R61

EZR32HG320F32R63

EZR32HG320F32R67

EZR32HG320F32R68

EZR32HG320F32R69

EZR32HG320F64R55

EZR32HG320F64R60

EZR32HG320F64R61

EZR32HG320F64R63

EZR32HG320F64R67

EZR32HG320F64R68

EZR32HG320F64R69

EZR32HG220F32R55

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG220F32R55_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG220F32R55_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG220F32R55_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG220F32R55

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG220F32R55_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG220F32R55_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG220F32R60

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG220F32R60_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG220F32R60_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG220F32R60_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG220F32R60

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG220F32R60_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG220F32R60_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG220F32R61

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG220F32R61_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG220F32R61_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG220F32R61_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG220F32R61

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG220F32R61_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG220F32R61_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG220F32R63

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG220F32R63_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG220F32R63_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG220F32R63_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG220F32R63

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG220F32R63_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG220F32R63_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG220F32R67

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG220F32R67_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG220F32R67_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG220F32R67_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG220F32R67

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG220F32R67_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG220F32R67_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG220F32R68

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG220F32R68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG220F32R68_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG220F32R68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG220F32R68

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG220F32R68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG220F32R68_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG220F32R69

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG220F32R69_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG220F32R69_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG220F32R69_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG220F32R69

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG220F32R69_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG220F32R69_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG220F64R55

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG220F64R55_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG220F64R55_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG220F64R55_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG220F64R55

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG220F64R55_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG220F64R55_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG220F64R60

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG220F64R60_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG220F64R60_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG220F64R60_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG220F64R60

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG220F64R60_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG220F64R60_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG220F64R61

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG220F64R61_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG220F64R61_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG220F64R61_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG220F64R61

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG220F64R61_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG220F64R61_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG220F64R63

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG220F64R63_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG220F64R63_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG220F64R63_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG220F64R63

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG220F64R63_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG220F64R63_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG220F64R67

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG220F64R67_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG220F64R67_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG220F64R67_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG220F64R67

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG220F64R67_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG220F64R67_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG220F64R68

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG220F64R68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG220F64R68_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG220F64R68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG220F64R68

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG220F64R68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG220F64R68_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG220F64R69

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG220F64R69_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG220F64R69_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG220F64R69_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG220F64R69

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG220F64R69_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG220F64R69_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG320F32R55

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG320F32R55_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG320F32R55_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG320F32R55_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG320F32R55

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG320F32R55_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG320F32R55_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG320F32R60

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG320F32R60_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG320F32R60_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG320F32R60_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG320F32R60

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG320F32R60_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG320F32R60_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG320F32R61

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG320F32R61_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG320F32R61_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG320F32R61_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG320F32R61

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG320F32R61_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG320F32R61_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG320F32R63

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG320F32R63_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG320F32R63_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG320F32R63_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG320F32R63

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG320F32R63_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG320F32R63_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG320F32R67

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG320F32R67_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG320F32R67_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG320F32R67_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG320F32R67

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG320F32R67_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG320F32R67_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG320F32R68

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG320F32R68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG320F32R68_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG320F32R68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG320F32R68

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG320F32R68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG320F32R68_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG320F32R69

Device Details	
CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG320F32R69_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG320F32R69_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG320F32R69_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
EZR32HG320F32R69	
__EZR32HG_Series_SUBFAMILY	
__EZR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
DeviceHeaderFile	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EZR32/XML/EZR32HG320F32R69_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
DeviceVectorsFile	\$(TargetsDir)/EZR32/Source/EZR32HG320F32R69_Vectors.s
DeviceFamily	EZR32
DeviceSubFamily	EZR32HG Series

EZR32HG320F64R55

Device Details	
CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG320F64R55_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG320F64R55_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG320F64R55_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
EZR32HG320F64R55	
__EZR32HG_Series_SUBFAMILY	
__EZR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
DeviceHeaderFile	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EZR32/XML/EZR32HG320F64R55_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
DeviceVectorsFile	\$(TargetsDir)/EZR32/Source/EZR32HG320F64R55_Vectors.s
DeviceFamily	EZR32
DeviceSubFamily	EZR32HG Series

EZR32HG320F64R60

Device Details	
CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG320F64R60_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG320F64R60_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG320F64R60_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
EZR32HG320F64R60	
__EZR32HG_Series_SUBFAMILY	
__EZR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
DeviceHeaderFile	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EZR32/XML/EZR32HG320F64R60_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
DeviceVectorsFile	\$(TargetsDir)/EZR32/Source/EZR32HG320F64R60_Vectors.s
DeviceFamily	EZR32
DeviceSubFamily	EZR32HG Series

EZR32HG320F64R61

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG320F64R61_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG320F64R61_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG320F64R61_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG320F64R61

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG320F64R61_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG320F64R61_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG320F64R63

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG320F64R63_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG320F64R63_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG320F64R63_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG320F64R63

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG320F64R63_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG320F64R63_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG320F64R67

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG320F64R67_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG320F64R67_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG320F64R67_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG320F64R67

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG320F64R67_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG320F64R67_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG320F64R68

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG320F64R68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG320F64R68_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG320F64R68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM0PLUS

EZR32HG320F64R68

__EZR32HG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32HG320F64R68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32HG320F64R68_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32HG Series

EZR32HG320F64R69

Device Details	
CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
Family	EZR32
Sub Family	EZR32HG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32HG320F64R69_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32HG320F64R69_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32HG320F64R69_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
EZR32HG320F64R69	
__EZR32HG_Series_SUBFAMILY	
__EZR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include
DeviceHeaderFile	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EZR32/Loader/EFM32M0P_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EZR32/XML/EZR32HG320F64R69_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32HG/Source/system_ezr32hg.c
DeviceVectorsFile	\$(TargetsDir)/EZR32/Source/EZR32HG320F64R69_Vectors.s
DeviceFamily	EZR32
DeviceSubFamily	EZR32HG Series

EZR32LG Series Family

EZR32LG230F128R55

EZR32LG230F128R60

EZR32LG230F128R61

EZR32LG230F128R63

EZR32LG230F128R67

EZR32LG230F128R68

EZR32LG230F128R69

EZR32LG230F256R55

EZR32LG230F256R60

EZR32LG230F256R61

EZR32LG230F256R63

EZR32LG230F256R67

EZR32LG230F256R68

EZR32LG230F256R69

EZR32LG230F64R55

EZR32LG230F64R60

EZR32LG230F64R61

EZR32LG230F64R63

EZR32LG230F64R67

EZR32LG230F64R68

EZR32LG230F64R69

EZR32LG330F128R55

EZR32LG330F128R60

EZR32LG330F128R61

EZR32LG330F128R63

EZR32LG330F128R67

EZR32LG330F128R68

EZR32LG330F128R69

EZR32LG330F256R55

EZR32LG330F256R60

EZR32LG330F256R61

EZR32LG330F256R63

EZR32LG330F256R67

EZR32LG330F256R68

EZR32LG330F256R69

EZR32LG330F64R55

EZR32LG330F64R60

EZR32LG330F64R61

[EZR32LG330F64R63](#)

[EZR32LG330F64R67](#)

[EZR32LG330F64R68](#)

[EZR32LG330F64R69](#)

EZR32LG230F128R55

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F128R55_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F128R55_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F128R55_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F128R55

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F128R55_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F128R55_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F128R60

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F128R60_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F128R60_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F128R60_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F128R60

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F128R60_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F128R60_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F128R61

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F128R61_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F128R61_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F128R61_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F128R61

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F128R61_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F128R61_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F128R63

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F128R63_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F128R63_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F128R63_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F128R63

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F128R63_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F128R63_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F128R67

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F128R67_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F128R67_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F128R67_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F128R67

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F128R67_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F128R67_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F128R68

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F128R68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F128R68_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F128R68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F128R68

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F128R68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F128R68_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F128R69

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F128R69_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F128R69_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F128R69_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F128R69

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F128R69_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F128R69_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F256R55

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F256R55_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F256R55_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F256R55_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F256R55

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F256R55_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F256R55_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F256R60

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F256R60_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F256R60_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F256R60_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F256R60

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F256R60_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F256R60_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F256R61

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F256R61_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F256R61_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F256R61_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F256R61

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F256R61_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F256R61_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F256R63

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F256R63_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F256R63_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F256R63_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F256R63

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F256R63_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F256R63_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F256R67

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F256R67_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F256R67_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F256R67_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F256R67

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F256R67_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F256R67_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F256R68

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F256R68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F256R68_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F256R68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F256R68

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F256R68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F256R68_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F256R69

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F256R69_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F256R69_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F256R69_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F256R69

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F256R69_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F256R69_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F64R55

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F64R55_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F64R55_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F64R55_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F64R55

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F64R55_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F64R55_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F64R60

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F64R60_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F64R60_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F64R60_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F64R60

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F64R60_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F64R60_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F64R61

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F64R61_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F64R61_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F64R61_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F64R61

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F64R61_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F64R61_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F64R63

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F64R63_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F64R63_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F64R63_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F64R63

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F64R63_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F64R63_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F64R67

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F64R67_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F64R67_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F64R67_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F64R67

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F64R67_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F64R67_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F64R68

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F64R68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F64R68_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F64R68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F64R68

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F64R68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F64R68_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG230F64R69

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG230F64R69_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG230F64R69_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG230F64R69_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG230F64R69

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG230F64R69_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG230F64R69_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG330F128R55

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F128R55_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F128R55_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F128R55_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG330F128R55

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F128R55_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F128R55_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG330F128R60

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F128R60_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F128R60_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F128R60_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG330F128R60

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F128R60_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F128R60_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG330F128R61

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F128R61_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F128R61_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F128R61_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG330F128R61

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F128R61_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F128R61_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG330F128R63

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F128R63_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F128R63_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F128R63_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG330F128R63

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F128R63_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F128R63_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG330F128R67

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F128R67_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F128R67_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F128R67_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG330F128R67

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F128R67_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F128R67_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG330F128R68

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F128R68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F128R68_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F128R68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG330F128R68

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F128R68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F128R68_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG330F128R69

Device Details	
CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F128R69_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F128R69_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F128R69_Vectors.s

Preprocessor Definitions
ARM_MATH_CM3
EZR32LG330F128R69
__EZR32LG_Series_SUBFAMILY
__EZR32_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F128R69_Registers.xml
DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F128R69_Vectors.s
DeviceFamily=EZR32
DeviceSubFamily=EZR32LG Series

EZR32LG330F256R55

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F256R55_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F256R55_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F256R55_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG330F256R55

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F256R55_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F256R55_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG330F256R60

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F256R60_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F256R60_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F256R60_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG330F256R60

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F256R60_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F256R60_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG330F256R61

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F256R61_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F256R61_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F256R61_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG330F256R61

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F256R61_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F256R61_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG330F256R63

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F256R63_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F256R63_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F256R63_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG330F256R63

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F256R63_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F256R63_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG330F256R67

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F256R67_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F256R67_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F256R67_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG330F256R67

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F256R67_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F256R67_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG330F256R68

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F256R68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F256R68_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F256R68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG330F256R68

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F256R68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F256R68_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG330F256R69

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F256R69_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F256R69_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F256R69_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG330F256R69

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F256R69_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F256R69_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG330F64R55

Device Details	
CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F64R55_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F64R55_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F64R55_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
EZR32LG330F64R55	
__EZR32LG_Series_SUBFAMILY	
__EZR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
DeviceHeaderFile	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EZR32/XML/EZR32LG330F64R55_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
DeviceVectorsFile	\$(TargetsDir)/EZR32/Source/EZR32LG330F64R55_Vectors.s
DeviceFamily	EZR32
DeviceSubFamily	EZR32LG Series

EZR32LG330F64R60

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F64R60_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F64R60_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F64R60_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG330F64R60

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F64R60_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F64R60_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG330F64R61

Device Details	
CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F64R61_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F64R61_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F64R61_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
EZR32LG330F64R61	
__EZR32LG_Series_SUBFAMILY	
__EZR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
DeviceHeaderFile	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EZR32/XML/EZR32LG330F64R61_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
DeviceVectorsFile	\$(TargetsDir)/EZR32/Source/EZR32LG330F64R61_Vectors.s
DeviceFamily	EZR32
DeviceSubFamily	EZR32LG Series

EZR32LG330F64R63

Device Details	
CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F64R63_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F64R63_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F64R63_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM3	
EZR32LG330F64R63	
__EZR32LG_Series_SUBFAMILY	
__EZR32_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
DeviceHeaderFile	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
DeviceLoaderFile	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/EZR32/XML/EZR32LG330F64R63_Registers.xml
DeviceSystemFile	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
DeviceVectorsFile	\$(TargetsDir)/EZR32/Source/EZR32LG330F64R63_Vectors.s
DeviceFamily	EZR32
DeviceSubFamily	EZR32LG Series

EZR32LG330F64R67

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F64R67_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F64R67_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F64R67_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG330F64R67

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F64R67_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F64R67_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG330F64R68

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F64R68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F64R68_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F64R68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG330F64R68

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F64R68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F64R68_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32LG330F64R69

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c
Family	EZR32
Sub Family	EZR32LG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32LG330F64R69_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32LG330F64R69_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32LG330F64R69_Vectors.s

Preprocessor Definitions

ARM_MATH_CM3

EZR32LG330F64R69

__EZR32LG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32LG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32LG330F64R69_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32LG/Source/system_ezr32lg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32LG330F64R69_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32LG Series

EZR32WG Series Family

EZR32WG230F128R55
EZR32WG230F128R60
EZR32WG230F128R61
EZR32WG230F128R63
EZR32WG230F128R67
EZR32WG230F128R68
EZR32WG230F128R69
EZR32WG230F256R55
EZR32WG230F256R60
EZR32WG230F256R61
EZR32WG230F256R63
EZR32WG230F256R67
EZR32WG230F256R68
EZR32WG230F256R69
EZR32WG230F64R55
EZR32WG230F64R60
EZR32WG230F64R61
EZR32WG230F64R63
EZR32WG230F64R67
EZR32WG230F64R68
EZR32WG230F64R69
EZR32WG330F128R55
EZR32WG330F128R60
EZR32WG330F128R61
EZR32WG330F128R63
EZR32WG330F128R67
EZR32WG330F128R68
EZR32WG330F128R69
EZR32WG330F256R55
EZR32WG330F256R60
EZR32WG330F256R61
EZR32WG330F256R63
EZR32WG330F256R67
EZR32WG330F256R68
EZR32WG330F256R69
EZR32WG330F64R55
EZR32WG330F64R60
EZR32WG330F64R61

[EZR32WG330F64R63](#)

[EZR32WG330F64R67](#)

[EZR32WG330F64R68](#)

[EZR32WG330F64R69](#)

EZR32WG230F128R55

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F128R55_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F128R55_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F128R55_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F128R55

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F128R55_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F128R55_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F128R60

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F128R60_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F128R60_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F128R60_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F128R60

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F128R60_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F128R60_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F128R61

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F128R61_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F128R61_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F128R61_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F128R61

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F128R61_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F128R61_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F128R63

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F128R63_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F128R63_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F128R63_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F128R63

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F128R63_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F128R63_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F128R67

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F128R67_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F128R67_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F128R67_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F128R67

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F128R67_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F128R67_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F128R68

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F128R68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F128R68_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F128R68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F128R68

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F128R68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F128R68_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F128R69

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F128R69_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F128R69_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F128R69_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F128R69

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F128R69_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F128R69_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F256R55

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F256R55_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F256R55_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F256R55_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F256R55

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F256R55_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F256R55_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F256R60

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F256R60_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F256R60_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F256R60_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F256R60

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F256R60_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F256R60_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F256R61

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F256R61_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F256R61_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F256R61_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F256R61

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F256R61_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F256R61_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F256R63

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F256R63_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F256R63_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F256R63_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F256R63

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F256R63_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F256R63_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F256R67

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F256R67_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F256R67_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F256R67_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F256R67

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F256R67_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F256R67_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F256R68

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F256R68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F256R68_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F256R68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F256R68

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F256R68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F256R68_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F256R69

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F256R69_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F256R69_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F256R69_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F256R69

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F256R69_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F256R69_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F64R55

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F64R55_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F64R55_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F64R55_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F64R55

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F64R55_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F64R55_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F64R60

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F64R60_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F64R60_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F64R60_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F64R60

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F64R60_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F64R60_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F64R61

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F64R61_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F64R61_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F64R61_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F64R61

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F64R61_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F64R61_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F64R63

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F64R63_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F64R63_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F64R63_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F64R63

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F64R63_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F64R63_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F64R67

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F64R67_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F64R67_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F64R67_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F64R67

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F64R67_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F64R67_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F64R68

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F64R68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F64R68_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F64R68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F64R68

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F64R68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F64R68_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG230F64R69

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG230F64R69_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG230F64R69_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG230F64R69_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG230F64R69

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG230F64R69_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG230F64R69_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F128R55

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F128R55_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F128R55_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F128R55_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F128R55

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F128R55_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F128R55_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F128R60

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F128R60_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F128R60_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F128R60_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F128R60

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F128R60_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F128R60_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F128R61

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F128R61_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F128R61_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F128R61_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F128R61

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F128R61_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F128R61_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F128R63

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F128R63_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F128R63_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F128R63_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F128R63

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F128R63_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F128R63_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F128R67

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F128R67_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F128R67_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F128R67_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F128R67

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F128R67_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F128R67_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F128R68

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F128R68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F128R68_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F128R68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F128R68

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F128R68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F128R68_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F128R69

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F128R69_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F128R69_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F128R69_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F128R69

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F128R69_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F128R69_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F256R55

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F256R55_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F256R55_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F256R55_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F256R55

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F256R55_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F256R55_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F256R60

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F256R60_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F256R60_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F256R60_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F256R60

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F256R60_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F256R60_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F256R61

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F256R61_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F256R61_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F256R61_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F256R61

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F256R61_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F256R61_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F256R63

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F256R63_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F256R63_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F256R63_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F256R63

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F256R63_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F256R63_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F256R67

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F256R67_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F256R67_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F256R67_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F256R67

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F256R67_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F256R67_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F256R68

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F256R68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F256R68_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F256R68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F256R68

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F256R68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F256R68_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F256R69

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F256R69_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F256R69_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F256R69_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F256R69

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F256R69_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F256R69_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F64R55

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F64R55_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F64R55_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F64R55_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F64R55

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F64R55_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F64R55_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F64R60

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F64R60_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F64R60_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F64R60_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F64R60

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F64R60_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F64R60_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F64R61

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F64R61_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F64R61_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F64R61_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F64R61

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F64R61_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F64R61_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F64R63

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F64R63_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F64R63_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F64R63_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F64R63

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F64R63_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F64R63_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F64R67

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F64R67_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F64R67_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F64R67_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F64R67

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F64R67_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F64R67_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F64R68

Device Details

CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F64R68_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F64R68_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F64R68_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

EZR32WG330F64R68

__EZR32WG_Series_SUBFAMILY

__EZR32_FAMILY

Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include

DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h

DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F64R68_Registers.xml

DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c

DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F64R68_Vectors.s

DeviceFamily=EZR32

DeviceSubFamily=EZR32WG Series

EZR32WG330F64R69

Device Details	
CMSIS Header File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h
CMSIS Include Path	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include
CMSIS System File	\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c
Family	EZR32
Sub Family	EZR32WG Series
Loader File	\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf
Memory Map File	\$(TargetsDir)/EZR32/XML/EZR32WG330F64R69_MemoryMap.xml
Register Definition File	\$(TargetsDir)/EZR32/XML/EZR32WG330F64R69_Registers.xml
Vectors File	\$(TargetsDir)/EZR32/Source/EZR32WG330F64R69_Vectors.s
Preprocessor Definitions	
ARM_MATH_CM4	
EZR32WG330F64R69	
__EZR32WG_Series_SUBFAMILY	
__EZR32_FAMILY	
Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20007FFF
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DeviceIncludePath=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include	
DeviceHeaderFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Include/em_device.h	
DeviceLoaderFile=\$(TargetsDir)/EZR32/Loader/EFM32WG_Loader.elf	
DeviceRegisterDefinitionFile=\$(TargetsDir)/EZR32/XML/EZR32WG330F64R69_Registers.xml	
DeviceSystemFile=\$(TargetsDir)/EZR32/CMSIS/Device/SiliconLabs/EZR32WG/Source/system_ezr32wg.c	
DeviceVectorsFile=\$(TargetsDir)/EZR32/Source/EZR32WG330F64R69_Vectors.s	
DeviceFamily=EZR32	
DeviceSubFamily=EZR32WG Series	