



FM4 CPU Support Package Guide

Version: 1.2



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S6E2C58L0A	128
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S6E2C59J0A	130
S6E2C59L0A	131
S6E2C5AH0A	132
S6E2C5AJ0A	133
S6E2C5AL0A	134
S6E2CC Family	135
S6E2CC8H0A	136
S6E2CC8J0A	137
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S6E2G38J	178
S6E2GH Family	179
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S6E2GH6J	182
S6E2GH8J	183
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S6E2GK6J	187
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S6E2GM6H	190
S6E2GM8H	191
S6E2GM6J	192
S6E2GM8J	193
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S6E2H44F	204
S6E2H46F	205
S6E2H44G	206
S6E2H46G	207
S6E2HE Family	208
S6E2HE4E	209
S6E2HE6E	210
S6E2HE4F	211
S6E2HE6F	212
S6E2HE4G	213
S6E2HE6G	214
S6E2HG Family	215
S6E2HG4E	216
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FM4 Support Package

This guide describes the following features of the FM4 CPU support package:

- [How to create FM4 projects](#)
- [How to open FM4 sample projects](#)
- [FM4 specific project properties](#)
- [FM4 specific project templates](#)
- [Supported FM4 devices](#)

Creating FM4 Projects

Creating an FM4 C/C++ executable project

To create a new minimal C/C++ executable project:

- Select the **File > New > New Project** menu item.
- Select the **A C/C++ executable for Spansion FM4** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

Creating an FM4 library project

To create a new library project:

- Select the **File > New > New Project** menu item.
- Select the **A library for Spansion FM4** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

Creating an FM4 externally built executable project

To create a new project that will allow you to debug an existing externally built executable file:

- Select the **File > New > New Project** menu item.
- Select the **An externally built executable for Spansion FM4** project template.
- Set the required project name and location directory.
- Click **Next**.
- Set the **Load File** project property to point to the executable file you want to download and debug.
- If required, change any of the other default project settings.
- Click **Finish** to create the project.

Creating an FM4 CrossWorks Tasking Library executable project

To create a new C/C++ executable project configured to use the CrossWorks Tasking Library:

- Select the **File > New > New Project** menu item.
- Select the **A CrossWorks Tasking Library executable for Spansion FM4** project template.
- Set the required project name and location directory.
- Click **Next**.

- If required, change any of the other default project settings.
- Click **Finish** to create the project.

Creating an FM4 assembly code only executable project

Please note, this template does not add any C/C++ startup code or libraries and is therefore not suitable for creating projects that include C/C++ code.

To create a new assembly code only executable project without:

- Select the **File > New > New Project** menu item.
- Select the **An assembly code only executable for Spansion FM4** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the other default project settings.
- Click **Finish** to create the project.

Opening FM4 Sample Solutions

FM4 Samples Solution

This solution contains general sample projects that run on FM4 devices. To open the FM4 Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **Spansion FM4 CPU Support Package** link.
- Select the **Samples Solutions > FM4 Samples Solution** link.

FM4 CMSIS-DSP Samples Solution

This solution contains sample projects that use the CMSIS-DSP library running on FM4 devices. To open the FM4 CMSIS-DSP Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **Spansion FM4 CPU Support Package** link.
- Select the **Sample Solutions > FM4 CMSIS-DSP Samples Solution** link.

FM4 Project Properties

Projects created using the project templates in this support package have the following device specific project properties:

Heap Size

The heap size is set to be 256 bytes when a project is created. The heap size can be modified using the **Heap Size** project property.

Section Placement

You can select the memory configuration you require using the **Section Placement** project property.

For FM4 projects, the set of placements are:

- **Flash** - The application runs in internal Flash memory (*default*).
- **Flash Vectors In RAM** - The application runs in internal Flash memory and exception vectors are copied to RAM memory.
- **Flash Copy To RAM** - The application starts in internal flash and copies itself to run from internal RAM memory.
- **RAM** - The application runs from internal RAM memory only.

Stack Sizes

The main stack size is set to be 256 bytes when a project is created.

The process stack size is set to be 0 bytes when a project is created.

The main and process stack sizes can be modified using the **Main Stack Size** and **Process Stack Size** project properties.

To change the location of the stacks, edit the section placement file and place the `.stack` and `.stack_process` sections as required.

Startup From Reset

By default, the application will only startup from power-on/reset in *Release* configuration. This acts as a safety net in case you accidentally download a program in FLASH during development that crashes and prevents the debugger from taking control of the target over the debug interface thus rendering the device unusable.

For FM4 projects, the **Startup From Reset** project property can be set to one of the following:

- **No** - The application will not startup from reset.

- **Release Only** - The application will only startup from reset when built in *Release* configuration (*default*).
- **Yes** - The application will always startup from reset.

Target Processor

Once a project has been created you can target different devices by modifying the **Target Processor** project property. See the [FM4 Devices](#) section for details on the files, preprocessor definitions and macro definitions used when a device is selected.

FM4 Project Templates

The project template system simplifies the creation of new projects with the IDE, it also system makes it easy to create new projects with a text editor or script. All that needs to be specified is the project name, the support packages that the project is dependent on, the target processor and the source files you want to add to the project. For example, create a file called *example.hzp* with the following contents:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="FM4_EXE">
    <configuration Name="Common" package_dependencies="FM4" Target="MB9BF568R" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

You can also add any other property settings that the project requires such as preprocessor definitions or include paths using the property save name, for example:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="FM4_EXE">
    <configuration Name="Common" package_dependencies="FM4" Target="MB9BF568R"
      c_preprocessor_definitions="MYDEF1=1;MYDEF2=TWO" c_user_include_directories="$(ProjectDir)/
include1;$(ProjectDir)/include2" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

Available FM4 project templates

Template Name	Template Description
FM4_ASM_EXE	FM4 Assembly Code Only Executable
FM4_CTL_EXE	FM4 CTL Executable
FM4_EXE	FM4 C/C++ Executable
FM4_EXT_EXE	FM4 Externally Built Executable
FM4_LIB	FM4 Library

FM4 Devices

This package supports the following FM4 devices:

- [MB9BF160L Family](#)
- [MB9BF160R Family](#)
- [MB9BF360L Family](#)
- [MB9BF360R Family](#)
- [MB9BF460L Family](#)
- [MB9BF460R Family](#)
- [MB9BF560L Family](#)
- [MB9BF560R Family](#)
- [S6E2C1 Family](#)
- [S6E2C2 Family](#)
- [S6E2C3 Family](#)
- [S6E2C4 Family](#)
- [S6E2C5 Family](#)
- [S6E2CC Family](#)
- [S6E2D3 Family](#)
- [S6E2D5 Family](#)
- [S6E2DF Family](#)
- [S6E2DH Family](#)
- [S6E2G2 Family](#)
- [S6E2G3 Family](#)
- [S6E2GH Family](#)
- [S6E2GK Family](#)
- [S6E2GM Family](#)
- [S6E2H1 Family](#)
- [S6E2H4 Family](#)
- [S6E2HE Family](#)
- [S6E2HG Family](#)

MB9BF160L Family

- [MB9BF164K](#)
- [MB9BF164L](#)
- [MB9BF165K](#)
- [MB9BF165L](#)
- [MB9BF166K](#)
- [MB9BF166L](#)

MB9BF164K

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L/mb9b160l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF160L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF160L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF164K_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B160L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B160L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF164K

__FM4_FAMILY

__MB9BF160L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L/mb9b160l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF160L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B160L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B160L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF160L

MB9BF164L

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L/mb9b160l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF160L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF160L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF164L_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B160L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B160L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF164L

__FM4_FAMILY

__MB9BF160L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L/mb9b160l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF160L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B160L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B160L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF160L

MB9BF165K

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L/mb9b160l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF160L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF160L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF165K_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B160L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B160L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF165K

__FM4_FAMILY

__MB9BF160L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFFA000 - 0x1FFFFFFF
RAM2	0x2003D000 - 0x20042FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L/mb9b160l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF160L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B160L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B160L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF160L

MB9BF165L

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L/mb9b160l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF160L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF160L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF165L_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B160L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B160L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF165L

__FM4_FAMILY

__MB9BF160L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFFA000 - 0x1FFFFFFF
RAM2	0x2003D000 - 0x20042FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L/mb9b160l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF160L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B160L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B160L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF160L

MB9BF166K

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L/mb9b160l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF160L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF160L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF166K_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B160L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B160L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF166K

__FM4_FAMILY

__MB9BF160L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L/mb9b160l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF160L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B160L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B160L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF160L

MB9BF166L

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L/mb9b160l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF160L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF160L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF166L_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B160L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B160L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF166L

__FM4_FAMILY

__MB9BF160L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160L/mb9b160l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF160L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B160L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B160L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF160L

MB9BF160R Family

- [MB9BF166M](#)
- [MB9BF166N](#)
- [MB9BF166R](#)
- [MB9BF167M](#)
- [MB9BF167N](#)
- [MB9BF167R](#)
- [MB9BF168M](#)
- [MB9BF168N](#)
- [MB9BF168R](#)

MB9BF166M

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF160R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF166M_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF166M

__FM4_FAMILY

__MB9BF160R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF160R

MB9BF166N

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF160R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF166N_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF166N

__FM4_FAMILY

__MB9BF160R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF160R

MB9BF166R

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF160R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF166R_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF166R

__FM4_FAMILY

__MB9BF160R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF160R

MB9BF167M

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF160R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF167M_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF167M

__FM4_FAMILY

__MB9BF160R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000BFFFF
RAM	0x1FFF4000 - 0x1FFFFFFF
RAM2	0x2003A000 - 0x20045FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF160R

MB9BF167N

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF160R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF167N_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF167N

__FM4_FAMILY

__MB9BF160R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000BFFFF
RAM	0x1FFF4000 - 0x1FFFFFFF
RAM2	0x2003A000 - 0x20045FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF160R

MB9BF167R

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF160R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF167R_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF167R

__FM4_FAMILY

__MB9BF160R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000BFFFF
RAM	0x1FFF4000 - 0x1FFFFFFF
RAM2	0x2003A000 - 0x20045FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF160R

MB9BF168M

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF160R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF168M_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF168M

__FM4_FAMILY

__MB9BF160R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF160R

MB9BF168N

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF160R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF168N_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF168N

__FM4_FAMILY

__MB9BF160R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF160R

MB9BF168R

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF160R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF168R_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF168R

__FM4_FAMILY

__MB9BF160R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B160R/mb9b160r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF160R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B160R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B160R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B160R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF160R

MB9BF360L Family

- [MB9BF364K](#)
- [MB9BF364L](#)
- [MB9BF365K](#)
- [MB9BF365L](#)
- [MB9BF366K](#)
- [MB9BF366L](#)

MB9BF364K

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L/mb9b360l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF360L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF360L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF364K_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B360L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B360L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF364K

__FM4_FAMILY

__MB9BF360L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L/mb9b360l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF360L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B360L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B360L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF360L

MB9BF364L

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L/mb9b360l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF360L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF360L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF364L_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B360L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B360L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF364L

__FM4_FAMILY

__MB9BF360L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L/mb9b360l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF360L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B360L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B360L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF360L

MB9BF365K

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L/mb9b360l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF360L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF360L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF365K_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B360L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B360L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF365K

__FM4_FAMILY

__MB9BF360L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFFA000 - 0x1FFFFFFF
RAM2	0x2003D000 - 0x20042FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L/mb9b360l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF360L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B360L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B360L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF360L

MB9BF365L

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L/mb9b360l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF360L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF360L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF365L_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B360L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B360L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF365L

__FM4_FAMILY

__MB9BF360L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFFA000 - 0x1FFFFFFF
RAM2	0x2003D000 - 0x20042FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L/mb9b360l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF360L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B360L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B360L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF360L

MB9BF366K

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L/mb9b360l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF360L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF360L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF366K_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B360L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B360L_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM4
MB9BF366K
__FM4_FAMILY
__MB9BF360L_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L
DeviceHeaderFile=$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L/mb9b360l.h
DeviceLoaderFile=$(TargetsDir)/FM4/Loader/MB9BF360L_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/FM4/XML/MB9B360L_Registers.xml
DeviceSystemFile=$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360L/system_mb9abxxx.c
DeviceVectorsFile=$(TargetsDir)/FM4/Source/MB9B360L_Vectors.s
DeviceFamily=FM4
DeviceSubFamily=MB9BF360L
```

MB9BF366L

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L/mb9b360l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF360L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF360L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF366L_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B360L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B360L_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM4
MB9BF366L
__FM4_FAMILY
__MB9BF360L_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L
DeviceHeaderFile=$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360L/mb9b360l.h
DeviceLoaderFile=$(TargetsDir)/FM4/Loader/MB9BF360L_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/FM4/XML/MB9B360L_Registers.xml
DeviceSystemFile=$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360L/system_mb9abxxx.c
DeviceVectorsFile=$(TargetsDir)/FM4/Source/MB9B360L_Vectors.s
DeviceFamily=FM4
DeviceSubFamily=MB9BF360L
```


MB9BF360R Family

- [MB9BF366M](#)
- [MB9BF366N](#)
- [MB9BF366R](#)
- [MB9BF367M](#)
- [MB9BF367N](#)
- [MB9BF367R](#)
- [MB9BF368M](#)
- [MB9BF368N](#)
- [MB9BF368R](#)

MB9BF366M

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF360R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF366M_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF366M

__FM4_FAMILY

__MB9BF360R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF360R

MB9BF366N

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF360R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF366N_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF366N

__FM4_FAMILY

__MB9BF360R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF360R

MB9BF366R

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF360R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF366R_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF366R

__FM4_FAMILY

__MB9BF360R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF360R

MB9BF367M

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF360R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF367M_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF367M

__FM4_FAMILY

__MB9BF360R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000BFFFF
RAM	0x1FFF4000 - 0x1FFFFFFF
RAM2	0x2003A000 - 0x20045FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF360R

MB9BF367N

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF360R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF367N_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF367N

__FM4_FAMILY

__MB9BF360R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000BFFFF
RAM	0x1FFF4000 - 0x1FFFFFFF
RAM2	0x2003A000 - 0x20045FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF360R

MB9BF367R

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF360R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF367R_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF367R

__FM4_FAMILY

__MB9BF360R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000BFFFF
RAM	0x1FFF4000 - 0x1FFFFFFF
RAM2	0x2003A000 - 0x20045FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF360R

MB9BF368M

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF360R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF368M_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF368M

__FM4_FAMILY

__MB9BF360R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF360R

MB9BF368N

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF360R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF368N_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF368N

__FM4_FAMILY

__MB9BF360R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF360R

MB9BF368R

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF360R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF368R_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF368R

__FM4_FAMILY

__MB9BF360R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B360R/mb9b360r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF360R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B360R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B360R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B360R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF360R

MB9BF460L Family

- [MB9BF464K](#)
- [MB9BF464L](#)
- [MB9BF465K](#)
- [MB9BF465L](#)
- [MB9BF466K](#)
- [MB9BF466L](#)

MB9BF464K

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L/mb9b460l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF460L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF460L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF464K_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B460L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B460L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF464K

__FM4_FAMILY

__MB9BF460L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L/mb9b460l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF460L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B460L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B460L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF460L

MB9BF464L

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L/mb9b460l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF460L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF460L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF464L_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B460L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B460L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF464L

__FM4_FAMILY

__MB9BF460L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L/mb9b460l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF460L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B460L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B460L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF460L

MB9BF465K

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L/mb9b460l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF460L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF460L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF465K_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B460L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B460L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF465K

__FM4_FAMILY

__MB9BF460L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFFA000 - 0x1FFFFFFF
RAM2	0x2003D000 - 0x20042FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L/mb9b460l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF460L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B460L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B460L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF460L

MB9BF465L

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L/mb9b460l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF460L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF460L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF465L_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B460L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B460L_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM4
MB9BF465L
__FM4_FAMILY
__MB9BF460L_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFFA000 - 0x1FFFFFFF
RAM2	0x2003D000 - 0x20042FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L
DeviceHeaderFile=$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L/mb9b460l.h
DeviceLoaderFile=$(TargetsDir)/FM4/Loader/MB9BF460L_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/FM4/XML/MB9B460L_Registers.xml
DeviceSystemFile=$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460L/system_mb9abxxx.c
DeviceVectorsFile=$(TargetsDir)/FM4/Source/MB9B460L_Vectors.s
DeviceFamily=FM4
DeviceSubFamily=MB9BF460L
```

MB9BF466K

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L/mb9b460l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF460L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF460L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF466K_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B460L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B460L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF466K

__FM4_FAMILY

__MB9BF460L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L/mb9b460l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF460L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B460L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B460L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF460L

MB9BF466L

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L/mb9b460l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF460L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF460L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF466L_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B460L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B460L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF466L

__FM4_FAMILY

__MB9BF460L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460L/mb9b460l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF460L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B460L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B460L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF460L

MB9BF460R Family

- [MB9BF466M](#)
- [MB9BF466N](#)
- [MB9BF466R](#)
- [MB9BF467M](#)
- [MB9BF467N](#)
- [MB9BF467R](#)
- [MB9BF468M](#)
- [MB9BF468N](#)
- [MB9BF468R](#)

MB9BF466M

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF460R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF466M_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF466M

__FM4_FAMILY

__MB9BF460R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF460R

MB9BF466N

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF460R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF466N_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF466N

__FM4_FAMILY

__MB9BF460R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF460R

MB9BF466R

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF460R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF466R_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF466R

__FM4_FAMILY

__MB9BF460R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF460R

MB9BF467M

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF460R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF467M_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF467M

__FM4_FAMILY

__MB9BF460R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000BFFFF
RAM	0x1FFF4000 - 0x1FFFFFFF
RAM2	0x2003A000 - 0x20045FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF460R

MB9BF467N

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF460R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF467N_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4
 MB9BF467N
 __FM4_FAMILY
 __MB9BF460R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000BFFFF
RAM	0x1FFF4000 - 0x1FFFFFFF
RAM2	0x2003A000 - 0x20045FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R
 DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h
 DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf
 DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml
 DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c
 DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s
 DeviceFamily=FM4
 DeviceSubFamily=MB9BF460R

MB9BF467R

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF460R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF467R_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF467R

__FM4_FAMILY

__MB9BF460R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000BFFFF
RAM	0x1FFF4000 - 0x1FFFFFFF
RAM2	0x2003A000 - 0x20045FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF460R

MB9BF468M

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF460R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF468M_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF468M

__FM4_FAMILY

__MB9BF460R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF460R

MB9BF468N

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF460R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF468N_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
MB9BF468N	
__FM4_FAMILY	
__MB9BF460R_SUBFAMILY	

Memory Segments	
FLASH	0x00000000 - 0x000FFFFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R
DeviceHeaderFile	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h
DeviceLoaderFile	\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml
DeviceSystemFile	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c
DeviceVectorsFile	\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s
DeviceFamily	FM4
DeviceSubFamily	MB9BF460R

MB9BF468R

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF460R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF468R_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF468R

__FM4_FAMILY

__MB9BF460R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B460R/mb9b460r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF460R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B460R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B460R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B460R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF460R

MB9BF560L Family

- [MB9BF564K](#)
- [MB9BF564L](#)
- [MB9BF565K](#)
- [MB9BF565L](#)
- [MB9BF566K](#)
- [MB9BF566L](#)

MB9BF564K

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L/mb9b560l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF560L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF560L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF564K_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B560L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B560L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF564K

__FM4_FAMILY

__MB9BF560L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L/mb9b560l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF560L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B560L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B560L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF560L

MB9BF564L

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L/mb9b560l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF560L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF560L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF564L_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B560L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B560L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF564L

__FM4_FAMILY

__MB9BF560L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L/mb9b560l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF560L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B560L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B560L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF560L

MB9BF565K

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L/mb9b560l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF560L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF560L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF565K_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B560L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B560L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF565K

__FM4_FAMILY

__MB9BF560L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFFA000 - 0x1FFFFFFF
RAM2	0x2003D000 - 0x20042FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L/mb9b560l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF560L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B560L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B560L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF560L

MB9BF565L

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L/mb9b560l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF560L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF560L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF565L_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B560L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B560L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF565L

__FM4_FAMILY

__MB9BF560L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFFA000 - 0x1FFFFFFF
RAM2	0x2003D000 - 0x20042FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L/mb9b560l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF560L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B560L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B560L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF560L

MB9BF566K

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L/mb9b560l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF560L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF560L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF566K_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B560L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B560L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF566K

__FM4_FAMILY

__MB9BF560L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L/mb9b560l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF560L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B560L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B560L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF560L

MB9BF566L

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L/mb9b560l.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560L/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF560L
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF560L_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF566L_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B560L_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B560L_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF566L

__FM4_FAMILY

__MB9BF560L_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560L/mb9b560l.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF560L_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B560L_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560L/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B560L_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF560L

MB9BF560R Family

- [MB9BF566M](#)
- [MB9BF566N](#)
- [MB9BF566R](#)
- [MB9BF567M](#)
- [MB9BF567N](#)
- [MB9BF567R](#)
- [MB9BF568M](#)
- [MB9BF568N](#)
- [MB9BF568R](#)

MB9BF566M

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF560R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF566M_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM4
MB9BF566M
__FM4_FAMILY
__MB9BF560R_SUBFAMILY
```

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R
DeviceHeaderFile=$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h
DeviceLoaderFile=$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml
DeviceSystemFile=$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c
DeviceVectorsFile=$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s
DeviceFamily=FM4
DeviceSubFamily=MB9BF560R
```

MB9BF566N

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF560R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF566N_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF566N

__FM4_FAMILY

__MB9BF560R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF560R

MB9BF566R

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF560R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF566R_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF566R

__FM4_FAMILY

__MB9BF560R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF560R

MB9BF567M

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF560R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF567M_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF567M

__FM4_FAMILY

__MB9BF560R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000BFFFF
RAM	0x1FFF4000 - 0x1FFFFFFF
RAM2	0x2003A000 - 0x20045FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF560R

MB9BF567N

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF560R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF567N_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF567N

__FM4_FAMILY

__MB9BF560R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000BFFFF
RAM	0x1FFF4000 - 0x1FFFFFFF
RAM2	0x2003A000 - 0x20045FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF560R

MB9BF567R

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF560R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF567R_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF567R

__FM4_FAMILY

__MB9BF560R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000BFFFF
RAM	0x1FFF4000 - 0x1FFFFFFF
RAM2	0x2003A000 - 0x20045FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF560R

MB9BF568M

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF560R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF568M_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF568M

__FM4_FAMILY

__MB9BF560R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF560R

MB9BF568N

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF560R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF568N_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF568N

__FM4_FAMILY

__MB9BF560R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF560R

MB9BF568R

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c
Family	FM4
Sub Family	MB9BF560R
Loader File	\$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/MB9BF568R_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

MB9BF568R

__FM4_FAMILY

__MB9BF560R_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/MB9B560R/mb9b560r.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/MB9BF560R_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/MB9B560R_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/MB9B560R/system_mb9abxxx.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/MB9B560R_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=MB9BF560R

S6E2C1 Family

- [S6E2C18H0A](#)
- [S6E2C18J0A](#)
- [S6E2C18L0A](#)
- [S6E2C19H0A](#)
- [S6E2C19J0A](#)
- [S6E2C19L0A](#)
- [S6E2C1AH0A](#)
- [S6E2C1AJ0A](#)
- [S6E2C1AL0A](#)

S6E2C18H0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c
Family	FM4
Sub Family	S6E2C1
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C18H0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C18H0A

__FM4_FAMILY

__S6E2C1_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C1

S6E2C18J0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c
Family	FM4
Sub Family	S6E2C1
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C18J0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C18J0A

__FM4_FAMILY

__S6E2C1_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C1

S6E2C18L0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c
Family	FM4
Sub Family	S6E2C1
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C18L0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C18L0A

__FM4_FAMILY

__S6E2C1_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C1

S6E2C19H0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c
Family	FM4
Sub Family	S6E2C1
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C19H0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C19H0A

__FM4_FAMILY

__S6E2C1_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C1

S6E2C19J0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c
Family	FM4
Sub Family	S6E2C1
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C19J0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C19J0A

__FM4_FAMILY

__S6E2C1_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C1

S6E2C19L0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c
Family	FM4
Sub Family	S6E2C1
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C19L0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C19L0A

__FM4_FAMILY

__S6E2C1_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C1

S6E2C1AH0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c
Family	FM4
Sub Family	S6E2C1
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C1AH0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C1AH0A

__FM4_FAMILY

__S6E2C1_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C1

S6E2C1AJ0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c
Family	FM4
Sub Family	S6E2C1
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C1AJ0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C1AJ0A

__FM4_FAMILY

__S6E2C1_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C1

S6E2C1AL0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c
Family	FM4
Sub Family	S6E2C1
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C1AL0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4
 S6E2C1AL0A
 __FM4_FAMILY
 __S6E2C1_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1
 DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C1/s6e2c1.h
 DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C1_Loader.elf
 DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C1_Registers.xml
 DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C1/system_s6e2c1.c
 DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C1_Vectors.s
 DeviceFamily=FM4
 DeviceSubFamily=S6E2C1

S6E2C2 Family

- [S6E2C28H0A](#)
- [S6E2C28J0A](#)
- [S6E2C28L0A](#)
- [S6E2C29H0A](#)
- [S6E2C29J0A](#)
- [S6E2C29L0A](#)
- [S6E2C2AH0A](#)
- [S6E2C2AJ0A](#)
- [S6E2C2AL0A](#)

S6E2C28H0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c
Family	FM4
Sub Family	S6E2C2
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C28H0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C28H0A

__FM4_FAMILY

__S6E2C2_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C2

S6E2C28J0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c
Family	FM4
Sub Family	S6E2C2
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C28J0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C28J0A

__FM4_FAMILY

__S6E2C2_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C2

S6E2C28L0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c
Family	FM4
Sub Family	S6E2C2
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C28L0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C28L0A

__FM4_FAMILY

__S6E2C2_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C2

S6E2C29H0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c
Family	FM4
Sub Family	S6E2C2
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C29H0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C29H0A

__FM4_FAMILY

__S6E2C2_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C2

S6E2C29J0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c
Family	FM4
Sub Family	S6E2C2
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C29J0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C29J0A

__FM4_FAMILY

__S6E2C2_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C2

S6E2C29L0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c
Family	FM4
Sub Family	S6E2C2
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C29L0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C29L0A

__FM4_FAMILY

__S6E2C2_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C2

S6E2C2AH0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c
Family	FM4
Sub Family	S6E2C2
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C2AH0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C2AH0A

__FM4_FAMILY

__S6E2C2_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C2

S6E2C2AJ0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c
Family	FM4
Sub Family	S6E2C2
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C2AJ0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C2AJ0A

__FM4_FAMILY

__S6E2C2_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C2

S6E2C2AL0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c
Family	FM4
Sub Family	S6E2C2
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C2AL0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C2AL0A

__FM4_FAMILY

__S6E2C2_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C2/s6e2c2.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C2_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C2/system_s6e2c2.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C2_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C2

S6E2C3 Family

- [S6E2C38H0A](#)
- [S6E2C38J0A](#)
- [S6E2C38L0A](#)
- [S6E2C39H0A](#)
- [S6E2C39J0A](#)
- [S6E2C39L0A](#)
- [S6E2C3AH0A](#)
- [S6E2C3AJ0A](#)
- [S6E2C3AL0A](#)

S6E2C38H0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c
Family	FM4
Sub Family	S6E2C3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C38H0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C38H0A

__FM4_FAMILY

__S6E2C3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C3

S6E2C38J0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c
Family	FM4
Sub Family	S6E2C3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C38J0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C38J0A

__FM4_FAMILY

__S6E2C3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C3

S6E2C38L0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c
Family	FM4
Sub Family	S6E2C3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C38L0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C38L0A

__FM4_FAMILY

__S6E2C3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C3

S6E2C39H0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c
Family	FM4
Sub Family	S6E2C3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C39H0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C39H0A

__FM4_FAMILY

__S6E2C3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C3

S6E2C39J0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c
Family	FM4
Sub Family	S6E2C3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C39J0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C39J0A

__FM4_FAMILY

__S6E2C3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C3

S6E2C39L0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c
Family	FM4
Sub Family	S6E2C3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C39L0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C39L0A

__FM4_FAMILY

__S6E2C3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C3

S6E2C3AH0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c
Family	FM4
Sub Family	S6E2C3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C3AH0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C3AH0A

__FM4_FAMILY

__S6E2C3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C3

S6E2C3AJ0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c
Family	FM4
Sub Family	S6E2C3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C3AJ0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C3AJ0A

__FM4_FAMILY

__S6E2C3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C3

S6E2C3AL0A

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c
Family	FM4
Sub Family	S6E2C3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C3AL0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C3AL0A

__FM4_FAMILY

__S6E2C3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C3/s6e2c3.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C3_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C3/system_s6e2c3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C3_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C3

S6E2C4 Family

- [S6E2C48H0A](#)
- [S6E2C48J0A](#)
- [S6E2C48L0A](#)
- [S6E2C49H0A](#)
- [S6E2C49J0A](#)
- [S6E2C49L0A](#)
- [S6E2C4AH0A](#)
- [S6E2C4AJ0A](#)
- [S6E2C4AL0A](#)

S6E2C48H0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c
Family	FM4
Sub Family	S6E2C4
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C48H0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C48H0A

__FM4_FAMILY

__S6E2C4_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C4

S6E2C48J0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c
Family	FM4
Sub Family	S6E2C4
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C48J0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C48J0A

__FM4_FAMILY

__S6E2C4_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C4

S6E2C48L0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c
Family	FM4
Sub Family	S6E2C4
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C48L0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C48L0A

__FM4_FAMILY

__S6E2C4_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C4

S6E2C49H0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c
Family	FM4
Sub Family	S6E2C4
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C49H0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C49H0A

__FM4_FAMILY

__S6E2C4_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C4

S6E2C49J0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c
Family	FM4
Sub Family	S6E2C4
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C49J0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C49J0A

__FM4_FAMILY

__S6E2C4_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C4

S6E2C49L0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c
Family	FM4
Sub Family	S6E2C4
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C49L0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C49L0A

__FM4_FAMILY

__S6E2C4_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C4

S6E2C4AH0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c
Family	FM4
Sub Family	S6E2C4
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C4AH0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C4AH0A

__FM4_FAMILY

__S6E2C4_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C4

S6E2C4AJ0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c
Family	FM4
Sub Family	S6E2C4
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C4AJ0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C4AJ0A

__FM4_FAMILY

__S6E2C4_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C4

S6E2C4AL0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c
Family	FM4
Sub Family	S6E2C4
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C4AL0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C4AL0A

__FM4_FAMILY

__S6E2C4_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C4/s6e2c4.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C4_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C4_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C4/system_s6e2c4.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C4_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C4

S6E2C5 Family

- [S6E2C58H0A](#)
- [S6E2C58J0A](#)
- [S6E2C58L0A](#)
- [S6E2C59H0A](#)
- [S6E2C59J0A](#)
- [S6E2C59L0A](#)
- [S6E2C5AH0A](#)
- [S6E2C5AJ0A](#)
- [S6E2C5AL0A](#)

S6E2C58H0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c
Family	FM4
Sub Family	S6E2C5
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C58H0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C58H0A

__FM4_FAMILY

__S6E2C5_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C5

S6E2C58J0A

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c
Family	FM4
Sub Family	S6E2C5
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C58J0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
S6E2C58J0A	
__FM4_FAMILY	
__S6E2C5_SUBFAMILY	

Memory Segments	
FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5
DeviceHeaderFile	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h
DeviceLoaderFile	\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml
DeviceSystemFile	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c
DeviceVectorsFile	\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s
DeviceFamily	FM4
DeviceSubFamily	S6E2C5

S6E2C58L0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c
Family	FM4
Sub Family	S6E2C5
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C58L0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C58L0A

__FM4_FAMILY

__S6E2C5_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C5

S6E2C59H0A

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c
Family	FM4
Sub Family	S6E2C5
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C59H0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
S6E2C59H0A	
__FM4_FAMILY	
__S6E2C5_SUBFAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5
DeviceHeaderFile	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h
DeviceLoaderFile	\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml
DeviceSystemFile	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c
DeviceVectorsFile	\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s
DeviceFamily	FM4
DeviceSubFamily	S6E2C5

S6E2C59J0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c
Family	FM4
Sub Family	S6E2C5
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C59J0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C59J0A

__FM4_FAMILY

__S6E2C5_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C5

S6E2C59L0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c
Family	FM4
Sub Family	S6E2C5
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C59L0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C59L0A

__FM4_FAMILY

__S6E2C5_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C5

S6E2C5AH0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c
Family	FM4
Sub Family	S6E2C5
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C5AH0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C5AH0A

__FM4_FAMILY

__S6E2C5_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C5

S6E2C5AJ0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c
Family	FM4
Sub Family	S6E2C5
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C5AJ0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C5AJ0A

__FM4_FAMILY

__S6E2C5_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C5

S6E2C5AL0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c
Family	FM4
Sub Family	S6E2C5
Loader File	\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2C5AL0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2C5AL0A

__FM4_FAMILY

__S6E2C5_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2C5/s6e2c5.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2C5_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2C5_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2C5/system_s6e2c5.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2C5_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2C5

S6E2CC Family

- [S6E2CC8H0A](#)
- [S6E2CC8J0A](#)
- [S6E2CC8L0A](#)
- [S6E2CC9H0A](#)
- [S6E2CC9J0A](#)
- [S6E2CC9L0A](#)
- [S6E2CCAH0A](#)
- [S6E2CCAJ0A](#)
- [S6E2CCAL0A](#)

S6E2CC8H0A

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c
Family	FM4
Sub Family	S6E2CC
Loader File	\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2CC8H0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2CC8H0A

__FM4_FAMILY

__S6E2CC_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2CC

S6E2CC8J0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c
Family	FM4
Sub Family	S6E2CC
Loader File	\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2CC8J0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2CC8J0A

__FM4_FAMILY

__S6E2CC_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2CC

S6E2CC8L0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c
Family	FM4
Sub Family	S6E2CC
Loader File	\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2CC8L0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2CC8L0A

__FM4_FAMILY

__S6E2CC_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2CC

S6E2CC9H0A

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c
Family	FM4
Sub Family	S6E2CC
Loader File	\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2CC9H0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2CC9H0A

__FM4_FAMILY

__S6E2CC_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2CC

S6E2CC9J0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c
Family	FM4
Sub Family	S6E2CC
Loader File	\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2CC9J0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2CC9J0A

__FM4_FAMILY

__S6E2CC_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC
DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h
DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml
DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c
DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s
DeviceFamily=FM4
DeviceSubFamily=S6E2CC

S6E2CC9L0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c
Family	FM4
Sub Family	S6E2CC
Loader File	\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2CC9L0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2CC9L0A

__FM4_FAMILY

__S6E2CC_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0017FFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2CC

S6E2CCA0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c
Family	FM4
Sub Family	S6E2CC
Loader File	\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2CCA0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2CCA0A

__FM4_FAMILY

__S6E2CC_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2CC

S6E2CCAJ0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c
Family	FM4
Sub Family	S6E2CC
Loader File	\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2CCAJ0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2CCAJ0A

__FM4_FAMILY

__S6E2CC_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2CC

S6E2CCAL0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c
Family	FM4
Sub Family	S6E2CC
Loader File	\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2CCAL0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2CCAL0A

__FM4_FAMILY

__S6E2CC_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x001FFFFFF
FLASH2	0x00406000 - 0x0040FFFF
RAM	0x1FFD0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2CC/s6e2cc.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2CC_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2CC_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2CC/system_s6e2cc.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2CC_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2CC

S6E2D3 Family

- [S6E2D35G0A](#)
- [S6E2D35GAA](#)
- [S6E2D35GJA](#)
- [S6E2D35J0A](#)
- [S6E2D35JAA](#)

S6E2D35G0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3/s6e2d3.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D3/system_s6e2d3.c
Family	FM4
Sub Family	S6E2D3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2D3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2D35G0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2D3_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2D3_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2D35G0A

__FM4_FAMILY

__S6E2D3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3/s6e2d3.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2D3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2D3_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D3/system_s6e2d3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2D3_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2D3

S6E2D35GAA

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3/s6e2d3.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D3/system_s6e2d3.c
Family	FM4
Sub Family	S6E2D3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2D3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2D35GAA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2D3_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2D3_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2D35GAA

__FM4_FAMILY

__S6E2D3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3/s6e2d3.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2D3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2D3_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D3/system_s6e2d3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2D3_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2D3

S6E2D35GJA

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3/s6e2d3.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D3/system_s6e2d3.c
Family	FM4
Sub Family	S6E2D3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2D3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2D35GJA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2D3_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2D3_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2D35GJA

__FM4_FAMILY

__S6E2D3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3/s6e2d3.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2D3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2D3_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D3/system_s6e2d3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2D3_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2D3

S6E2D35J0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3/s6e2d3.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D3/system_s6e2d3.c
Family	FM4
Sub Family	S6E2D3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2D3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2D35J0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2D3_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2D3_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2D35J0A

__FM4_FAMILY

__S6E2D3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3/s6e2d3.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2D3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2D3_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D3/system_s6e2d3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2D3_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2D3

S6E2D35JAA

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3/s6e2d3.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D3/system_s6e2d3.c
Family	FM4
Sub Family	S6E2D3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2D3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2D35JAA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2D3_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2D3_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2D35JAA

__FM4_FAMILY

__S6E2D3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D3/s6e2d3.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2D3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2D3_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D3/system_s6e2d3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2D3_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2D3

S6E2D5 Family

- [S6E2D55G0A](#)
- [S6E2D55GAA](#)
- [S6E2D55GJA](#)
- [S6E2D55J0A](#)
- [S6E2D55JAA](#)

S6E2D55G0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5/s6e2d5.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D5/system_s6e2d5.c
Family	FM4
Sub Family	S6E2D5
Loader File	\$(TargetsDir)/FM4/Loader/S6E2D5_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2D55G0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2D5_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2D5_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2D55G0A

__FM4_FAMILY

__S6E2D5_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5/s6e2d5.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2D5_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2D5_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D5/system_s6e2d5.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2D5_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2D5

S6E2D55GAA

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5/s6e2d5.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D5/system_s6e2d5.c
Family	FM4
Sub Family	S6E2D5
Loader File	\$(TargetsDir)/FM4/Loader/S6E2D5_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2D55GAA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2D5_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2D5_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2D55GAA

__FM4_FAMILY

__S6E2D5_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5/s6e2d5.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2D5_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2D5_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D5/system_s6e2d5.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2D5_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2D5

S6E2D55GJA

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5/s6e2d5.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D5/system_s6e2d5.c
Family	FM4
Sub Family	S6E2D5
Loader File	\$(TargetsDir)/FM4/Loader/S6E2D5_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2D55GJA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2D5_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2D5_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2D55GJA

__FM4_FAMILY

__S6E2D5_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5/s6e2d5.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2D5_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2D5_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D5/system_s6e2d5.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2D5_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2D5

S6E2D55J0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5/s6e2d5.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D5/system_s6e2d5.c
Family	FM4
Sub Family	S6E2D5
Loader File	\$(TargetsDir)/FM4/Loader/S6E2D5_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2D55J0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2D5_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2D5_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2D55J0A

__FM4_FAMILY

__S6E2D5_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5/s6e2d5.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2D5_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2D5_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D5/system_s6e2d5.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2D5_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2D5

S6E2D55JAA

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5/s6e2d5.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D5/system_s6e2d5.c
Family	FM4
Sub Family	S6E2D5
Loader File	\$(TargetsDir)/FM4/Loader/S6E2D5_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2D55JAA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2D5_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2D5_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2D55JAA

__FM4_FAMILY

__S6E2D5_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2D5/s6e2d5.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2D5_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2D5_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2D5/system_s6e2d5.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2D5_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2D5

S6E2DF Family

- [S6E2DF5G0A](#)
- [S6E2DF5GAA](#)
- [S6E2DF5GJA](#)
- [S6E2DF5J0A](#)
- [S6E2DF5JAA](#)

S6E2DF5G0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF/s6e2df.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DF/system_s6e2df.c
Family	FM4
Sub Family	S6E2DF
Loader File	\$(TargetsDir)/FM4/Loader/S6E2DF_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2DF5G0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2DF_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2DF_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2DF5G0A

__FM4_FAMILY

__S6E2DF_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF/s6e2df.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2DF_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2DF_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DF/system_s6e2df.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2DF_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2DF

S6E2DF5GAA

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF/s6e2df.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DF/system_s6e2df.c
Family	FM4
Sub Family	S6E2DF
Loader File	\$(TargetsDir)/FM4/Loader/S6E2DF_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2DF5GAA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2DF_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2DF_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2DF5GAA

__FM4_FAMILY

__S6E2DF_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF/s6e2df.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2DF_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2DF_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DF/system_s6e2df.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2DF_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2DF

S6E2DF5GJA

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF/s6e2df.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DF/system_s6e2df.c
Family	FM4
Sub Family	S6E2DF
Loader File	\$(TargetsDir)/FM4/Loader/S6E2DF_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2DF5GJA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2DF_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2DF_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2DF5GJA

__FM4_FAMILY

__S6E2DF_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF/s6e2df.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2DF_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2DF_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DF/system_s6e2df.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2DF_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2DF

S6E2DF5J0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF/s6e2df.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DF/system_s6e2df.c
Family	FM4
Sub Family	S6E2DF
Loader File	\$(TargetsDir)/FM4/Loader/S6E2DF_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2DF5J0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2DF_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2DF_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2DF5J0A

__FM4_FAMILY

__S6E2DF_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF/s6e2df.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2DF_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2DF_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DF/system_s6e2df.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2DF_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2DF

S6E2DF5JAA

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF/s6e2df.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DF/system_s6e2df.c
Family	FM4
Sub Family	S6E2DF
Loader File	\$(TargetsDir)/FM4/Loader/S6E2DF_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2DF5JAA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2DF_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2DF_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2DF5JAA

__FM4_FAMILY

__S6E2DF_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DF/s6e2df.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2DF_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2DF_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DF/system_s6e2df.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2DF_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2DF

S6E2DH Family

- [S6E2DH5G0A](#)
- [S6E2DH5GAA](#)
- [S6E2DH5GJA](#)
- [S6E2DH5J0A](#)
- [S6E2DH5JAA](#)

S6E2DH5G0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH/s6e2dh.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DH/system_s6e2dh.c
Family	FM4
Sub Family	S6E2DH
Loader File	\$(TargetsDir)/FM4/Loader/S6E2DH_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2DH5G0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2DH_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2DH_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2DH5G0A

__FM4_FAMILY

__S6E2DH_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH/s6e2dh.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2DH_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2DH_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DH/system_s6e2dh.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2DH_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2DH

S6E2DH5GAA

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH/s6e2dh.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DH/system_s6e2dh.c
Family	FM4
Sub Family	S6E2DH
Loader File	\$(TargetsDir)/FM4/Loader/S6E2DH_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2DH5GAA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2DH_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2DH_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2DH5GAA

__FM4_FAMILY

__S6E2DH_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH/s6e2dh.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2DH_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2DH_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DH/system_s6e2dh.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2DH_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2DH

S6E2DH5GJA

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH/s6e2dh.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DH/system_s6e2dh.c
Family	FM4
Sub Family	S6E2DH
Loader File	\$(TargetsDir)/FM4/Loader/S6E2DH_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2DH5GJA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2DH_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2DH_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2DH5GJA

__FM4_FAMILY

__S6E2DH_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH/s6e2dh.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2DH_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2DH_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DH/system_s6e2dh.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2DH_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2DH

S6E2DH5J0A

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH/s6e2dh.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DH/system_s6e2dh.c
Family	FM4
Sub Family	S6E2DH
Loader File	\$(TargetsDir)/FM4/Loader/S6E2DH_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2DH5J0A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2DH_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2DH_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2DH5J0A

__FM4_FAMILY

__S6E2DH_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH/s6e2dh.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2DH_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2DH_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DH/system_s6e2dh.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2DH_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2DH

S6E2DH5JAA

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH/s6e2dh.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DH/system_s6e2dh.c
Family	FM4
Sub Family	S6E2DH
Loader File	\$(TargetsDir)/FM4/Loader/S6E2DH_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2DH5JAA_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2DH_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2DH_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2DH5JAA

__FM4_FAMILY

__S6E2DH_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0005FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x20040000 - 0x20040FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2DH/s6e2dh.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2DH_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2DH_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2DH/system_s6e2dh.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2DH_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2DH

S6E2G2 Family

- [S6E2G26H](#)
- [S6E2G28H](#)
- [S6E2G26J](#)
- [S6E2G28J](#)

S6E2G26H

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G2/S6E2G2xH/s6e2g2xh.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G2/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2G2/system_s6e2g2.c
Family	FM4
Sub Family	S6E2G2
Loader File	\$(TargetsDir)/FM4/Loader/S6E2G2_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2G26H_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2G2xH_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2G2xH_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2G26H

__FM4_FAMILY

__S6E2G2_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G2/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G2/S6E2G2xH/s6e2g2xh.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2G2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2G2xH_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2G2/system_s6e2g2.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2G2xH_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2G2

S6E2G28H

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G2/S6E2G2xH/s6e2g2xh.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G2/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2G2/system_s6e2g2.c
Family	FM4
Sub Family	S6E2G2
Loader File	\$(TargetsDir)/FM4/Loader/S6E2G2_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2G28H_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2G2xH_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2G2xH_Vectors.s

Preprocessor Definitions
ARM_MATH_CM4
S6E2G28H
__FM4_FAMILY
__S6E2G2_SUBFAMILY

Memory Segments	
FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G2/Common
DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G2/S6E2G2xH/s6e2g2xh.h
DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2G2_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2G2xH_Registers.xml
DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2G2/system_s6e2g2.c
DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2G2xH_Vectors.s
DeviceFamily=FM4
DeviceSubFamily=S6E2G2

S6E2G26J

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G2/S6E2G2xJ/s6e2g2xj.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G2/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2G2/system_s6e2g2.c
Family	FM4
Sub Family	S6E2G2
Loader File	\$(TargetsDir)/FM4/Loader/S6E2G2_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2G26J_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2G2xJ_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2G2xJ_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2G26J

__FM4_FAMILY

__S6E2G2_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G2/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G2/S6E2G2xJ/s6e2g2xj.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2G2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2G2xJ_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2G2/system_s6e2g2.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2G2xJ_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2G2

S6E2G28J

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G2/S6E2G2xJ/s6e2g2xj.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G2/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2G2/system_s6e2g2.c
Family	FM4
Sub Family	S6E2G2
Loader File	\$(TargetsDir)/FM4/Loader/S6E2G2_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2G28J_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2G2xJ_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2G2xJ_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2G28J

__FM4_FAMILY

__S6E2G2_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G2/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G2/S6E2G2xJ/s6e2g2xj.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2G2_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2G2xJ_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2G2/system_s6e2g2.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2G2xJ_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2G2

S6E2G3 Family

- [S6E2G36H](#)
- [S6E2G38H](#)
- [S6E2G36J](#)
- [S6E2G38J](#)

S6E2G36H

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G3/S6E2G3xH/s6e2g3xh.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G3/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2G3/system_s6e2g3.c
Family	FM4
Sub Family	S6E2G3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2G3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2G36H_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2G3xH_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2G3xH_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2G36H

__FM4_FAMILY

__S6E2G3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G3/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G3/S6E2G3xH/s6e2g3xh.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2G3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2G3xH_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2G3/system_s6e2g3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2G3xH_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2G3

S6E2G38H

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G3/S6E2G3xH/s6e2g3xh.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G3/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2G3/system_s6e2g3.c
Family	FM4
Sub Family	S6E2G3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2G3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2G38H_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2G3xH_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2G3xH_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2G38H

__FM4_FAMILY

__S6E2G3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G3/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G3/S6E2G3xH/s6e2g3xh.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2G3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2G3xH_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2G3/system_s6e2g3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2G3xH_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2G3

S6E2G36J

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G3/S6E2G3xJ/s6e2g3xj.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G3/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2G3/system_s6e2g3.c
Family	FM4
Sub Family	S6E2G3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2G3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2G36J_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2G3xJ_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2G3xJ_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2G36J

__FM4_FAMILY

__S6E2G3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G3/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G3/S6E2G3xJ/s6e2g3xj.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2G3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2G3xJ_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2G3/system_s6e2g3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2G3xJ_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2G3

S6E2G38J

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G3/S6E2G3xJ/s6e2g3xj.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G3/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2G3/system_s6e2g3.c
Family	FM4
Sub Family	S6E2G3
Loader File	\$(TargetsDir)/FM4/Loader/S6E2G3_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2G38J_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2G3xJ_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2G3xJ_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2G38J

__FM4_FAMILY

__S6E2G3_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G3/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2G3/S6E2G3xJ/s6e2g3xj.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2G3_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2G3xJ_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2G3/system_s6e2g3.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2G3xJ_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2G3

S6E2GH Family

- [S6E2GH6H](#)
- [S6E2GH8H](#)
- [S6E2GH6J](#)
- [S6E2GH8J](#)

S6E2GH6H

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GH/S6E2GHxH/s6e2ghxh.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GH/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GH/system_s6e2gh.c
Family	FM4
Sub Family	S6E2GH
Loader File	\$(TargetsDir)/FM4/Loader/S6E2GH_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2GH6H_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2GHxH_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2GHxH_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2GH6H

__FM4_FAMILY

__S6E2GH_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GH/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GH/S6E2GHxH/s6e2ghxh.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2GH_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2GHxH_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GH/system_s6e2gh.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2GHxH_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2GH

S6E2GH8H

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GH/S6E2GHxH/s6e2ghxh.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GH/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GH/system_s6e2gh.c
Family	FM4
Sub Family	S6E2GH
Loader File	\$(TargetsDir)/FM4/Loader/S6E2GH_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2GH8H_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2GHxH_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2GHxH_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2GH8H

__FM4_FAMILY

__S6E2GH_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GH/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GH/S6E2GHxH/s6e2ghxh.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2GH_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2GHxH_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GH/system_s6e2gh.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2GHxH_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2GH

S6E2GH6J

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GH/S6E2GHxJ/s6e2ghxj.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GH/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GH/system_s6e2gh.c
Family	FM4
Sub Family	S6E2GH
Loader File	\$(TargetsDir)/FM4/Loader/S6E2GH_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2GH6J_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2GHxJ_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2GHxJ_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2GH6J

__FM4_FAMILY

__S6E2GH_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GH/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GH/S6E2GHxJ/s6e2ghxj.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2GH_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2GHxJ_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GH/system_s6e2gh.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2GHxJ_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2GH

S6E2GH8J

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GH/S6E2GHxJ/s6e2ghxj.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GH/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GH/system_s6e2gh.c
Family	FM4
Sub Family	S6E2GH
Loader File	\$(TargetsDir)/FM4/Loader/S6E2GH_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2GH8J_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2GHxJ_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2GHxJ_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2GH8J

__FM4_FAMILY

__S6E2GH_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GH/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GH/S6E2GHxJ/s6e2ghxj.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2GH_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2GHxJ_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GH/system_s6e2gh.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2GHxJ_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2GH

S6E2GK Family

- [S6E2GK6H](#)
- [S6E2GK8H](#)
- [S6E2GK6J](#)
- [S6E2GK8J](#)

S6E2GK6H

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GK/S6E2GKxH/s6e2gkxh.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GK/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GK/system_s6e2gk.c
Family	FM4
Sub Family	S6E2GK
Loader File	\$(TargetsDir)/FM4/Loader/S6E2GK_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2GK6H_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2GKxH_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2GKxH_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2GK6H

__FM4_FAMILY

__S6E2GK_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GK/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GK/S6E2GKxH/s6e2gkxh.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2GK_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2GKxH_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GK/system_s6e2gk.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2GKxH_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2GK

S6E2GK8H

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GK/S6E2GKxH/s6e2gkxh.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GK/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GK/system_s6e2gk.c
Family	FM4
Sub Family	S6E2GK
Loader File	\$(TargetsDir)/FM4/Loader/S6E2GK_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2GK8H_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2GKxH_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2GKxH_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2GK8H

__FM4_FAMILY

__S6E2GK_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GK/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GK/S6E2GKxH/s6e2gkxh.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2GK_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2GKxH_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GK/system_s6e2gk.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2GKxH_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2GK

S6E2GK6J

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GK/S6E2GKxJ/s6e2gkxj.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GK/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GK/system_s6e2gk.c
Family	FM4
Sub Family	S6E2GK
Loader File	\$(TargetsDir)/FM4/Loader/S6E2GK_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2GK6J_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2GKxJ_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2GKxJ_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2GK6J

__FM4_FAMILY

__S6E2GK_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GK/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GK/S6E2GKxJ/s6e2gkxj.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2GK_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2GKxJ_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GK/system_s6e2gk.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2GKxJ_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2GK

S6E2GK8J

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GK/S6E2GKxJ/s6e2gkxj.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GK/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GK/system_s6e2gk.c
Family	FM4
Sub Family	S6E2GK
Loader File	\$(TargetsDir)/FM4/Loader/S6E2GK_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2GK8J_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2GKxJ_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2GKxJ_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2GK8J

__FM4_FAMILY

__S6E2GK_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GK/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GK/S6E2GKxJ/s6e2gkxj.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2GK_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2GKxJ_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GK/system_s6e2gk.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2GKxJ_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2GK

S6E2GM Family

- [S6E2GM6H](#)
- [S6E2GM8H](#)
- [S6E2GM6J](#)
- [S6E2GM8J](#)

S6E2GM6H

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GM/S6E2GMxH/s6e2gmhx.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GM/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GM/system_s6e2gm.c
Family	FM4
Sub Family	S6E2GM
Loader File	\$(TargetsDir)/FM4/Loader/S6E2GM_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2GM6H_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2GMxH_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2GMxH_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2GM6H

__FM4_FAMILY

__S6E2GM_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GM/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GM/S6E2GMxH/s6e2gmhx.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2GM_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2GMxH_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GM/system_s6e2gm.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2GMxH_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2GM

S6E2GM8H

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GM/S6E2GMxH/s6e2gmh.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GM/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GM/system_s6e2gm.c
Family	FM4
Sub Family	S6E2GM
Loader File	\$(TargetsDir)/FM4/Loader/S6E2GM_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2GM8H_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2GMxH_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2GMxH_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2GM8H

__FM4_FAMILY

__S6E2GM_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GM/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GM/S6E2GMxH/s6e2gmh.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2GM_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2GMxH_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GM/system_s6e2gm.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2GMxH_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2GM

S6E2GM6J

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GM/S6E2GMxJ/s6e2gmj.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GM/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GM/system_s6e2gm.c
Family	FM4
Sub Family	S6E2GM
Loader File	\$(TargetsDir)/FM4/Loader/S6E2GM_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2GM6J_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2GMxJ_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2GMxJ_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2GM6J

__FM4_FAMILY

__S6E2GM_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFF0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GM/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GM/S6E2GMxJ/s6e2gmj.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2GM_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2GMxJ_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GM/system_s6e2gm.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2GMxJ_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2GM

S6E2GM8J

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GM/S6E2GMxJ/s6e2gmj.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GM/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GM/system_s6e2gm.c
Family	FM4
Sub Family	S6E2GM
Loader File	\$(TargetsDir)/FM4/Loader/S6E2GM_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2GM8J_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2GMxJ_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2GMxJ_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4
 S6E2GM8J
 __FM4_FAMILY
 __S6E2GM_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x000FFFFFF
FLASH2	0x00406000 - 0x00407FFF
RAM	0x1FFE0000 - 0x1FFFFFFF
RAM2	0x20038000 - 0x20047FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GM/Common
 DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2GM/S6E2GMxJ/s6e2gmj.h
 DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2GM_Loader.elf
 DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2GMxJ_Registers.xml
 DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2GM/system_s6e2gm.c
 DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2GMxJ_Vectors.s
 DeviceFamily=FM4
 DeviceSubFamily=S6E2GM

S6E2H1 Family

- [S6E2H14E](#)
- [S6E2H16E](#)
- [S6E2H14F](#)
- [S6E2H16F](#)
- [S6E2H14G](#)
- [S6E2H16G](#)

S6E2H14E

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/S6E2H1xE/s6e2h1xe.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H1/system_s6e2h1.c
Family	FM4
Sub Family	S6E2H1
Loader File	\$(TargetsDir)/FM4/Loader/S6E2H1_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2H14E_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2H1xE_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2H1xE_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2H14E

__FM4_FAMILY

__S6E2H1_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/S6E2H1xE/s6e2h1xe.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2H1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2H1xE_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H1/system_s6e2h1.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2H1xE_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2H1

S6E2H16E

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/S6E2H1xE/s6e2h1xe.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H1/system_s6e2h1.c
Family	FM4
Sub Family	S6E2H1
Loader File	\$(TargetsDir)/FM4/Loader/S6E2H1_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2H16E_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2H1xE_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2H1xE_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2H16E

__FM4_FAMILY

__S6E2H1_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/S6E2H1xE/s6e2h1xe.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2H1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2H1xE_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H1/system_s6e2h1.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2H1xE_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2H1

S6E2H14F

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/S6E2H1xF/s6e2h1xf.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H1/system_s6e2h1.c
Family	FM4
Sub Family	S6E2H1
Loader File	\$(TargetsDir)/FM4/Loader/S6E2H1_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2H14F_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2H1xF_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2H1xF_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2H14F

__FM4_FAMILY

__S6E2H1_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/S6E2H1xF/s6e2h1xf.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2H1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2H1xF_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H1/system_s6e2h1.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2H1xF_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2H1

S6E2H16F

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/S6E2H1xF/s6e2h1xf.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H1/system_s6e2h1.c
Family	FM4
Sub Family	S6E2H1
Loader File	\$(TargetsDir)/FM4/Loader/S6E2H1_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2H16F_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2H1xF_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2H1xF_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2H16F

__FM4_FAMILY

__S6E2H1_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/S6E2H1xF/s6e2h1xf.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2H1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2H1xF_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H1/system_s6e2h1.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2H1xF_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2H1

S6E2H14G

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/S6E2H1xG/s6e2h1xg.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H1/system_s6e2h1.c
Family	FM4
Sub Family	S6E2H1
Loader File	\$(TargetsDir)/FM4/Loader/S6E2H1_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2H14G_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2H1xG_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2H1xG_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2H14G

__FM4_FAMILY

__S6E2H1_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/S6E2H1xG/s6e2h1xg.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2H1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2H1xG_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H1/system_s6e2h1.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2H1xG_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2H1

S6E2H16G

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/S6E2H1xG/s6e2h1xg.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H1/system_s6e2h1.c
Family	FM4
Sub Family	S6E2H1
Loader File	\$(TargetsDir)/FM4/Loader/S6E2H1_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2H16G_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2H1xG_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2H1xG_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2H16G

__FM4_FAMILY

__S6E2H1_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H1/S6E2H1xG/s6e2h1xg.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2H1_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2H1xG_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H1/system_s6e2h1.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2H1xG_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2H1

S6E2H4 Family

- [S6E2H44E](#)
- [S6E2H46E](#)
- [S6E2H44F](#)
- [S6E2H46F](#)
- [S6E2H44G](#)
- [S6E2H46G](#)

S6E2H44E

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/S6E2H4xE/s6e2h4xe.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H4/system_s6e2h4.c
Family	FM4
Sub Family	S6E2H4
Loader File	\$(TargetsDir)/FM4/Loader/S6E2H4_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2H44E_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2H4xE_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2H4xE_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2H44E

__FM4_FAMILY

__S6E2H4_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/S6E2H4xE/s6e2h4xe.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2H4_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2H4xE_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H4/system_s6e2h4.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2H4xE_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2H4

S6E2H46E

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/S6E2H4xE/s6e2h4xe.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H4/system_s6e2h4.c
Family	FM4
Sub Family	S6E2H4
Loader File	\$(TargetsDir)/FM4/Loader/S6E2H4_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2H46E_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2H4xE_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2H4xE_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2H46E

__FM4_FAMILY

__S6E2H4_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/S6E2H4xE/s6e2h4xe.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2H4_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2H4xE_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H4/system_s6e2h4.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2H4xE_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2H4

S6E2H44F

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/S6E2H4xF/s6e2h4xf.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H4/system_s6e2h4.c
Family	FM4
Sub Family	S6E2H4
Loader File	\$(TargetsDir)/FM4/Loader/S6E2H4_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2H44F_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2H4xF_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2H4xF_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2H44F

__FM4_FAMILY

__S6E2H4_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/S6E2H4xF/s6e2h4xf.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2H4_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2H4xF_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H4/system_s6e2h4.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2H4xF_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2H4

S6E2H46F

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/S6E2H4xF/s6e2h4xf.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H4/system_s6e2h4.c
Family	FM4
Sub Family	S6E2H4
Loader File	\$(TargetsDir)/FM4/Loader/S6E2H4_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2H46F_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2H4xF_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2H4xF_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2H46F

__FM4_FAMILY

__S6E2H4_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/S6E2H4xF/s6e2h4xf.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2H4_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2H4xF_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H4/system_s6e2h4.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2H4xF_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2H4

S6E2H44G

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/S6E2H4xG/s6e2h4xg.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H4/system_s6e2h4.c
Family	FM4
Sub Family	S6E2H4
Loader File	\$(TargetsDir)/FM4/Loader/S6E2H4_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2H44G_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2H4xG_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2H4xG_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2H44G

__FM4_FAMILY

__S6E2H4_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/S6E2H4xG/s6e2h4xg.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2H4_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2H4xG_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H4/system_s6e2h4.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2H4xG_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2H4

S6E2H46G

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/S6E2H4xG/s6e2h4xg.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H4/system_s6e2h4.c
Family	FM4
Sub Family	S6E2H4
Loader File	\$(TargetsDir)/FM4/Loader/S6E2H4_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2H46G_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2H4xG_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2H4xG_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2H46G

__FM4_FAMILY

__S6E2H4_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2H4/S6E2H4xG/s6e2h4xg.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2H4_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2H4xG_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2H4/system_s6e2h4.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2H4xG_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2H4

S6E2HE Family

- [S6E2HE4E](#)
- [S6E2HE6E](#)
- [S6E2HE4F](#)
- [S6E2HE6F](#)
- [S6E2HE4G](#)
- [S6E2HE6G](#)

S6E2HE4E

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/S6E2HExE/s6e2hexe.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HE/system_s6e2he.c
Family	FM4
Sub Family	S6E2HE
Loader File	\$(TargetsDir)/FM4/Loader/S6E2HE_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2HE4E_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2HExE_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2HExE_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2HE4E

__FM4_FAMILY

__S6E2HE_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/S6E2HExE/s6e2hexe.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2HE_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2HExE_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HE/system_s6e2he.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2HExE_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2HE

S6E2HE6E

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/S6E2HExE/s6e2hexe.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HE/system_s6e2he.c
Family	FM4
Sub Family	S6E2HE
Loader File	\$(TargetsDir)/FM4/Loader/S6E2HE_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2HE6E_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2HExE_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2HExE_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2HE6E

__FM4_FAMILY

__S6E2HE_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/S6E2HExE/s6e2hexe.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2HE_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2HExE_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HE/system_s6e2he.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2HExE_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2HE

S6E2HE4F

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/S6E2HExF/s6e2hexf.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HE/system_s6e2he.c
Family	FM4
Sub Family	S6E2HE
Loader File	\$(TargetsDir)/FM4/Loader/S6E2HE_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2HE4F_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2HExF_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2HExF_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2HE4F

__FM4_FAMILY

__S6E2HE_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/S6E2HExF/s6e2hexf.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2HE_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2HExF_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HE/system_s6e2he.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2HExF_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2HE

S6E2HE6F

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/S6E2HExF/s6e2hexf.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HE/system_s6e2he.c
Family	FM4
Sub Family	S6E2HE
Loader File	\$(TargetsDir)/FM4/Loader/S6E2HE_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2HE6F_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2HExF_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2HExF_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2HE6F

__FM4_FAMILY

__S6E2HE_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/S6E2HExF/s6e2hexf.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2HE_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2HExF_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HE/system_s6e2he.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2HExF_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2HE

S6E2HE4G

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/S6E2HEXG/s6e2hexg.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HE/system_s6e2he.c
Family	FM4
Sub Family	S6E2HE
Loader File	\$(TargetsDir)/FM4/Loader/S6E2HE_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2HE4G_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2HEXG_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2HEXG_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2HE4G

__FM4_FAMILY

__S6E2HE_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/S6E2HEXG/s6e2hexg.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2HE_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2HEXG_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HE/system_s6e2he.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2HEXG_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2HE

S6E2HE6G

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/S6E2HEXG/s6e2hexg.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HE/system_s6e2he.c
Family	FM4
Sub Family	S6E2HE
Loader File	\$(TargetsDir)/FM4/Loader/S6E2HE_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2HE6G_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2HEXG_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2HEXG_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2HE6G

__FM4_FAMILY

__S6E2HE_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HE/S6E2HEXG/s6e2hexg.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2HE_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2HEXG_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HE/system_s6e2he.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2HEXG_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2HE

S6E2HG Family

- [S6E2HG4E](#)
- [S6E2HG6E](#)
- [S6E2HG4F](#)
- [S6E2HG6F](#)
- [S6E2HG4G](#)
- [S6E2HG6G](#)

S6E2HG4E

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/S6E2HGxE/s6e2hgxe.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HG/system_s6e2hg.c
Family	FM4
Sub Family	S6E2HG
Loader File	\$(TargetsDir)/FM4/Loader/S6E2HG_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2HG4E_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2HGxE_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2HGxE_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2HG4E

__FM4_FAMILY

__S6E2HG_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/S6E2HGxE/s6e2hgxe.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2HG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2HGxE_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HG/system_s6e2hg.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2HGxE_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2HG

S6E2HG6E

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/S6E2HGxE/s6e2hgxe.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HG/system_s6e2hg.c
Family	FM4
Sub Family	S6E2HG
Loader File	\$(TargetsDir)/FM4/Loader/S6E2HG_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2HG6E_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2HGxE_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2HGxE_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2HG6E

__FM4_FAMILY

__S6E2HG_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/S6E2HGxE/s6e2hgxe.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2HG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2HGxE_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HG/system_s6e2hg.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2HGxE_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2HG

S6E2HG4F

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/S6E2HGxF/s6e2hgxf.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HG/system_s6e2hg.c
Family	FM4
Sub Family	S6E2HG
Loader File	\$(TargetsDir)/FM4/Loader/S6E2HG_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2HG4F_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2HGxF_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2HGxF_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2HG4F

__FM4_FAMILY

__S6E2HG_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/S6E2HGxF/s6e2hgxf.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2HG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2HGxF_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HG/system_s6e2hg.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2HGxF_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2HG

S6E2HG6F

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/S6E2HGxF/s6e2hgxf.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HG/system_s6e2hg.c
Family	FM4
Sub Family	S6E2HG
Loader File	\$(TargetsDir)/FM4/Loader/S6E2HG_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2HG6F_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2HGxF_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2HGxF_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM4	
S6E2HG6F	
__FM4_FAMILY	
__S6E2HG_SUBFAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/Common
DeviceHeaderFile	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/S6E2HGxF/s6e2hgxf.h
DeviceLoaderFile	\$(TargetsDir)/FM4/Loader/S6E2HG_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/FM4/XML/S6E2HGxF_Registers.xml
DeviceSystemFile	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HG/system_s6e2hg.c
DeviceVectorsFile	\$(TargetsDir)/FM4/Source/S6E2HGxF_Vectors.s
DeviceFamily	FM4
DeviceSubFamily	S6E2HG

S6E2HG4G

Device Details

CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/S6E2HGxG/s6e2hg.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HG/system_s6e2hg.c
Family	FM4
Sub Family	S6E2HG
Loader File	\$(TargetsDir)/FM4/Loader/S6E2HG_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2HG4G_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2HGxG_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2HGxG_Vectors.s

Preprocessor Definitions

ARM_MATH_CM4

S6E2HG4G

__FM4_FAMILY

__S6E2HG_SUBFAMILY

Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x1FFFC000 - 0x1FFFFFFF
RAM2	0x2003E000 - 0x20041FFF
FLASH2	0x200C0000 - 0x200C7FFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/Common

DeviceHeaderFile=\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/S6E2HGxG/s6e2hg.h

DeviceLoaderFile=\$(TargetsDir)/FM4/Loader/S6E2HG_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/FM4/XML/S6E2HGxG_Registers.xml

DeviceSystemFile=\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HG/system_s6e2hg.c

DeviceVectorsFile=\$(TargetsDir)/FM4/Source/S6E2HGxG_Vectors.s

DeviceFamily=FM4

DeviceSubFamily=S6E2HG

S6E2HG6G

Device Details	
CMSIS Header File	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/S6E2HGxG/s6e2hg.h
CMSIS Include Path	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/Common
CMSIS System File	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HG/system_s6e2hg.c
Family	FM4
Sub Family	S6E2HG
Loader File	\$(TargetsDir)/FM4/Loader/S6E2HG_Loader.elf
Memory Map File	\$(TargetsDir)/FM4/XML/S6E2HG6G_MemoryMap.xml
Register Definition File	\$(TargetsDir)/FM4/XML/S6E2HGxG_Registers.xml
Vectors File	\$(TargetsDir)/FM4/Source/S6E2HGxG_Vectors.s
Preprocessor Definitions	
ARM_MATH_CM4	
S6E2HG6G	
__FM4_FAMILY	
__S6E2HG_SUBFAMILY	
Memory Segments	
FLASH	0x00000000 - 0x0007FFFF
RAM	0x1FFF8000 - 0x1FFFFFFF
RAM2	0x2003C000 - 0x20043FFF
FLASH2	0x200C0000 - 0x200C7FFF
Project Macros	
DeviceIncludePath	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/Common
DeviceHeaderFile	\$(TargetsDir)/FM4/CMSIS/Device/Include/S6E2HG/S6E2HGxG/s6e2hg.h
DeviceLoaderFile	\$(TargetsDir)/FM4/Loader/S6E2HG_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/FM4/XML/S6E2HGxG_Registers.xml
DeviceSystemFile	\$(TargetsDir)/FM4/CMSIS/Device/Source/S6E2HG/system_s6e2hg.c
DeviceVectorsFile	\$(TargetsDir)/FM4/Source/S6E2HGxG_Vectors.s
DeviceFamily	FM4
DeviceSubFamily	S6E2HG