



LPC1300 CPU Support Package Guide

Version: 3.0



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LPC1300 Support Package

This guide describes the following features of the LPC1300 CPU support package:

- [How to create LPC1300 projects](#)
- [How to open LPC1300 sample projects](#)
- [LPC1300 specific project properties](#)
- [LPC1300 specific project templates](#)
- [Supported LPC1300 devices](#)

Creating LPC1300 Projects

Creating an LPC1300 C/C++ executable project

To create a new minimal C/C++ executable project:

- Select the **File > New > New Project** menu item.
- Select the **A C/C++ executable for NXP LPC1300** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

Creating an LPC1300 library project

To create a new library project:

- Select the **File > New > New Project** menu item.
- Select the **A library for NXP LPC1300** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

Creating an LPC1300 externally built executable project

To create a new project that will allow you to debug an existing externally built executable file:

- Select the **File > New > New Project** menu item.
- Select the **An externally built executable for NXP LPC1300** project template.
- Set the required project name and location directory.
- Click **Next**.
- Set the **Load File** project property to point to the executable file you want to download and debug.
- If required, change any of the other default project settings.
- Click **Finish** to create the project.

Creating an LPC1300 CrossWorks Tasking Library executable project

To create a new C/C++ executable project configured to use the CrossWorks Tasking Library:

- Select the **File > New > New Project** menu item.
- Select the **A CrossWorks Tasking Library executable for NXP LPC1300** project template.
- Set the required project name and location directory.
- Click **Next**.

If required, change any of the other default project settings.
Click **Finish** to create the project.

Creating an LPC1300 assembly code only executable project

Please note, this template does not add any C/C++ startup code or libraries and is therefore not suitable for creating projects that include C/C++ code.

To create a new assembly code only executable project without:

Select the **File > New > New Project** menu item.
Select the **An assembly code only executable for NXP LPC1300** project template.
Set the required project name and location directory.
Click **Next**.
If required, change any of the other default project settings.
Click **Finish** to create the project.

Opening LPC1300 Sample Solutions

LPC1300 Samples Solution

This solution contains general sample projects that run on LPC1300 devices. To open the LPC1300 Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **NXP LPC1300 CPU Support Package** link.
- Select the **Samples Solutions > LPC1300 Samples Solution** link.

LPC1300 CMSIS-DSP Samples Solution

This solution contains sample projects that use the CMSIS-DSP library running on LPC1300 devices. To open the LPC1300 CMSIS-DSP Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **NXP LPC1300 CPU Support Package** link.
- Select the **Sample Solutions > LPC1300 CMSIS-DSP Samples Solution** link.

LPC1300 Project Properties

Projects creating using the project templates in this support package have the following device specific project properties:

Heap Size

The heap size is set to be 256 bytes when a project is created. The heap size can be modified using the **Heap Size** project property.

Section Placement

You can select the memory configuration you require using the **Section Placement** project property.

For LPC1300 projects, the set of placements are:

Flash - The application runs in internal Flash memory (*default*).

Flash Vectors In RAM - The application runs in internal Flash memory and exception vectors are copied to RAM memory.

Flash Copy To RAM - The application starts in internal flash and copies itself to run from internal RAM memory.

RAM - The application runs from internal RAM memory only.

Stack Sizes

The main stack size is set to be 256 bytes when a project is created.

The process stack size is set to be 0 bytes when a project is created.

The main and process stack sizes can be modified using the **Main Stack Size** and **Process Stack Size** project properties.

To change the location of the stacks, edit the section placement file and place the `.stack` and `.stack_process` sections as required.

Startup From Reset

By default, the application will only startup from power-on/reset in *Release* configuration. This acts as a safety net in case you accidentally download a program in FLASH during development that crashes and prevents the debugger from taking control of the target over the debug interface thus rendering the device unusable.

For LPC1300 projects, the **Startup From Reset** project property can be set to one of the following:

No - The application will not startup from reset.

Release Only - The application will only startup from reset when built in *Release* configuration (*default*).

Yes - The application will always startup from reset.

Target Processor

Once a project has been created you can target different devices by modifying the **Target Processor** project property. See the [LPC1300 Devices](#) section for details on the files, preprocessor definitions and macro definitions used when a device is selected.

LPC1300 Project Templates

The project template system simplifies the creation of new projects with the IDE, it also system makes it easy to create new projects with a text editor or script. All that needs to be specified is the project name, the support packages that the project is dependent on, the target processor and the source files you want to add to the project. For example, create a file called *example.hzp* with the following contents:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="LPC1300_EXE">
    <configuration Name="Common" package_dependencies="LPC1300" Target="LPC1343FBD48" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

You can also add any other property settings that the project requires such as preprocessor definitions or include paths using the property save name, for example:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="LPC1300_EXE">
    <configuration Name="Common" package_dependencies="LPC1300" Target="LPC1343FBD48"
      c_preprocessor_definitions="MYDEF1=1;MYDEF2=TWO" c_user_include_directories="$(ProjectDir)/
include1;$(ProjectDir)/include2" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

Available LPC1300 project templates

| Template Name | Template Description |
|-----------------|---------------------------------------|
| LPC1300_ASM_EXE | LPC1300 Assembly Code Only Executable |
| LPC1300_CTL_EXE | LPC1300 CTL Executable |
| LPC1300_EXE | LPC1300 C/C++ Executable |
| LPC1300_EXT_EXE | LPC1300 Externally Built Executable |
| LPC1300_LIB | LPC1300 Library |

LPC1300 Devices

This package supports the following LPC1300 devices:

[LPC131x Family](#)

[LPC134x Family](#)

LPC131x Family

[LPC1311FHN33](#)

[LPC1311FHN33/01](#)

[LPC1313FHN33](#)

[LPC1313FHN33/01](#)

[LPC1313FBD48](#)

[LPC1313FBD48/01](#)

[LPC1315FHN33](#)

[LPC1315FBD48](#)

[LPC1316FHN33](#)

[LPC1316FBD48](#)

[LPC1317FHN33](#)

[LPC1317FBD48](#)

[LPC1317FBD64](#)

LPC1311FHN33

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c |
| Family | LPC1300 |
| Sub Family | LPC131x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1311FHN33_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s |

Preprocessor Definitions

```

ARM_MATH_CM3
LPC1311FHN33_DEVICE
__LPC1300_FAMILY
__LPC1311FHN33__
__LPC131x_SUBFAMILY

```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x00001FFF |
| RAM | 0x10000000 - 0x10000FFF |

Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC131x

```

LPC1311FHN33/01

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c |
| Family | LPC1300 |
| Sub Family | LPC131x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1311FHN33_01_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s |

Preprocessor Definitions

```

ARM_MATH_CM3
LPC1311FHN33_01_DEVICE
__LPC1300_FAMILY
__LPC1311FHN33_01__
__LPC131x_SUBFAMILY

```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x00001FFF |
| RAM | 0x10000000 - 0x10000FFF |

Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC131x

```

LPC1313FHN33

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c |
| Family | LPC1300 |
| Sub Family | LPC131x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1313FHN33_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s |

Preprocessor Definitions

```

ARM_MATH_CM3
LPC1313FHN33_DEVICE
__LPC1300_FAMILY
__LPC1313FHN33__
__LPC131x_SUBFAMILY

```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x00007FFF |
| RAM | 0x10000000 - 0x10001FFF |

Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC131x

```


LPC1313FHN33/01

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c |
| Family | LPC1300 |
| Sub Family | LPC131x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1313FHN33_01_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s |

Preprocessor Definitions

```
ARM_MATH_CM3
LPC1313FHN33_01_DEVICE
__LPC1300_FAMILY
__LPC1313FHN33_01__
__LPC131x_SUBFAMILY
```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x00007FFF |
| RAM | 0x10000000 - 0x10001FFF |

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC131x
```

LPC1313FBD48

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c |
| Family | LPC1300 |
| Sub Family | LPC131x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1313FBD48_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s |

Preprocessor Definitions

```

ARM_MATH_CM3
LPC1313FBD48_DEVICE
__LPC1300_FAMILY
__LPC1313FBD48__
__LPC131x_SUBFAMILY

```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x00007FFF |
| RAM | 0x10000000 - 0x10001FFF |

Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC131x

```

LPC1313FBD48/01

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c |
| Family | LPC1300 |
| Sub Family | LPC131x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1313FBD48_01_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s |

Preprocessor Definitions

```
ARM_MATH_CM3
LPC1313FBD48_01_DEVICE
__LPC1300_FAMILY
__LPC1313FBD48_01__
__LPC131x_SUBFAMILY
```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x00007FFF |
| RAM | 0x10000000 - 0x10001FFF |

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC131x
```

LPC1315FHN33

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c |
| Family | LPC1300 |
| Sub Family | LPC131x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1315FHN33_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s |

Preprocessor Definitions

```
ARM_MATH_CM3
LPC1315FHN33_DEVICE
__LPC1300_FAMILY
__LPC1315FHN33__
__LPC131x_SUBFAMILY
```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x00007FFF |
| RAM | 0x10000000 - 0x10001FFF |

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC131x
```

LPC1315FBD48

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c |
| Family | LPC1300 |
| Sub Family | LPC131x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1315FBD48_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s |

Preprocessor Definitions

```
ARM_MATH_CM3
LPC1315FBD48_DEVICE
__LPC1300_FAMILY
__LPC1315FBD48__
__LPC131x_SUBFAMILY
```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x00007FFF |
| RAM | 0x10000000 - 0x10001FFF |

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC131x
```

LPC1316FHN33

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c |
| Family | LPC1300 |
| Sub Family | LPC131x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1316FHN33_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s |

Preprocessor Definitions

```
ARM_MATH_CM3
LPC1316FHN33_DEVICE
__LPC1300_FAMILY
__LPC1316FHN33__
__LPC131x_SUBFAMILY
```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x0000BFFF |
| RAM | 0x10000000 - 0x10001FFF |

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC131x
```

LPC1316FBD48

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c |
| Family | LPC1300 |
| Sub Family | LPC131x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1316FBD48_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s |

Preprocessor Definitions

```
ARM_MATH_CM3
LPC1316FBD48_DEVICE
__LPC1300_FAMILY
__LPC1316FBD48__
__LPC131x_SUBFAMILY
```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x0000BFFF |
| RAM | 0x10000000 - 0x10001FFF |

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC131x
```

LPC1317FHN33

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c |
| Family | LPC1300 |
| Sub Family | LPC131x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1317FHN33_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s |

Preprocessor Definitions

```
ARM_MATH_CM3
LPC1317FHN33_DEVICE
__LPC1300_FAMILY
__LPC1317FHN33__
__LPC131x_SUBFAMILY
```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x0000FFFF |
| RAM | 0x10000000 - 0x10001FFF |
| RAM2 | 0x20000000 - 0x200007FF |

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC131x
```


LPC1317FBD48

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c |
| Family | LPC1300 |
| Sub Family | LPC131x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1317FBD48_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s |

Preprocessor Definitions

```
ARM_MATH_CM3
LPC1317FBD48_DEVICE
__LPC1300_FAMILY
__LPC1317FBD48__
__LPC131x_SUBFAMILY
```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x0000FFFF |
| RAM | 0x10000000 - 0x10001FFF |
| RAM2 | 0x20000000 - 0x200007FF |

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC131x
```

LPC1317FBD64

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c |
| Family | LPC1300 |
| Sub Family | LPC131x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1317FBD64_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s |

Preprocessor Definitions

```

ARM_MATH_CM3
LPC1317FBD64_DEVICE
__LPC1300_FAMILY
__LPC1317FBD64__
__LPC131x_SUBFAMILY

```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x0000FFFF |
| RAM | 0x10000000 - 0x10001FFF |
| RAM2 | 0x20000000 - 0x200007FF |

Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC131x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC131x

```

LPC134x Family

[LPC1342FHN33](#)

[LPC1342FBD48](#)

[LPC1343FHN33](#)

[LPC1343FBD48](#)

[LPC1345FHN33](#)

[LPC1345FBD48](#)

[LPC1346FHN33](#)

[LPC1346FBD48](#)

[LPC1347FHN33](#)

[LPC1347FBD48](#)

[LPC1347FBD64](#)

LPC1342FHN33

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c |
| Family | LPC1300 |
| Sub Family | LPC134x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1342FHN33_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s |

Preprocessor Definitions

ARM_MATH_CM3
 LPC1342FHN33_DEVICE
 __LPC1300_FAMILY
 __LPC1342FHN33__
 __LPC134x_SUBFAMILY

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x00003FFF |
| RAM | 0x10000000 - 0x10000FFF |

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx
 DeviceHeaderFile=\$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h
 DeviceLoaderFile=\$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf
 DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml
 DeviceSystemFile=\$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c
 DeviceVectorsFile=\$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s
 DeviceFamily=LPC1300
 DeviceSubFamily=LPC134x

LPC1342FBD48

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c |
| Family | LPC1300 |
| Sub Family | LPC134x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1342FBD48_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s |

Preprocessor Definitions

```
ARM_MATH_CM3
LPC1342FBD48_DEVICE
__LPC1300_FAMILY
__LPC1342FBD48__
__LPC134x_SUBFAMILY
```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x00003FFF |
| RAM | 0x10000000 - 0x10000FFF |

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC134x
```

LPC1343FHN33

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c |
| Family | LPC1300 |
| Sub Family | LPC134x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1343FHN33_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s |

Preprocessor Definitions

ARM_MATH_CM3
 LPC1343FHN33_DEVICE
 __LPC1300_FAMILY
 __LPC1343FHN33__
 __LPC134x_SUBFAMILY

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x00007FFF |
| RAM | 0x10000000 - 0x10001FFF |

Project Macros

DeviceIncludePath=\$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx
 DeviceHeaderFile=\$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h
 DeviceLoaderFile=\$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf
 DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml
 DeviceSystemFile=\$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c
 DeviceVectorsFile=\$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s
 DeviceFamily=LPC1300
 DeviceSubFamily=LPC134x

LPC1343FBD48

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c |
| Family | LPC1300 |
| Sub Family | LPC134x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1343FBD48_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s |

Preprocessor Definitions

```

ARM_MATH_CM3
LPC1343FBD48_DEVICE
__LPC1300_FAMILY
__LPC1343FBD48__
__LPC134x_SUBFAMILY

```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x00007FFF |
| RAM | 0x10000000 - 0x10001FFF |

Project Macros

```

DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13xx/LPC13xx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13xx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13xx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13xx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC134x

```

LPC1345FHN33

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c |
| Family | LPC1300 |
| Sub Family | LPC134x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1345FHN33_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s |

Preprocessor Definitions

```
ARM_MATH_CM3
LPC1345FHN33_DEVICE
__LPC1300_FAMILY
__LPC1345FHN33__
__LPC134x_SUBFAMILY
```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x00007FFF |
| RAM | 0x10000000 - 0x10001FFF |

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC134x
```


LPC1345FBD48

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c |
| Family | LPC1300 |
| Sub Family | LPC134x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1345FBD48_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s |

Preprocessor Definitions

```
ARM_MATH_CM3
LPC1345FBD48_DEVICE
__LPC1300_FAMILY
__LPC1345FBD48__
__LPC134x_SUBFAMILY
```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x00007FFF |
| RAM | 0x10000000 - 0x10001FFF |

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC134x
```

LPC1346FHN33

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c |
| Family | LPC1300 |
| Sub Family | LPC134x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1346FHN33_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s |

Preprocessor Definitions

```
ARM_MATH_CM3
LPC1346FHN33_DEVICE
__LPC1300_FAMILY
__LPC1346FHN33__
__LPC134x_SUBFAMILY
```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x0000BFFF |
| RAM | 0x10000000 - 0x10001FFF |

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC134x
```

LPC1346FBD48

Device Details

| | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c |
| Family | LPC1300 |
| Sub Family | LPC134x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1346FBD48_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s |

Preprocessor Definitions

```
ARM_MATH_CM3
LPC1346FBD48_DEVICE
__LPC1300_FAMILY
__LPC1346FBD48__
__LPC134x_SUBFAMILY
```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x0000BFFF |
| RAM | 0x10000000 - 0x10001FFF |

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC134x
```

LPC1347FHN33

| Device Details | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c |
| Family | LPC1300 |
| Sub Family | LPC134x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1347FHN33_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s |

| Preprocessor Definitions |
|--------------------------|
| ARM_MATH_CM3 |
| LPC1347FHN33_DEVICE |
| __LPC1300_FAMILY |
| __LPC1347FHN33__ |
| __LPC134x_SUBFAMILY |

| Memory Segments | |
|-----------------|-------------------------|
| FLASH | 0x00000000 - 0x0000FFFF |
| RAM | 0x10000000 - 0x10001FFF |
| RAM2 | 0x20000000 - 0x200007FF |

| Project Macros |
|--|
| DeviceIncludePath=\$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx |
| DeviceHeaderFile=\$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h |
| DeviceLoaderFile=\$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf |
| DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml |
| DeviceSystemFile=\$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c |
| DeviceVectorsFile=\$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s |
| DeviceFamily=LPC1300 |
| DeviceSubFamily=LPC134x |

LPC1347FBD48

| Device Details | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c |
| Family | LPC1300 |
| Sub Family | LPC134x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1347FBD48_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s |

| Preprocessor Definitions |
|--------------------------|
| ARM_MATH_CM3 |
| LPC1347FBD48_DEVICE |
| __LPC1300_FAMILY |
| __LPC1347FBD48__ |
| __LPC134x_SUBFAMILY |

| Memory Segments | |
|-----------------|-------------------------|
| FLASH | 0x00000000 - 0x0000FFFF |
| RAM | 0x10000000 - 0x10001FFF |
| RAM2 | 0x20000000 - 0x200007FF |

| Project Macros |
|--|
| DeviceIncludePath=\$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx |
| DeviceHeaderFile=\$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h |
| DeviceLoaderFile=\$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf |
| DeviceRegisterDefinitionFile=\$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml |
| DeviceSystemFile=\$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c |
| DeviceVectorsFile=\$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s |
| DeviceFamily=LPC1300 |
| DeviceSubFamily=LPC134x |

LPC1347FBD64

| Device Details | |
|--------------------------|---|
| CMSIS Header File | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h |
| CMSIS Include Path | \$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx |
| CMSIS System File | \$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c |
| Family | LPC1300 |
| Sub Family | LPC134x |
| Loader File | \$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf |
| Memory Map File | \$(TargetsDir)/LPC1300/XML/LPC1347FBD64_MemoryMap.xml |
| Register Definition File | \$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml |
| Vectors File | \$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s |

Preprocessor Definitions

```
ARM_MATH_CM3
LPC1347FBD64_DEVICE
__LPC1300_FAMILY
__LPC1347FBD64__
__LPC134x_SUBFAMILY
```

Memory Segments

| | |
|-------|-------------------------|
| FLASH | 0x00000000 - 0x0000FFFF |
| RAM | 0x10000000 - 0x10001FFF |
| RAM2 | 0x20000000 - 0x200007FF |

Project Macros

```
DeviceIncludePath=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx
DeviceHeaderFile=$(TargetsDir)/LPC1300/CMSIS/Device/Include/LPC13Uxx/LPC13Uxx.h
DeviceLoaderFile=$(TargetsDir)/LPC1300/Loader/LPC134x_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/LPC1300/XML/LPC13Uxx_Registers.xml
DeviceSystemFile=$(TargetsDir)/LPC1300/CMSIS/Device/Source/system_LPC13Uxx.c
DeviceVectorsFile=$(TargetsDir)/LPC1300/Source/LPC13Uxx_Vectors.s
DeviceFamily=LPC1300
DeviceSubFamily=LPC134x
```