



# SAM D CPU Support Package Guide

**Version: 1.2**





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# SAM D Support Package

This guide describes the following features of the SAM D CPU support package:

- [How to create SAM D projects](#)
- [How to open SAM D sample projects](#)
- [SAM D specific project properties](#)
- [SAM D specific project templates](#)
- [Supported SAM D devices](#)

# Creating SAM D Projects

## Creating an SAM D C/C++ executable project

To create a new minimal C/C++ executable project:

- Select the **File > New > New Project** menu item.
- Select the **A C/C++ executable for Atmel SAM D** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

## Creating an SAM D library project

To create a new library project:

- Select the **File > New > New Project** menu item.
- Select the **A library for Atmel SAM D** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

## Creating an SAM D externally built executable project

To create a new project that will allow you to debug an existing externally built executable file:

- Select the **File > New > New Project** menu item.
- Select the **An externally built executable for Atmel SAM D** project template.
- Set the required project name and location directory.
- Click **Next**.
- Set the **Load File** project property to point to the executable file you want to download and debug.
- If required, change any of the other default project settings.
- Click **Finish** to create the project.

## Creating an SAM D CrossWorks Tasking Library executable project

To create a new C/C++ executable project configured to use the CrossWorks Tasking Library:

- Select the **File > New > New Project** menu item.
- Select the **A CrossWorks Tasking Library executable for Atmel SAM D** project template.
- Set the required project name and location directory.
- Click **Next**.



If required, change any of the other default project settings.  
Click **Finish** to create the project.

## Creating an SAM D assembly code only executable project

*Please note, this template does not add any C/C++ startup code or libraries and is therefore not suitable for creating projects that include C/C++ code.*

To create a new assembly code only executable project without:

Select the **File > New > New Project** menu item.

Select the **An assembly code only executable for Atmel SAM D** project template.

Set the required project name and location directory.

Click **Next**.

If required, change any of the other default project settings.

Click **Finish** to create the project.

# Opening SAM D Sample Solutions

## SAM D Samples Solution

This solution contains general sample projects that run on SAM D devices. To open the SAM D Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **Atmel SAM D CPU Support Package** link.
- Select the **Samples Solutions > SAM D Samples Solution** link.

## SAM D CMSIS-DSP Samples Solution

This solution contains sample projects that use the CMSIS-DSP library running on SAM D devices. To open the SAM D CMSIS-DSP Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **Atmel SAM D CPU Support Package** link.
- Select the **Sample Solutions > SAM D CMSIS-DSP Samples Solution** link.

# SAM D Project Properties

Projects creating using the project templates in this support package have the following device specific project properties:

## Heap Size

The heap size is set to be 256 bytes when a project is created. The heap size can be modified using the **Heap Size** project property.

## Section Placement

You can select the memory configuration you require using the **Section Placement** project property.

For SAM D projects, the set of placements are:

**Flash** - The application runs in internal Flash memory (*default*).

**Flash Vectors In RAM** - The application runs in internal Flash memory and exception vectors are copied to RAM memory.

**Flash Copy To RAM** - The application starts in internal flash and copies itself to run from internal RAM memory.

**RAM** - The application runs from internal RAM memory only.

## Stack Sizes

The main stack size is set to be 256 bytes when a project is created.

The process stack size is set to be 0 bytes when a project is created.

The main and process stack sizes can be modified using the **Main Stack Size** and **Process Stack Size** project properties.

To change the location of the stacks, edit the section placement file and place the `.stack` and `.stack_process` sections as required.

## Startup From Reset

By default, the application will only startup from power-on/reset in *Release* configuration. This acts as a safety net in case you accidentally download a program in FLASH during development that crashes and prevents the debugger from taking control of the target over the debug interface thus rendering the device unusable.

For SAM D projects, the **Startup From Reset** project property can be set to one of the following:

**No** - The application will not startup from reset.

**Release Only** - The application will only startup from reset when built in *Release* configuration (*default*).

**Yes** - The application will always startup from reset.

## Target Processor

Once a project has been created you can target different devices by modifying the **Target Processor** project property. See the [SAM D Devices](#) section for details on the files, preprocessor definitions and macro definitions used when a device is selected.

## SAM D Project Templates

The project template system simplifies the creation of new projects with the IDE, it also system makes it easy to create new projects with a text editor or script. All that needs to be specified is the project name, the support packages that the project is dependent on, the target processor and the source files you want to add to the project. For example, create a file called *example.hzp* with the following contents:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="SAM_D_EXE">
    <configuration Name="Common" package_dependencies="SAM_D" Target="ATSAMD20E18" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

You can also add any other property settings that the project requires such as preprocessor definitions or include paths using the property save name, for example:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="SAM_D_EXE">
    <configuration Name="Common" package_dependencies="SAM_D" Target="ATSAMD20E18"
      c_preprocessor_definitions="MYDEF1=1;MYDEF2=TWO" c_user_include_directories="$(ProjectDir)/
      include1;$(ProjectDir)/include2" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

### Available SAM D project templates

Template Name	Template Description
SAM D_ASM_EXE	SAM D Assembly Code Only Executable
SAM D_CTL_EXE	SAM D CTL Executable
SAM D_EXE	SAM D C/C++ Executable
SAM D_EXT_EXE	SAM D Externally Built Executable
SAM D_LIB	SAM D Library

# SAM D Devices

This package supports the following SAM D devices:

[SAM D09 Family](#)

[SAM D10 Family](#)

[SAM D11 Family](#)

[SAM D20 Family](#)

[SAM D21 Family](#)

[SAM DA0 Family](#)

[SAM DA1 Family](#)

# SAM D09 Family

[ATSAMD09C13A](#)

[ATSAMD09D14A](#)

# ATSAMD09C13A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD09/Include/samd09.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD09/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD09/Source/system_samd09.c
Family	SAM D
Sub Family	SAM D09
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D09_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD09C13A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD09C13A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD09C13A_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD09C13A__	
__SAM_D09_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00001FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD09/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD09/Include/samd09.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D09_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD09C13A_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD09/Source/system_samd09.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD09C13A_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D09



# ATSAMD09D14A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD09/Include/samd09.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD09/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD09/Source/system_samd09.c
Family	SAM D
Sub Family	SAM D09
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D09_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD09D14A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD09D14A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD09D14A_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD09D14A__	
__SAM_D09_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD09/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD09/Include/samd09.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D09_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD09D14A_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD09/Source/system_samd09.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD09D14A_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D09

# SAM D10 Family

[ATSAMD10C13A](#)

[ATSAMD10C14A](#)

[ATSAMD10D13A](#)

[ATSAMD10D14A](#)

# ATSAMD10C13A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Include/samd10.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Source/system_samd10.c
Family	SAM D
Sub Family	SAM D10
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D10_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD10C13A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD10C13A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD10C13A_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD10C13A__	
__SAM_D10_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00001FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Include/samd10.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D10_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD10C13A_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Source/system_samd10.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD10C13A_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D10

# ATSAMD10C14A

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Include/samd10.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Source/system_samd10.c
Family	SAM D
Sub Family	SAM D10
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D10_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD10C14A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD10C14A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD10C14A_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMD10C14A\_\_

\_\_SAM\_D10\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x20000FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD10/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD10/Include/samd10.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_D10\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMD10C14A\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD10/Source/system\_samd10.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMD10C14A\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM D10

# ATSAMD10D13A

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Include/samd10.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Source/system_samd10.c
Family	SAM D
Sub Family	SAM D10
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D10_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD10D13A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD10D13A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD10D13A_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
__SAMD10D13A__
__SAM_D10_SUBFAMILY
__SAM_D_FAMILY
```

## Memory Segments

FLASH	0x00000000 - 0x00001FFF
RAM	0x20000000 - 0x20000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Include
DeviceHeaderFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Include/samd10.h
DeviceLoaderFile=$(TargetsDir)/SAM_D/Loader/SAM_D10_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_D/XML/ATSAMD10D13A_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Source/system_samd10.c
DeviceVectorsFile=$(TargetsDir)/SAM_D/Source/ATSAMD10D13A_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM D10
```

# ATSAMD10D14A

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Include/samd10.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD10/Source/system_samd10.c
Family	SAM D
Sub Family	SAM D10
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D10_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD10D14A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD10D14A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD10D14A_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMD10D14A\_\_

\_\_SAM\_D10\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x20000FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD10/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD10/Include/samd10.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_D10\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMD10D14A\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD10/Source/system\_samd10.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMD10D14A\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM D10

# SAM D11 Family

[ATSAMD11C13A](#)

[ATSAMD11C14A](#)

[ATSAMD11D14AM](#)

[ATSAMD11D14AS](#)

# ATSAMD11C13A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Include/samd11.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Source/system_samd11.c
Family	SAM D
Sub Family	SAM D11
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D11_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD11C13A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD11C13A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD11C13A_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD11C13A__	
__SAM_D11_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00001FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Include/samd11.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D11_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD11C13A_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Source/system_samd11.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD11C13A_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D11



# ATSAMD11C14A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Include/samd11.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Source/system_samd11.c
Family	SAM D
Sub Family	SAM D11
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D11_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD11C14A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD11C14A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD11C14A_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD11C14A__	
__SAM_D11_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Include/samd11.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D11_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD11C14A_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Source/system_samd11.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD11C14A_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D11

# ATSAMD11D14AM

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Include/samd11.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Source/system_samd11.c
Family	SAM D
Sub Family	SAM D11
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D11_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD11D14AM_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD11D14AM_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD11D14AM_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD11D14AM__	
__SAM_D11_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Include/samd11.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D11_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD11D14AM_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Source/system_samd11.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD11D14AM_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D11

# ATSAMD11D14AS

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Include/samd11.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Source/system_samd11.c
Family	SAM D
Sub Family	SAM D11
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D11_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD11D14AS_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD11D14AS_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD11D14AS_Vectors.s

Preprocessor Definitions
ARM_MATH_CM0PLUS
__SAMD11D14AS__
__SAM_D11_SUBFAMILY
__SAM_D_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x20000FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Include
DeviceHeaderFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Include/samd11.h
DeviceLoaderFile=\$(TargetsDir)/SAM_D/Loader/SAM_D11_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM_D/XML/ATSAMD11D14AS_Registers.xml
DeviceSystemFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD11/Source/system_samd11.c
DeviceVectorsFile=\$(TargetsDir)/SAM_D/Source/ATSAMD11D14AS_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM D11

## SAM D20 Family

[ATSAMD20E14](#)

[ATSAMD20E15](#)

[ATSAMD20E16](#)

[ATSAMD20E17](#)

[ATSAMD20E18](#)

[ATSAMD20G14](#)

[ATSAMD20G15](#)

[ATSAMD20G16](#)

[ATSAMD20G17](#)

[ATSAMD20G18](#)

[ATSAMD20J14](#)

[ATSAMD20J15](#)

[ATSAMD20J16](#)

[ATSAMD20J17](#)

[ATSAMD20J18](#)

# ATSAMD20E14

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
Family	SAM D
Sub Family	SAM D20
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Small_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD20E14_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD20E14_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD20E14_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
__SAMD20E14__
__SAM_D20_SUBFAMILY
__SAM_D_FAMILY
```

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x200007FF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
DeviceHeaderFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
DeviceLoaderFile=$(TargetsDir)/SAM_D/Loader/SAM_D20_Small_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_D/XML/ATSAMD20E14_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
DeviceVectorsFile=$(TargetsDir)/SAM_D/Source/ATSAMD20E14_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM D20
```

# ATSAMD20E15

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
Family	SAM D
Sub Family	SAM D20
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD20E15_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD20E15_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD20E15_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
__SAMD20E15__
__SAM_D20_SUBFAMILY
__SAM_D_FAMILY
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
DeviceHeaderFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
DeviceLoaderFile=$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_D/XML/ATSAMD20E15_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
DeviceVectorsFile=$(TargetsDir)/SAM_D/Source/ATSAMD20E15_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM D20
```

# ATSAMD20E16

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
Family	SAM D
Sub Family	SAM D20
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD20E16_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD20E16_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD20E16_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD20E16__	
__SAM_D20_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD20E16_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD20E16_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D20

# ATSAMD20E17

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
Family	SAM D
Sub Family	SAM D20
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD20E17_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD20E17_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD20E17_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
__SAMD20E17__
__SAM_D20_SUBFAMILY
__SAM_D_FAMILY
```

## Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
DeviceHeaderFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
DeviceLoaderFile=$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_D/XML/ATSAMD20E17_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
DeviceVectorsFile=$(TargetsDir)/SAM_D/Source/ATSAMD20E17_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM D20
```



# ATSAMD20E18

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
Family	SAM D
Sub Family	SAM D20
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD20E18_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD20E18_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD20E18_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
__SAMD20E18__
__SAM_D20_SUBFAMILY
__SAM_D_FAMILY
```

## Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
DeviceHeaderFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
DeviceLoaderFile=$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_D/XML/ATSAMD20E18_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
DeviceVectorsFile=$(TargetsDir)/SAM_D/Source/ATSAMD20E18_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM D20
```

# ATSAMD20G14

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
Family	SAM D
Sub Family	SAM D20
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Small_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD20G14_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD20G14_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD20G14_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMD20G14\_\_

\_\_SAM\_D20\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x200007FF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD20/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD20/Include/samd20.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_D20\_Small\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMD20G14\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD20/Source/system\_samd20.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMD20G14\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM D20

# ATSAMD20G15

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
Family	SAM D
Sub Family	SAM D20
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD20G15_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD20G15_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD20G15_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
__SAMD20G15__
__SAM_D20_SUBFAMILY
__SAM_D_FAMILY
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
DeviceHeaderFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
DeviceLoaderFile=$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_D/XML/ATSAMD20G15_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
DeviceVectorsFile=$(TargetsDir)/SAM_D/Source/ATSAMD20G15_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM D20
```

# ATSAMD20G16

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
Family	SAM D
Sub Family	SAM D20
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD20G16_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD20G16_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD20G16_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD20G16__	
__SAM_D20_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD20G16_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD20G16_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D20

# ATSAMD20G17

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
Family	SAM D
Sub Family	SAM D20
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD20G17_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD20G17_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD20G17_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
__SAMD20G17__
__SAM_D20_SUBFAMILY
__SAM_D_FAMILY
```

## Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
DeviceHeaderFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
DeviceLoaderFile=$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_D/XML/ATSAMD20G17_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
DeviceVectorsFile=$(TargetsDir)/SAM_D/Source/ATSAMD20G17_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM D20
```

# ATSAMD20G18

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
Family	SAM D
Sub Family	SAM D20
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD20G18_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD20G18_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD20G18_Vectors.s

Preprocessor Definitions
ARM_MATH_CM0PLUS
__SAMD20G18__
__SAM_D20_SUBFAMILY
__SAM_D_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
DeviceHeaderFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
DeviceLoaderFile=\$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM_D/XML/ATSAMD20G18_Registers.xml
DeviceSystemFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
DeviceVectorsFile=\$(TargetsDir)/SAM_D/Source/ATSAMD20G18_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM D20

# ATSAMD20J14

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
Family	SAM D
Sub Family	SAM D20
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Small_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD20J14_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD20J14_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD20J14_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD20J14__	
__SAM_D20_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x200007FF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Small_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD20J14_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD20J14_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D20

# ATSAMD20J15

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
Family	SAM D
Sub Family	SAM D20
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD20J15_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD20J15_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD20J15_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
__SAMD20J15__
__SAM_D20_SUBFAMILY
__SAM_D_FAMILY
```

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
DeviceHeaderFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
DeviceLoaderFile=$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_D/XML/ATSAMD20J15_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
DeviceVectorsFile=$(TargetsDir)/SAM_D/Source/ATSAMD20J15_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM D20
```



# ATSAMD20J16

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
Family	SAM D
Sub Family	SAM D20
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD20J16_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD20J16_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD20J16_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD20J16__	
__SAM_D20_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD20J16_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD20J16_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D20

# ATSAMD20J17

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
Family	SAM D
Sub Family	SAM D20
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD20J17_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD20J17_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD20J17_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
__SAMD20J17__
__SAM_D20_SUBFAMILY
__SAM_D_FAMILY
```

## Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
DeviceHeaderFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
DeviceLoaderFile=$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_D/XML/ATSAMD20J17_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
DeviceVectorsFile=$(TargetsDir)/SAM_D/Source/ATSAMD20J17_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM D20
```

# ATSAMD20J18

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
Family	SAM D
Sub Family	SAM D20
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD20J18_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD20J18_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD20J18_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
__SAMD20J18__
__SAM_D20_SUBFAMILY
__SAM_D_FAMILY
```

## Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include
DeviceHeaderFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Include/samd20.h
DeviceLoaderFile=$(TargetsDir)/SAM_D/Loader/SAM_D20_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_D/XML/ATSAMD20J18_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD20/Source/system_samd20.c
DeviceVectorsFile=$(TargetsDir)/SAM_D/Source/ATSAMD20J18_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM D20
```

## SAM D21 Family

[ATSAMD21E15A](#)

[ATSAMD21E15B](#)

[ATSAMD21E15BU](#)

[ATSAMD21E16A](#)

[ATSAMD21E16B](#)

[ATSAMD21E16BU](#)

[ATSAMD21E17A](#)

[ATSAMD21E18A](#)

[ATSAMD21G15A](#)

[ATSAMD21G15B](#)

[ATSAMD21G16A](#)

[ATSAMD21G16B](#)

[ATSAMD21G17A](#)

[ATSAMD21G17AU](#)

[ATSAMD21G18A](#)

[ATSAMD21G18AU](#)

[ATSAMD21J15A](#)

[ATSAMD21J15B](#)

[ATSAMD21J16A](#)

[ATSAMD21J16B](#)

[ATSAMD21J17A](#)

[ATSAMD21J18A](#)

# ATSAMD21E15A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21E15A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21E15A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21E15A_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD21E15A__	
__SAM_D21_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD21E15A_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD21E15A_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D21

# ATSAMD21E15B

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21E15B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21E15B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21E15B_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD21E15B__	
__SAM_D21_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD21E15B_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD21E15B_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D21

# ATSAMD21E15BU

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21E15BU_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21E15BU_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21E15BU_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD21E15BU__	
__SAM_D21_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD21E15BU_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD21E15BU_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D21

# ATSAMD21E16A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21E16A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21E16A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21E16A_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD21E16A__	
__SAM_D21_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD21E16A_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD21E16A_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D21



# ATSAMD21E16B

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21E16B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21E16B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21E16B_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD21E16B__	
__SAM_D21_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD21E16B_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD21E16B_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D21

# ATSAMD21E16BU

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21E16BU_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21E16B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21E16B_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD21E16BU__	
__SAM_D21_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD21E16B_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD21E16B_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D21

# ATSAMD21E17A

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21E17A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21E17A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21E17A_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMD21E17A\_\_

\_\_SAM\_D21\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD21/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD21/Include/samd21.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_D21\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMD21E17A\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD21/Source/system\_samd21.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMD21E17A\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM D21

# ATSAMD21E18A

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21E18A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21E18A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21E18A_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMD21E18A\_\_

\_\_SAM\_D21\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD21/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD21/Include/samd21.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_D21\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMD21E18A\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD21/Source/system\_samd21.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMD21E18A\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM D21

# ATSAMD21G15A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21G15A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21G15A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21G15A_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD21G15A__	
__SAM_D21_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD21G15A_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD21G15A_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D21

# ATSAMD21G15B

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21G15B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21G15B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21G15B_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD21G15B__	
__SAM_D21_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD21G15B_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD21G15B_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D21

# ATSAMD21G16A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21G16A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21G16A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21G16A_Vectors.s

Preprocessor Definitions
ARM_MATH_CM0PLUS
__SAMD21G16A__
__SAM_D21_SUBFAMILY
__SAM_D_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
DeviceHeaderFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
DeviceLoaderFile=\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM_D/XML/ATSAMD21G16A_Registers.xml
DeviceSystemFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
DeviceVectorsFile=\$(TargetsDir)/SAM_D/Source/ATSAMD21G16A_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM D21

# ATSAMD21G16B

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21G16B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21G16B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21G16B_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMD21G16B\_\_

\_\_SAM\_D21\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD21/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD21/Include/samd21.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_D21\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMD21G16B\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD21/Source/system\_samd21.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMD21G16B\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM D21



# ATSAMD21G17A

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21G17A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21G17A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21G17A_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMD21G17A\_\_

\_\_SAM\_D21\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD21/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD21/Include/samd21.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_D21\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMD21G17A\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD21/Source/system\_samd21.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMD21G17A\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM D21

# ATSAMD21G17AU

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21G17AU_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21G17AU_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21G17AU_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD21G17AU__	
__SAM_D21_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD21G17AU_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD21G17AU_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D21

# ATSAMD21G18A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21G18A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21G18A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21G18A_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD21G18A__	
__SAM_D21_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD21G18A_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD21G18A_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D21

# ATSAMD21G18AU

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21G18AU_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21G18AU_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21G18AU_Vectors.s

Preprocessor Definitions
ARM_MATH_CM0PLUS
__SAMD21G18AU__
__SAM_D21_SUBFAMILY
__SAM_D_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
DeviceHeaderFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
DeviceLoaderFile=\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM_D/XML/ATSAMD21G18AU_Registers.xml
DeviceSystemFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
DeviceVectorsFile=\$(TargetsDir)/SAM_D/Source/ATSAMD21G18AU_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM D21

# ATSAMD21J15A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21J15A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21J15A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21J15A_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD21J15A__	
__SAM_D21_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD21J15A_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD21J15A_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D21

# ATSAMD21J15B

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21J15B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21J15B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21J15B_Vectors.s

Preprocessor Definitions
ARM_MATH_CM0PLUS
__SAMD21J15B__
__SAM_D21_SUBFAMILY
__SAM_D_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
DeviceHeaderFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
DeviceLoaderFile=\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM_D/XML/ATSAMD21J15B_Registers.xml
DeviceSystemFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
DeviceVectorsFile=\$(TargetsDir)/SAM_D/Source/ATSAMD21J15B_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM D21

# ATSAMD21J16A

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21J16A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21J16A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21J16A_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
__SAMD21J16A__
__SAM_D21_SUBFAMILY
__SAM_D_FAMILY
```

## Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
DeviceHeaderFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
DeviceLoaderFile=$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_D/XML/ATSAMD21J16A_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
DeviceVectorsFile=$(TargetsDir)/SAM_D/Source/ATSAMD21J16A_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM D21
```

# ATSAMD21J16B

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21J16B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21J16B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21J16B_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMD21J16B__	
__SAM_D21_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMD21J16B_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMD21J16B_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM D21



# ATSAMD21J17A

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21J17A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21J17A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21J17A_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMD21J17A\_\_

\_\_SAM\_D21\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x0001FFFF
RAM	0x20000000 - 0x20003FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD21/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD21/Include/samd21.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_D21\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMD21J17A\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMD21/Source/system\_samd21.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMD21J17A\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM D21

# ATSAMD21J18A

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
Family	SAM D
Sub Family	SAM D21
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD21J18A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD21J18A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD21J18A_Vectors.s

## Preprocessor Definitions

```
ARM_MATH_CM0PLUS
__SAMD21J18A__
__SAM_D21_SUBFAMILY
__SAM_D_FAMILY
```

## Memory Segments

FLASH	0x00000000 - 0x0003FFFF
RAM	0x20000000 - 0x20007FFF

## Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include
DeviceHeaderFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Include/samd21.h
DeviceLoaderFile=$(TargetsDir)/SAM_D/Loader/SAM_D21_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_D/XML/ATSAMD21J18A_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_D/CMSIS/Device/SAMD21/Source/system_samd21.c
DeviceVectorsFile=$(TargetsDir)/SAM_D/Source/ATSAMD21J18A_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM D21
```

## SAM DA0 Family

[ATSAMDA0E14A](#)

[ATSAMDA0E15A](#)

[ATSAMDA0E16A](#)

[ATSAMDA0G14A](#)

[ATSAMDA0G15A](#)

[ATSAMDA0G16A](#)

[ATSAMDA0J14A](#)

[ATSAMDA0J15A](#)

[ATSAMDA0J16A](#)

# ATSAMDA0E14A

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include/samda0.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Source/system_samda0.c
Family	SAM D
Sub Family	SAM DA0
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA0_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0E14A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0E14A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA0E14A_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMDA0E14A\_\_

\_\_SAM\_DA0\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x20000FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Include/samda0.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_DA0\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMDA0E14A\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Source/system\_samda0.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMDA0E14A\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM DA0

# ATSAMDA0E15A

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include/samda0.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Source/system_samda0.c
Family	SAM D
Sub Family	SAM DA0
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA0_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0E15A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0E15A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA0E15A_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMDA0E15A\_\_

\_\_SAM\_DA0\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Include/samda0.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_DA0\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMDA0E15A\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Source/system\_samda0.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMDA0E15A\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM DA0

# ATSAMDA0E16A

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include/samda0.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Source/system_samda0.c
Family	SAM D
Sub Family	SAM DA0
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA0_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0E16A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0E16A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA0E16A_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMDA0E16A\_\_

\_\_SAM\_DA0\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Include/samda0.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_DA0\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMDA0E16A\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Source/system\_samda0.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMDA0E16A\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM DA0

# ATSAMDA0G14A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include/samda0.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Source/system_samda0.c
Family	SAM D
Sub Family	SAM DA0
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA0_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0G14A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0G14A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA0G14A_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMDA0G14A__	
__SAM_DA0_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include/samda0.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_DA0_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMDA0G14A_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Source/system_samda0.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMDA0G14A_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM DA0

# ATSAMDA0G15A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include/samda0.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Source/system_samda0.c
Family	SAM D
Sub Family	SAM DA0
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA0_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0G15A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0G15A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA0G15A_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMDA0G15A__	
__SAM_DA0_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include/samda0.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_DA0_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMDA0G15A_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Source/system_samda0.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMDA0G15A_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM DA0



# ATSAMDA0G16A

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include/samda0.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Source/system_samda0.c
Family	SAM D
Sub Family	SAM DA0
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA0_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0G16A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0G16A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA0G16A_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMDA0G16A\_\_

\_\_SAM\_DA0\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Include/samda0.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_DA0\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMDA0G16A\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Source/system\_samda0.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMDA0G16A\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM DA0

# ATSAMDA0J14A

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include/samda0.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Source/system_samda0.c
Family	SAM D
Sub Family	SAM DA0
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA0_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0J14A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0J14A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA0J14A_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMDA0J14A\_\_

\_\_SAM\_DA0\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x20000FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Include/samda0.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_DA0\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMDA0J14A\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Source/system\_samda0.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMDA0J14A\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM DA0

# ATSAMDA0J15A

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include/samda0.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Source/system_samda0.c
Family	SAM D
Sub Family	SAM DA0
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA0_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0J15A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0J15A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA0J15A_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMDA0J15A\_\_

\_\_SAM\_DA0\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Include/samda0.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_DA0\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMDA0J15A\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA0/Source/system\_samda0.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMDA0J15A\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM DA0

# ATSAMDA0J16A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include/samda0.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Source/system_samda0.c
Family	SAM D
Sub Family	SAM DA0
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA0_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0J16A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA0J16A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA0J16A_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMDA0J16A__	
__SAM_DA0_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Include/samda0.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_DA0_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMDA0J16A_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA0/Source/system_samda0.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMDA0J16A_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM DA0

## SAM DA1 Family

[ATSAMDA1E14A](#)

[ATSAMDA1E14B](#)

[ATSAMDA1J15A](#)

[ATSAMDA1J14A](#)

[ATSAMDA1E15B](#)

[ATSAMDA1J15B](#)

[ATSAMDA1J14B](#)

[ATSAMDA1G15A](#)

[ATSAMDA1E15A](#)

[ATSAMDA1J16A](#)

[ATSAMDA1G15B](#)

[ATSAMDA1G14B](#)

[ATSAMDA1G14A](#)

[ATSAMDA1G16A](#)

[ATSAMDA1G16B](#)

[ATSAMDA1J16B](#)

[ATSAMDA1E16B](#)

[ATSAMDA1E16A](#)

# ATSAMDA1E14A

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1E14A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1E14A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA1E14A_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMDA1E14A\_\_

\_\_SAM\_DA1\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x20000FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA1/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA1/Include/samda1.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_DA1\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMDA1E14A\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA1/Source/system\_samda1.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMDA1E14A\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM DA1

# ATSAMDA1E14B

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1E14B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1E14B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA1E14B_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMDA1E14B\_\_

\_\_SAM\_DA1\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x20000FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA1/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA1/Include/samda1.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_DA1\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMDA1E14B\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA1/Source/system\_samda1.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMDA1E14B\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM DA1

# ATSAMDA1J15A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1J15A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1J15A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA1J15A_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMDA1J15A__	
__SAM_DA1_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMDA1J15A_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMDA1J15A_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM DA1



# ATSAMDA1J14A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1J14A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1J14A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA1J14A_Vectors.s

Preprocessor Definitions
ARM_MATH_CM0PLUS
__SAMDA1J14A__
__SAM_DA1_SUBFAMILY
__SAM_D_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x20000FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
DeviceHeaderFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
DeviceLoaderFile=\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM_D/XML/ATSAMDA1J14A_Registers.xml
DeviceSystemFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
DeviceVectorsFile=\$(TargetsDir)/SAM_D/Source/ATSAMDA1J14A_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM DA1

# ATSAMDA1E15B

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1E15B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1E15B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA1E15B_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMDA1E15B__	
__SAM_DA1_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMDA1E15B_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMDA1E15B_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM DA1

# ATSAMDA1J15B

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1J15B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1J15B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA1J15B_Vectors.s

Preprocessor Definitions
ARM_MATH_CM0PLUS
__SAMDA1J15B__
__SAM_DA1_SUBFAMILY
__SAM_D_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
DeviceHeaderFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
DeviceLoaderFile=\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM_D/XML/ATSAMDA1J15B_Registers.xml
DeviceSystemFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
DeviceVectorsFile=\$(TargetsDir)/SAM_D/Source/ATSAMDA1J15B_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM DA1

# ATSAMDA1J14B

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1J14B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1J14B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA1J14B_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMDA1J14B\_\_

\_\_SAM\_DA1\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x20000FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA1/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA1/Include/samda1.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_DA1\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMDA1J14B\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA1/Source/system\_samda1.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMDA1J14B\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM DA1

# ATSAMDA1G15A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1G15A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1G15A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA1G15A_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMDA1G15A__	
__SAM_DA1_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMDA1G15A_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMDA1G15A_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM DA1

# ATSAMDA1E15A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1E15A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1E15A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA1E15A_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMDA1E15A__	
__SAM_DA1_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMDA1E15A_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMDA1E15A_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM DA1

# ATSAMDA1J16A

## Device Details

CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1J16A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1J16A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA1J16A_Vectors.s

## Preprocessor Definitions

ARM\_MATH\_CM0PLUS

\_\_SAMDA1J16A\_\_

\_\_SAM\_DA1\_SUBFAMILY

\_\_SAM\_D\_FAMILY

## Memory Segments

FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

## Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA1/Include

DeviceHeaderFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA1/Include/samda1.h

DeviceLoaderFile=\$(TargetsDir)/SAM\_D/Loader/SAM\_DA1\_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM\_D/XML/ATSAMDA1J16A\_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM\_D/CMSIS/Device/SAMDA1/Source/system\_samda1.c

DeviceVectorsFile=\$(TargetsDir)/SAM\_D/Source/ATSAMDA1J16A\_Vectors.s

DeviceFamily=SAM D

DeviceSubFamily=SAM DA1

# ATSAMDA1G15B

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1G15B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1G15B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA1G15B_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMDA1G15B__	
__SAM_DA1_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00007FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMDA1G15B_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMDA1G15B_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM DA1



# ATSAMDA1G14B

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1G14B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1G14B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA1G14B_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMDA1G14B__	
__SAM_DA1_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x20000FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMDA1G14B_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMDA1G14B_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM DA1

# ATSAMDA1G14A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1G14A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1G14A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA1G14A_Vectors.s

Preprocessor Definitions
ARM_MATH_CM0PLUS
__SAMDA1G14A__
__SAM_DA1_SUBFAMILY
__SAM_D_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x00003FFF
RAM	0x20000000 - 0x20000FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
DeviceHeaderFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
DeviceLoaderFile=\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM_D/XML/ATSAMDA1G14A_Registers.xml
DeviceSystemFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
DeviceVectorsFile=\$(TargetsDir)/SAM_D/Source/ATSAMDA1G14A_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM DA1

# ATSAMDA1G16A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1G16A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1G16A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA1G16A_Vectors.s

Preprocessor Definitions
ARM_MATH_CM0PLUS
__SAMDA1G16A__
__SAM_DA1_SUBFAMILY
__SAM_D_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
DeviceHeaderFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
DeviceLoaderFile=\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM_D/XML/ATSAMDA1G16A_Registers.xml
DeviceSystemFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
DeviceVectorsFile=\$(TargetsDir)/SAM_D/Source/ATSAMDA1G16A_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM DA1

# ATSAMDA1G16B

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1G16B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1G16B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA1G16B_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMDA1G16B__	
__SAM_DA1_SUBFAMILY	
__SAM_D_FAMILY	

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMDA1G16B_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMDA1G16B_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM DA1

# ATSAMDA1J16B

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1J16B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1J16B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA1J16B_Vectors.s

Preprocessor Definitions
ARM_MATH_CM0PLUS
__SAMDA1J16B__
__SAM_DA1_SUBFAMILY
__SAM_D_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
DeviceHeaderFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
DeviceLoaderFile=\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM_D/XML/ATSAMDA1J16B_Registers.xml
DeviceSystemFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
DeviceVectorsFile=\$(TargetsDir)/SAM_D/Source/ATSAMDA1J16B_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM DA1

# ATSAMD1E16B

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD1E16B/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD1E16B/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD1E16B/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMD1E16B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMD1E16B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMD1E16B_Vectors.s

Preprocessor Definitions
ARM_MATH_CM0PLUS
__SAMD1E16B__
__SAM_DA1_SUBFAMILY
__SAM_D_FAMILY

Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF

Project Macros
DeviceIncludePath=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD1E16B/Include
DeviceHeaderFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD1E16B/Include/samda1.h
DeviceLoaderFile=\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM_D/XML/ATSAMD1E16B_Registers.xml
DeviceSystemFile=\$(TargetsDir)/SAM_D/CMSIS/Device/SAMD1E16B/Source/system_samda1.c
DeviceVectorsFile=\$(TargetsDir)/SAM_D/Source/ATSAMD1E16B_Vectors.s
DeviceFamily=SAM D
DeviceSubFamily=SAM DA1

# ATSAMDA1E16A

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
CMSIS Include Path	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
CMSIS System File	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
Family	SAM D
Sub Family	SAM DA1
Loader File	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1E16A_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_D/XML/ATSAMDA1E16A_Registers.xml
Vectors File	\$(TargetsDir)/SAM_D/Source/ATSAMDA1E16A_Vectors.s
Preprocessor Definitions	
ARM_MATH_CM0PLUS	
__SAMDA1E16A__	
__SAM_DA1_SUBFAMILY	
__SAM_D_FAMILY	
Memory Segments	
FLASH	0x00000000 - 0x0000FFFF
RAM	0x20000000 - 0x20001FFF
Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include
DeviceHeaderFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Include/samda1.h
DeviceLoaderFile	\$(TargetsDir)/SAM_D/Loader/SAM_DA1_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_D/XML/ATSAMDA1E16A_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_D/CMSIS/Device/SAMDA1/Source/system_samda1.c
DeviceVectorsFile	\$(TargetsDir)/SAM_D/Source/ATSAMDA1E16A_Vectors.s
DeviceFamily	SAM D
DeviceSubFamily	SAM DA1