



SAM V CPU Support Package Guide

Version: 1.3



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SAM V Support Package

This guide describes the following features of the SAM V CPU support package:

- [How to create SAM V projects](#)
- [How to open SAM V sample projects](#)
- [SAM V specific project properties](#)
- [SAM V specific project templates](#)
- [Supported SAM V devices](#)
- [Troubleshooting](#)

Creating SAM V Projects

Creating an SAM V C/C++ executable project

To create a new minimal C/C++ executable project:

- Select the **File > New > New Project** menu item.
- Select the **A C/C++ executable for Microchip SAM V** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

Creating an SAM V library project

To create a new library project:

- Select the **File > New > New Project** menu item.
- Select the **A library for Microchip SAM V** project template.
- Set the required project name and location directory.
- Click **Next**.
- If required, change any of the default project settings.
- Click **Finish** to create the project.

Creating an SAM V externally built executable project

To create a new project that will allow you to debug an existing externally built executable file:

- Select the **File > New > New Project** menu item.
- Select the **An externally built executable for Microchip SAM V** project template.
- Set the required project name and location directory.
- Click **Next**.
- Set the **Load File** project property to point to the executable file you want to download and debug.
- If required, change any of the other default project settings.
- Click **Finish** to create the project.

Creating an SAM V CrossWorks Tasking Library executable project

To create a new C/C++ executable project configured to use the CrossWorks Tasking Library:

- Select the **File > New > New Project** menu item.
- Select the **A CrossWorks Tasking Library executable for Microchip SAM V** project template.
- Set the required project name and location directory.
- Click **Next**.

If required, change any of the other default project settings.
Click **Finish** to create the project.

Creating an SAM V assembly code only executable project

Please note, this template does not add any C/C++ startup code or libraries and is therefore not suitable for creating projects that include C/C++ code.

To create a new assembly code only executable project without:

Select the **File > New > New Project** menu item.

Select the **An assembly code only executable for Microchip SAM V** project template.

Set the required project name and location directory.

Click **Next**.

If required, change any of the other default project settings.

Click **Finish** to create the project.

Opening SAM V Sample Solutions

SAM V Samples Solution

This solution contains general sample projects that run on SAM V devices. To open the SAM V Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **Microchip SAM V CPU Support Package** link.
- Select the **Samples Solutions > SAM V Samples Solution** link.

SAM V CMSIS-DSP Samples Solution

This solution contains sample projects that use the CMSIS-DSP library running on SAM V devices. To open the SAM V CMSIS-DSP Samples Solution:

- Select the **Tools > Show Installed Packages** menu item.
- Select the **Microchip SAM V CPU Support Package** link.
- Select the **Sample Solutions > SAM V CMSIS-DSP Samples Solution** link.

SAM V Project Properties

Projects creating using the project templates in this support package have the following device specific project properties:

Heap Size

The heap size is set to be 256 bytes when a project is created. The heap size can be modified using the **Heap Size** project property.

Section Placement

You can select the memory configuration you require using the **Section Placement** project property.

For SAM V projects, the set of placements are:

Flash - The application runs in internal Flash memory (*default*).

Flash Vectors In RAM - The application runs in internal Flash memory and exception vectors are copied to RAM memory.

Flash Copy To RAM - The application starts in internal flash and copies itself to run internal RAM memory.

RAM - The application runs internal RAM memory.

Stack Sizes

The main stack size is set to be 256 bytes when a project is created.

The process stack size is set to be 0 bytes when a project is created.

The main and process stack sizes can be modified using the **Main Stack Size** and **Process Stack Size** project properties.

To change the location of the stacks, edit the section placement file and place the `.stack` and `.stack_process` sections as required.

Startup From Reset

By default, the application will only startup from power-on/reset in *Release* configuration. This acts as a safety net in case you accidentally download a program in FLASH during development that crashes and prevents the debugger from taking control of the target over the debug interface thus rendering the device unusable.

For SAM V projects, the **Startup From Reset** project property can be set to one of the following:

No - The application will not startup from reset.

Release Only - The application will only startup from reset when built in *Release* configuration (*default*).

Yes - The application will always startup from reset.

Target Processor

Once a project has been created you can target different devices by modifying the **Target Processor** project property. See the [SAM V Devices](#) section for details on the files, preprocessor definitions and macro definitions used when a device is selected.

Use On-Chip RC Oscillator

The **Build Options > Use On-Chip RC Oscillator** project property can be set to one of the following:

No - The startup code will configure the device to use an external oscillator (*default*).

Yes - The startup code will configure the device to use the on-chip RC oscillator.

TCM Configuration

The **Build Options > TCM Configuration** project property can be set to one of the following:

ITCM and DTCM Disabled - The startup code will configure the device to have no ITCM and DTCM (*default*).

32 Kbytes ITCM and DTCM - The startup code will configure the device to have a 32 Kbyte ITCM and DTCM.

64 Kbytes ITCM and DTCM - The startup code will configure the device to have a 64 Kbyte ITCM and DTCM.

128 Kbytes ITCM and DTCM - The startup code will configure the device to have a 128 Kbyte ITCM and DTCM.

SAM V Project Templates

The project template system simplifies the creation of new projects with the IDE, it also system makes it easy to create new projects with a text editor or script. All that needs to be specified is the project name, the support packages that the project is dependent on, the target processor and the source files you want to add to the project. For example, create a file called *example.hzp* with the following contents:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="SAM_V_EXE">
    <configuration Name="Common" package_dependencies="SAM_V" Target="ATSAMV71J21" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

You can also add any other property settings that the project requires such as preprocessor definitions or include paths using the property save name, for example:

```
<!DOCTYPE CrossStudio_Project_File>
<solution Name="Example Solution">
  <project Name="Example Project" template_name="SAM_V_EXE">
    <configuration Name="Common" package_dependencies="SAM_V" Target="ATSAMV71J21"
      c_preprocessor_definitions="MYDEF1=1;MYDEF2=TWO" c_user_include_directories="$(ProjectDir)/
include1;$(ProjectDir)/include2" />
    <folder Name="Source Files">
      <file file_name="file1.c" />
      <file file_name="file2.c" />
    </folder>
  </project>
</solution>
```

Available SAM V project templates

Template Name	Template Description
SAM V_ASM_EXE	SAM V Assembly Code Only Executable
SAM V_CTL_EXE	SAM V CTL Executable
SAM V_EXE	SAM V C/C++ Executable
SAM V_EXT_EXE	SAM V Externally Built Executable
SAM V_LIB	SAM V Library

SAM V Devices

This package supports the following SAM V devices:

[SAM V70 Family](#)

[SAM V71 Family](#)

SAM V70 Family

[ATSAMV70J19](#)
[ATSAMV70J20](#)
[ATSAMV70N19](#)
[ATSAMV70N20](#)
[ATSAMV70Q19](#)
[ATSAMV70Q20](#)
[ATSAMV70J19B](#)
[ATSAMV70J20B](#)
[ATSAMV70N19B](#)
[ATSAMV70N20B](#)
[ATSAMV70Q19B](#)
[ATSAMV70Q20B](#)

ATSAMV70J19

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv70/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv70/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv70/source/system_SAMV70.c
Family	SAM V
Sub Family	SAM V70
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV70J19_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV70J19_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV70J19_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV70J19__
__SAM_V70_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x0047FFFF
RAM	0x20400000 - 0x2043FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv70/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv70/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV70J19_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv70/source/system_SAMV70.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV70J19_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V70
```

ATSAMV70J20

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv70/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv70/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv70/source/system_SAMV70.c
Family	SAM V
Sub Family	SAM V70
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV70J20_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV70J20_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV70J20_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV70J20__
__SAM_V70_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x004FFFFFF
RAM	0x20400000 - 0x2045FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv70/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv70/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV70J20_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv70/source/system_SAMV70.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV70J20_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V70
```

ATSAMV70N19

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv70/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv70/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv70/source/system_SAMV70.c
Family	SAM V
Sub Family	SAM V70
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV70N19_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV70N19_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV70N19_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV70N19__
__SAM_V70_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x0047FFFF
RAM	0x20400000 - 0x2043FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv70/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv70/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV70N19_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv70/source/system_SAMV70.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV70N19_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V70
```


ATSAMV70N20

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv70/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv70/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv70/source/system_SAMV70.c
Family	SAM V
Sub Family	SAM V70
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV70N20_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV70N20_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV70N20_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV70N20__
__SAM_V70_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x004FFFFFF
RAM	0x20400000 - 0x2045FFFFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv70/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv70/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV70N20_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv70/source/system_SAMV70.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV70N20_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V70
```

ATSAMV70Q19

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv70/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv70/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv70/source/system_SAMV70.c
Family	SAM V
Sub Family	SAM V70
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV70Q19_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV70Q19_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV70Q19_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV70Q19__
__SAM_V70_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x0047FFFF
RAM	0x20400000 - 0x2043FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv70/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv70/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV70Q19_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv70/source/system_SAMV70.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV70Q19_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V70
```

ATSAMV70Q20

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv70/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv70/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv70/source/system_SAMV70.c
Family	SAM V
Sub Family	SAM V70
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV70Q20_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV70Q20_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV70Q20_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV70Q20__
__SAM_V70_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x004FFFFFF
RAM	0x20400000 - 0x2045FFFFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv70/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv70/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV70Q20_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv70/source/system_SAMV70.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV70Q20_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V70
```

ATSAMV70J19B

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv70b/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv70b/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv70b/source/system_SAMV70.c
Family	SAM V
Sub Family	SAM V70
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV70J19B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV70J19B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV70J19B_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM7	
__SAMV70J19B__	
__SAM_V70_SUBFAMILY	
__SAM_V_FAMILY	

Memory Segments	
FLASH	0x00400000 - 0x0047FFFF
RAM	0x20400000 - 0x2043FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_V/CMSIS/samv70b/include
DeviceHeaderFile	\$(TargetsDir)/SAM_V/CMSIS/samv70b/include/sam.h
DeviceLoaderFile	\$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_V/XML/ATSAMV70J19B_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_V/CMSIS/samv70b/source/system_SAMV70.c
DeviceVectorsFile	\$(TargetsDir)/SAM_V/Source/ATSAMV70J19B_Vectors.s
DeviceFamily	SAM V
DeviceSubFamily	SAM V70

ATSAMV70J20B

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv70b/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv70b/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv70b/source/system_SAMV70.c
Family	SAM V
Sub Family	SAM V70
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV70J20B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV70J20B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV70J20B_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV70J20B__
__SAM_V70_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x004FFFFFF
RAM	0x20400000 - 0x2045FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv70b/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv70b/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV70J20B_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv70b/source/system_SAMV70.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV70J20B_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V70
```

ATSAMV70N19B

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv70b/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv70b/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv70b/source/system_SAMV70.c
Family	SAM V
Sub Family	SAM V70
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV70N19B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV70N19B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV70N19B_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV70N19B__
__SAM_V70_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x0047FFFF
RAM	0x20400000 - 0x2043FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv70b/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv70b/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV70N19B_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv70b/source/system_SAMV70.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV70N19B_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V70
```

ATSAMV70N20B

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv70b/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv70b/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv70b/source/system_SAMV70.c
Family	SAM V
Sub Family	SAM V70
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV70N20B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV70N20B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV70N20B_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV70N20B__
__SAM_V70_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x004FFFFFF
RAM	0x20400000 - 0x2045FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv70b/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv70b/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV70N20B_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv70b/source/system_SAMV70.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV70N20B_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V70
```

ATSAMV70Q19B

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv70b/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv70b/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv70b/source/system_SAMV70.c
Family	SAM V
Sub Family	SAM V70
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV70Q19B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV70Q19B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV70Q19B_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV70Q19B__
__SAM_V70_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x0047FFFF
RAM	0x20400000 - 0x2043FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv70b/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv70b/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV70Q19B_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv70b/source/system_SAMV70.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV70Q19B_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V70
```


ATSAMV70Q20B

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv70b/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv70b/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv70b/source/system_SAMV70.c
Family	SAM V
Sub Family	SAM V70
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV70Q20B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV70Q20B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV70Q20B_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM7	
__SAMV70Q20B__	
__SAM_V70_SUBFAMILY	
__SAM_V_FAMILY	

Memory Segments	
FLASH	0x00400000 - 0x004FFFFFF
RAM	0x20400000 - 0x2045FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_V/CMSIS/samv70b/include
DeviceHeaderFile	\$(TargetsDir)/SAM_V/CMSIS/samv70b/include/sam.h
DeviceLoaderFile	\$(TargetsDir)/SAM_V/Loader/SAM_V70_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_V/XML/ATSAMV70Q20B_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_V/CMSIS/samv70b/source/system_SAMV70.c
DeviceVectorsFile	\$(TargetsDir)/SAM_V/Source/ATSAMV70Q20B_Vectors.s
DeviceFamily	SAM V
DeviceSubFamily	SAM V70

SAM V71 Family

[ATSAMV71J19](#)
[ATSAMV71J20](#)
[ATSAMV71J21](#)
[ATSAMV71N19](#)
[ATSAMV71N20](#)
[ATSAMV71N21](#)
[ATSAMV71Q19](#)
[ATSAMV71Q20](#)
[ATSAMV71Q21](#)
[ATSAMV71J19B](#)
[ATSAMV71J20B](#)
[ATSAMV71J21B](#)
[ATSAMV71N19B](#)
[ATSAMV71N20B](#)
[ATSAMV71N21B](#)
[ATSAMV71Q19B](#)
[ATSAMV71Q20B](#)
[ATSAMV71Q21B](#)

ATSAMV71J19

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71J19_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71J19_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71J19_Vectors.s

Preprocessor Definitions

ARM_MATH_CM7

__SAMV71J19__

__SAM_V71_SUBFAMILY

__SAM_V_FAMILY

Memory Segments

FLASH	0x00400000 - 0x0047FFFF
RAM	0x20400000 - 0x2043FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM_V/CMSIS/samv71/include

DeviceHeaderFile=\$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h

DeviceLoaderFile=\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM_V/XML/ATSAMV71J19_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c

DeviceVectorsFile=\$(TargetsDir)/SAM_V/Source/ATSAMV71J19_Vectors.s

DeviceFamily=SAM V

DeviceSubFamily=SAM V71

ATSAMV71J20

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71J20_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71J20_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71J20_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM7	
__SAMV71J20__	
__SAM_V71_SUBFAMILY	
__SAM_V_FAMILY	

Memory Segments	
FLASH	0x00400000 - 0x004FFFFFF
RAM	0x20400000 - 0x2045FFFFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_V/CMSIS/samv71/include
DeviceHeaderFile	\$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h
DeviceLoaderFile	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_V/XML/ATSAMV71J20_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c
DeviceVectorsFile	\$(TargetsDir)/SAM_V/Source/ATSAMV71J20_Vectors.s
DeviceFamily	SAM V
DeviceSubFamily	SAM V71

ATSAMV71J21

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71J21_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71J21_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71J21_Vectors.s

Preprocessor Definitions

ARM_MATH_CM7

__SAMV71J21__

__SAM_V71_SUBFAMILY

__SAM_V_FAMILY

Memory Segments

FLASH	0x00400000 - 0x005FFFFFF
RAM	0x20400000 - 0x2045FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM_V/CMSIS/samv71/include

DeviceHeaderFile=\$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h

DeviceLoaderFile=\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM_V/XML/ATSAMV71J21_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c

DeviceVectorsFile=\$(TargetsDir)/SAM_V/Source/ATSAMV71J21_Vectors.s

DeviceFamily=SAM V

DeviceSubFamily=SAM V71

ATSAMV71N19

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71N19_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71N19_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71N19_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM7	
__SAMV71N19__	
__SAM_V71_SUBFAMILY	
__SAM_V_FAMILY	

Memory Segments	
FLASH	0x00400000 - 0x0047FFFF
RAM	0x20400000 - 0x2043FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_V/CMSIS/samv71/include
DeviceHeaderFile	\$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h
DeviceLoaderFile	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_V/XML/ATSAMV71N19_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c
DeviceVectorsFile	\$(TargetsDir)/SAM_V/Source/ATSAMV71N19_Vectors.s
DeviceFamily	SAM V
DeviceSubFamily	SAM V71

ATSAMV71N20

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71N20_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71N20_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71N20_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV71N20__
__SAM_V71_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x004FFFFFF
RAM	0x20400000 - 0x2045FFFFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv71/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV71N20_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV71N20_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V71
```

ATSAMV71N21

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71N21_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71N21_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71N21_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV71N21__
__SAM_V71_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x005FFFFFF
RAM	0x20400000 - 0x2045FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv71/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV71N21_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV71N21_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V71
```


ATSAMV71Q19

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71Q19_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71Q19_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71Q19_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV71Q19__
__SAM_V71_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x0047FFFF
RAM	0x20400000 - 0x2043FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv71/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV71Q19_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV71Q19_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V71
```

ATSAMV71Q20

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71Q20_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71Q20_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71Q20_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV71Q20__
__SAM_V71_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x004FFFFFF
RAM	0x20400000 - 0x2045FFFFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv71/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV71Q20_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV71Q20_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V71
```

ATSAMV71Q21

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71Q21_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71Q21_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71Q21_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV71Q21__
__SAM_V71_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x005FFFFFF
RAM	0x20400000 - 0x2045FFFFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv71/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv71/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV71Q21_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv71/source/system_SAMV71.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV71Q21_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V71
```

ATSAMV71J19B

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71J19B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71J19B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71J19B_Vectors.s

Preprocessor Definitions

ARM_MATH_CM7

__SAMV71J19B__

__SAM_V71_SUBFAMILY

__SAM_V_FAMILY

Memory Segments

FLASH	0x00400000 - 0x0047FFFF
RAM	0x20400000 - 0x2043FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM_V/CMSIS/samv71b/include

DeviceHeaderFile=\$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h

DeviceLoaderFile=\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM_V/XML/ATSAMV71J19B_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c

DeviceVectorsFile=\$(TargetsDir)/SAM_V/Source/ATSAMV71J19B_Vectors.s

DeviceFamily=SAM V

DeviceSubFamily=SAM V71

ATSAMV71J20B

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71J20B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71J20B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71J20B_Vectors.s

Preprocessor Definitions

ARM_MATH_CM7

__SAMV71J20B__

__SAM_V71_SUBFAMILY

__SAM_V_FAMILY

Memory Segments

FLASH	0x00400000 - 0x004FFFFFF
RAM	0x20400000 - 0x2045FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM_V/CMSIS/samv71b/include

DeviceHeaderFile=\$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h

DeviceLoaderFile=\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM_V/XML/ATSAMV71J20B_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c

DeviceVectorsFile=\$(TargetsDir)/SAM_V/Source/ATSAMV71J20B_Vectors.s

DeviceFamily=SAM V

DeviceSubFamily=SAM V71

ATSAMV71J21B

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71J21B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71J21B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71J21B_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV71J21B__
__SAM_V71_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x005FFFFFF
RAM	0x20400000 - 0x2045FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv71b/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV71J21B_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV71J21B_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V71
```

ATSAMV71N19B

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71N19B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71N19B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71N19B_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV71N19B__
__SAM_V71_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x0047FFFF
RAM	0x20400000 - 0x2043FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv71b/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV71N19B_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV71N19B_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V71
```

ATSAMV71N20B

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71N20B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71N20B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71N20B_Vectors.s

Preprocessor Definitions

ARM_MATH_CM7

__SAMV71N20B__

__SAM_V71_SUBFAMILY

__SAM_V_FAMILY

Memory Segments

FLASH	0x00400000 - 0x004FFFFFF
RAM	0x20400000 - 0x2045FFFF

Project Macros

DeviceIncludePath=\$(TargetsDir)/SAM_V/CMSIS/samv71b/include

DeviceHeaderFile=\$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h

DeviceLoaderFile=\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf

DeviceRegisterDefinitionFile=\$(TargetsDir)/SAM_V/XML/ATSAMV71N20B_Registers.xml

DeviceSystemFile=\$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c

DeviceVectorsFile=\$(TargetsDir)/SAM_V/Source/ATSAMV71N20B_Vectors.s

DeviceFamily=SAM V

DeviceSubFamily=SAM V71

ATSAMV71N21B

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71N21B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71N21B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71N21B_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM7	
__SAMV71N21B__	
__SAM_V71_SUBFAMILY	
__SAM_V_FAMILY	

Memory Segments	
FLASH	0x00400000 - 0x005FFFFFF
RAM	0x20400000 - 0x2045FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include
DeviceHeaderFile	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h
DeviceLoaderFile	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_V/XML/ATSAMV71N21B_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c
DeviceVectorsFile	\$(TargetsDir)/SAM_V/Source/ATSAMV71N21B_Vectors.s
DeviceFamily	SAM V
DeviceSubFamily	SAM V71

ATSAMV71Q19B

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71Q19B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71Q19B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71Q19B_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV71Q19B__
__SAM_V71_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x0047FFFF
RAM	0x20400000 - 0x2043FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv71b/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV71Q19B_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV71Q19B_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V71
```

ATSAMV71Q20B

Device Details

CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71Q20B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71Q20B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71Q20B_Vectors.s

Preprocessor Definitions

```
ARM_MATH_CM7
__SAMV71Q20B__
__SAM_V71_SUBFAMILY
__SAM_V_FAMILY
```

Memory Segments

FLASH	0x00400000 - 0x004FFFFFF
RAM	0x20400000 - 0x2045FFFF

Project Macros

```
DeviceIncludePath=$(TargetsDir)/SAM_V/CMSIS/samv71b/include
DeviceHeaderFile=$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h
DeviceLoaderFile=$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
DeviceRegisterDefinitionFile=$(TargetsDir)/SAM_V/XML/ATSAMV71Q20B_Registers.xml
DeviceSystemFile=$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c
DeviceVectorsFile=$(TargetsDir)/SAM_V/Source/ATSAMV71Q20B_Vectors.s
DeviceFamily=SAM V
DeviceSubFamily=SAM V71
```

ATSAMV71Q21B

Device Details	
CMSIS Header File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h
CMSIS Include Path	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include
CMSIS System File	\$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c
Family	SAM V
Sub Family	SAM V71
Loader File	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
Memory Map File	\$(TargetsDir)/SAM_V/XML/ATSAMV71Q21B_MemoryMap.xml
Register Definition File	\$(TargetsDir)/SAM_V/XML/ATSAMV71Q21B_Registers.xml
Vectors File	\$(TargetsDir)/SAM_V/Source/ATSAMV71Q21B_Vectors.s

Preprocessor Definitions	
ARM_MATH_CM7	
__SAMV71Q21B__	
__SAM_V71_SUBFAMILY	
__SAM_V_FAMILY	

Memory Segments	
FLASH	0x00400000 - 0x005FFFFFF
RAM	0x20400000 - 0x2045FFFF

Project Macros	
DeviceIncludePath	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include
DeviceHeaderFile	\$(TargetsDir)/SAM_V/CMSIS/samv71b/include/sam.h
DeviceLoaderFile	\$(TargetsDir)/SAM_V/Loader/SAM_V71_Loader.elf
DeviceRegisterDefinitionFile	\$(TargetsDir)/SAM_V/XML/ATSAMV71Q21B_Registers.xml
DeviceSystemFile	\$(TargetsDir)/SAM_V/CMSIS/samv71b/source/system_SAMV71.c
DeviceVectorsFile	\$(TargetsDir)/SAM_V/Source/ATSAMV71Q21B_Vectors.s
DeviceFamily	SAM V
DeviceSubFamily	SAM V71

Troubleshooting

On-Chip RC Oscillator

If your program doesn't start up and you are using a board without an external oscillator, set the **Build Options > Use On-Chip RC Oscillator** project property to **Yes**.

TCM

Code and data can be placed in TCM memory either by setting the **Section Placement** project property to one of the TCM configurations or by placing code/data in the TCM program sections (.bss_tcm, .data_tcm, .rodata_tcm and .text_tcm).

For this to work, the device must also be configured to enable TCM memory by setting the **Build Options > TCM Configuration** project property. Failure to do this will result in ITCM/DTCM memory segment overflow errors from the linker.